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# On the Invariance of the Unitary Cost Model for Head Reduction\*

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## Abstract

The  $\lambda$ -calculus is a widely accepted computational model of higher-order functional programs, yet there is not any direct and universally accepted cost model for it. As a consequence, the computational difficulty of reducing  $\lambda$ -terms to their normal form is typically studied by reasoning on concrete implementation algorithms. In this paper, we show that when head reduction is the underlying dynamics, the unitary cost model is indeed invariant. This improves on known results, which only deal with weak (call-by-value or call-by-name) reduction. Invariance is proved by way of a *linear* calculus of explicit substitutions, which allows to nicely decompose any head reduction step in the  $\lambda$ -calculus into more elementary substitution steps, thus making the combinatorics of head-reduction easier to reason about. The technique is also a promising tool to attack what we see as the main open problem, namely understanding for which *normalizing* strategies the unitary cost model is invariant, if any.

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## 1 Introduction

Giving an estimate of the amount of time  $T$  needed to execute a program is a natural refinement of the termination problem, which only requires to decide whether  $T$  is either finite or infinite. The shift from termination to complexity analysis brings more informative outcomes at the price of an increased difficulty. In particular, complexity analysis depends much on the chosen computational model. Is it possible to express such estimates in a way which is independent from the specific machine the program is run on? An answer to this question can be given following computational complexity, which classifies algorithms (and functions) based on the amount of time (or space) they consume when executed by *any* abstract device endowed with a *reasonable* cost model, depending on the size of input. When can a cost model be considered reasonable? The answer lies in the so-called invariance thesis [23]: any time cost model is reasonable if it is polynomially related to the (standard) one of Turing machines.

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If programs are expressed as rewrite systems (e.g. as first-order TRSs), an abstract but effective way to execute programs, rewriting itself, is always available. As a consequence, a natural time cost model turns out to be *derivational complexity*, namely the (maximum) number of rewrite steps which can possibly be performed from the given term. A rewriting step, however, may not be an atomic operation, so derivational complexity is not by definition invariant. For first-order TRSs, however, derivational complexity has been recently shown to be an invariant cost model, by way of term graph rewriting [12, 8, 9].

The case of  $\lambda$ -calculus is definitely more delicate: if  $\beta$ -reduction is weak, i.e., if it cannot take place in the scope of  $\lambda$ -abstractions, one can see  $\lambda$ -calculus as a TRS and get invariance by way of the already cited results [11], or by other means [21]. But if one needs to reduce “under lambdas” because the final term needs to be in normal form (e.g., when performing type checking in dependent type theories), no invariance results are known at the time of writing.

In this paper we give a partial solution to this problem, by showing that the unitary cost model is indeed invariant for the  $\lambda$ -calculus endowed with *head reduction*, in which reduction *can* take place in the scope of  $\lambda$ -abstractions, but *can only* be performed in head position. Our proof technique consists in implementing head reduction in a calculus of explicit substitutions.

Explicit substitutions were introduced to close the gap between the theory of  $\lambda$ -calculus and implementations [1]. Their rewriting theory has also been studied in depth, after Melliès showed the possibility of pathological behaviors [15]. Starting from graphical syntaxes, a new *at a distance* approach to explicit substitutions has recently been proposed [6]. The new formalisms are simpler than those of the earlier generation, and another thread of applications — to which this paper belongs — also started: new results on  $\lambda$ -calculus have been proved by means of explicit substitutions [6, 7].

In this paper we use the *linear-substitution calculus*  $\Lambda_{[\cdot]}$ , a slight variation over a calculus of explicit substitutions introduced by Robin Milner [17]. The variation is inspired by the structural  $\lambda$ -calculus [6]. We study in detail the relation between  $\lambda$ -calculus head reduction and *linear head reduction* [14], the head reduction of  $\Lambda_{[\cdot]}$ , and prove that the latter is at most quadratically longer than the former. This is proved without any termination assumption, by a detailed rewriting analysis.

To get the Invariance Theorem, however, other ingredients are required:

1. *The Subterm Property.* Linear head reduction has a property not enjoyed by head  $\beta$ -reduction: linear substitutions along a reduction  $t \rightarrow^* u$  duplicate subterms of  $t$  only. It easily follows that  $\rightarrow$ -steps can be simulated by Turing machines in time polynomial in the size of  $t$  and the length of  $\rightarrow^*$ . This is explained in Section 3.
2. *Compact representations.* Explicit substitutions, decomposing  $\beta$ -reduction into more atomic steps, allow to take advantage of sharing and thus provide compact representations of terms, avoiding the exponential blowups of term size happening in plain  $\lambda$ -calculus. Is it reasonable to use these compact representations of  $\lambda$ -terms? We answer affirmatively, by exhibiting a dynamic programming algorithm for checking equality of terms with explicit substitutions modulo unfolding, and proving it to work in polynomial time in the size of the involved compact representations. This is the topic of Section 5.
3. *Head simulation of Turing machines.* We also provide the simulation of Turing machines by  $\lambda$ -terms. We give a new encoding of Turing machines, since the known ones do not work with *head*  $\beta$ -reduction, and prove it induces a polynomial overhead. Some details of the encoding are given in Section 6.

We emphasize the result for head  $\beta$ -reduction, but our technical detour also proves invariance

for linear head reduction. To our knowledge, we are the first ones to use the fine granularity of explicit substitutions for complexity analysis. Many calculi with bounded complexity (e.g. [22]) use `let`-constructs, an avatar of explicit substitutions, but they do not take advantage of the refined dynamics, as they always use big-steps substitution rules.

To conclude, we strongly believe that the main contribution of this paper lies in the technique rather than in the invariance result. Indeed, the main open problem in this area, namely the invariance of the unitary cost model for any *normalizing* strategy remains open but, as we argue in Section 4, seems now within reach.

An extended version of this paper with more detailed proofs is available [4].

## 2 $\lambda$ -Calculus and Cost Models: an Informal Account

Consider the pure, untyped,  $\lambda$ -calculus. Terms can be variables, abstractions or applications and computation is modeled by  $\beta$ -reduction. Once a reduction strategy is fixed, one could be tempted to make *time* and *reduction steps* to correspond: firing a  $\beta$ -redex requires one time instant (or, equivalently, a finite number of time instants) and thus the number of reduction steps to normal form could be seen as a measure of its time complexity. This would be very convenient, since reasoning on the complexity of normalization could be done this way directly on  $\lambda$ -terms. However, doing so one could in principle risk to be too optimistic about the complexity of obtaining the normal form of a term  $t$ , given  $t$  as an input. This section will articulate on this issue by giving some examples and pointers to the relevant literature.

Consider the sequence of  $\lambda$ -terms defined as follows, by induction on a natural number  $n$  (where  $u$  is the lambda term  $yxx$ ):  $t_0 = u$  and for every  $n \in \mathbb{N}$ ,  $t_{n+1} = (\lambda x.t_n)u$ .  $t_n$  has size linear in  $n$ , and  $t_n$  rewrites to its normal form  $r_n$  in exactly  $n$  steps, following a leftmost-outermost strategy:

$$\begin{aligned} t_0 &\equiv u \equiv r_0; \\ t_1 &\rightarrow yuu \equiv yr_0r_0 \equiv r_1; \\ t_2 &\rightarrow (\lambda x.t_0)(yuu) \equiv (\lambda x.u)r_1 \rightarrow yr_1r_1 \equiv r_2; \\ &\vdots \end{aligned}$$

For every  $n$ , however,  $r_{n+1}$  contains two copies of  $r_n$ , hence the size of  $r_n$  is *exponential* in  $n$ . As a consequence, if we stick to the leftmost-outermost strategy and if we insist on normal forms to be represented explicitly, without taking advantage of sharing, the unitary cost model *is not* invariant: in a linear number of  $\beta$ -steps we reach an object which cannot even be written down in polynomial time.

One may wonder whether this problem is due to the specific, inefficient, adopted strategy. However, it is quite easy to rebuild a sequence of terms exhibiting the same behavior along an innermost strategy: if  $s = \lambda y.yxx$ , define  $v_0$  to be just  $\lambda x.s$ , for every  $n \in \mathbb{N}$ ,  $v_{n+1}$  to be  $\lambda x.v_n s$ , and consider  $v_n(\lambda x.x)$ . Actually, there *are* invariant cost-models for the  $\lambda$ -calculus even if one wants to obtain the normal form in an explicit, linear format, like the difference cost model [10]. But they pay a price for that: they do not attribute a constant weight to each reduction step. Then, another natural question arises: is it that the gap between the unitary cost model and the real complexity of reducing terms is only due to a *representation* problem? In other words, could we take advantage of a shared representation of terms, even if only to encode  $\lambda$ -terms (and normal forms in particular) in a compact way?

The literature offers some positive answers to the question above. In particular, the unitary cost model can be proved to be invariant for both call-by-name and call-by-value

$\lambda$ -calculi, as defined by Plotkin [19]. In one way or another, the mentioned results are based on sharing subterms, either by translating the  $\lambda$ -calculus to a TRS [11] or by going through abstract machines [21]. Plotkin's calculi, however, are endowed with *weak* notions of reduction, which prevent computation to happen in the scope of a  $\lambda$ -abstraction. And the proposed approaches crucially rely on that.

The question now becomes the following: is it possible to prove the invariance of the unitary cost model for some *strong* notion of reduction? This paper gives a first, positive answer to this question by proving the number of  $\beta$ -reduction steps to be an invariant cost model for *head* reduction, in which one *is* allowed to reduce in the scope of  $\lambda$ -abstractions, but evaluation stops on *head* normal forms.

We are convinced that the exponential blowup in the examples above is, indeed, only due to the  $\lambda$ -calculus being a very inefficient *representation* formalism. Following this thesis we use terms with explicit substitutions as compact representations: our approach, in contrast to other ones, consists in using sharing (here under the form of explicit substitutions) only to obtain compactness, and not to design some sort of optimal strategy reducing shared redexes. Actually, we follow the opposite direction: the leftmost-outermost strategy — being standard — can be considered as the maximally *non-sharing* strategy. How much are we losing limiting ourselves to head reduction? Not so much: in Section 6 we show an encoding of Turing machines for which the normal form is reached by head-reduction only. Moreover, from a denotational semantics point of view head-normal forms — and not full normal forms — are the right notion of result for  $\beta$ -reduction.

The next two sections introduce explicit substitutions and prove that the length of their head strategy is polynomially related to the length of head  $\beta$ -reduction. In other words, the switch to compact representations does not affect the cost model in any essential way.

### 3 Linear Explicit Substitutions

First of all, we introduce the  $\lambda$ -calculus. Its terms are given by the grammar:

$$t, u, r ::= x \mid t t \mid \lambda x.t$$

and its reduction rule  $\rightarrow_\beta$  is defined as the context closure of  $(\lambda x.t) u \mapsto_\beta t\{x/u\}$ .  $\mathcal{T}_\lambda$  is the set of all terms of the  $\lambda$ -calculus. We will mainly work with head reduction, instead of full  $\beta$ -reduction. We define head reduction as follows. Let an *head context*  $\hat{H}$  be defined by:

$$\hat{H} ::= [\cdot] \mid \hat{H} t \mid \lambda x.\hat{H}.$$

Then define *head reduction*  $\rightarrow_h$  as the closure by head contexts of  $\mapsto_\beta$ . Our definition of head reduction is slightly more liberal than the usual one. Indeed, it is non-deterministic, for instance:

$$(\lambda x.I) t \leftarrow (\lambda x.(I I)) t \rightarrow_h I I.$$

Usually only one of the two redexes would be considered an head redex. However, this non-determinism is harmless, since one easily proves that  $\rightarrow_h$  has the diamond property. Reducing  $\rightarrow_h$  in an outermost way we recover the usual notion of head reduction, so our approach gains in generality without loosing any property of head reduction. Our notion is motivated by the corresponding notion of head reduction for explicit substitutions, which is easier to manage in this more general approach.

The calculus of explicit substitutions we are going to use is a minor variation over a simple calculus introduced by Milner [17]. The grammar is standard:

$$t, u, r ::= x \mid t t \mid \lambda x.t \mid t[x/t].$$

$$\begin{array}{lcl}
(\lambda x.t)L u & \mapsto_{\text{dB}} & t[x/u]L \\
C[x][x/u] & \mapsto_{\text{1s}} & C[u][x/u] \\
t[x/u] & \mapsto_{\text{gc}} & t \quad \text{if } x \notin \text{fv}(t)
\end{array}$$

■ **Figure 1**  $\Lambda_{[\cdot]}$  Rewriting Rules.

The term  $t[x/u]$  is an *explicit substitution*.  $\mathcal{T}$  is the set of terms with explicit substitutions. Observe that  $\mathcal{T}_\lambda \subset \mathcal{T}$ . Both constructors  $\lambda x.t$  and  $t[x/u]$  bind  $x$  in  $t$ . We note  $L$  a possibly empty list of explicit substitutions  $[x_1/u_1] \dots [x_k/u_k]$ . Contexts are defined by:

$$C, D, E, F ::= [\cdot] \mid C t \mid t C \mid \lambda x.C \mid C[x/t] \mid t[x/C].$$

We note  $C[t]$  the usual operation of substituting  $[\cdot]$  in  $C$  possibly capturing free variables of  $t$ . We will often use expressions like  $C[x][x/u]$  where it is implicitly assumed that  $C$  does not capture  $x$ . The *linear-substitution calculus*  $\Lambda_{[\cdot]}$  is given by the rewriting rules  $\rightarrow_{\text{dB}}$ ,  $\rightarrow_{\text{1s}}$  and  $\rightarrow_{\text{gc}}$ , defined as the context closures of the rules  $\mapsto_{\text{dB}}$ ,  $\mapsto_{\text{1s}}$  and  $\mapsto_{\text{gc}}$  in Figure 1. We also use the notation  $\rightarrow_{\Lambda_{[\cdot]}} = \rightarrow_{\text{dB}} \cup \rightarrow_{\text{1s}} \cup \rightarrow_{\text{gc}}$  and  $\rightarrow_{\text{s}} = \rightarrow_{\text{1s}} \cup \rightarrow_{\text{gc}}$ . Rule  $\rightarrow_{\text{dB}}$  acts at a *distance*: the function  $\lambda x.t$  and the argument  $u$  can interact even if there is  $L$  between them. This is motivated by the close relation between  $\Lambda_{[\cdot]}$  and graphical formalisms as proof-nets and  $\lambda$ j-dags [3, 5], and is also the difference with Milner's presentation of  $\Lambda_{[\cdot]}$  [17].

The linear-substitution calculus enjoys all properties required to explicit substitutions calculi, obtained by easy adaptations of the proofs for  $\lambda$ j in [6]. Moreover, it is confluent and preserves  $\beta$ -strong normalization. In particular,  $\rightarrow_{\text{s}}$  is a strongly normalizing and confluent relation.

Given a term  $t$  with explicit substitutions, its normal form with respect to  $\rightarrow_{\text{s}}$  is a  $\lambda$ -term, noted  $t\downarrow$ , called the *unfolding* of  $t$  and verifying the following equalities:

$$(t u)\downarrow = t\downarrow u\downarrow; \quad (\lambda x.t)\downarrow = \lambda x.t\downarrow; \quad (t[x/u])\downarrow = t\downarrow\{x/u\downarrow\}.$$

Another useful property is the so-called *full-composition*, which states that any explicit substitution can be reduced to its implicit form independently from the other substitutions in the term, formally  $t[x/u] \rightarrow_{\text{s}}^* t\{x/u\}$ . Last,  $\Lambda_{[\cdot]}$  simulates  $\lambda$ -calculus ( $t \rightarrow_{\beta} u$  implies  $t \rightarrow_{\Lambda_{[\cdot]}}^* u$ ) and reductions in  $\Lambda_{[\cdot]}$  can be projected on  $\lambda$ -calculus via unfolding ( $t \rightarrow_{\Lambda_{[\cdot]}} u$  implies  $t\downarrow \rightarrow_{\beta}^* u\downarrow$ ).

The calculus  $\Lambda_{[\cdot]}$  has a strong relation with proof-nets and linear logic: it can be mapped to Danos' and Regnier's pure proof-nets [20] or to  $\lambda$ j-dags [5]. The rule  $\rightarrow_{\text{dB}}$  corresponds to proof-nets multiplicative cut-elimination,  $\rightarrow_{\text{1s}}$  to the cut-elimination rule between  $!$  (every substitution is in a  $!$ -box) and contraction,  $\rightarrow_{\text{gc}}$  to the cut-elimination rule between  $!$  and weakening. The case of a cut between  $!$  and dereliction is handled by  $\rightarrow_{\text{1s}}$ , as if cut derelictions were always contracted with a weakening.

### 3.1 Linear Head Reduction, the Subterm Property and Shallow Terms

In this paper, we mainly deal with a specific notion of reduction for  $\Lambda_{[\cdot]}$ , called *linear head reduction* [14, 13], and related to the representation of  $\lambda$ -calculus into linear logic proof-nets. In order to define it we need the notion of head context for explicit substitutions.

► **Definition 3.1** (head context). *Head contexts* are defined by the following grammar:

$$H ::= [\cdot] \mid H t \mid \lambda x.H \mid H[x/t].$$

The fundamental property of an head context  $H$  is that the hole cannot be duplicated nor erased. In terms of linear logic proof-nets, the head hole is not contained in any box (since boxes are associated with the arguments of applications and with explicit substitutions). We now need to consider a modified version of  $\mapsto_{1s}$ :

$$H[x][x/u] \multimap_{\hat{1}s} H[u][x/u].$$

Now, let  $\multimap_{dB}$  (resp.  $\multimap_{1s}$ ) be the closure by head contexts of  $\mapsto_{dB}$  (resp.  $\multimap_{1s}$ ). Last, define *head linear reduction*  $\multimap$  as  $\multimap_{dB} \cup \multimap_{1s}$ . Please notice that  $\multimap$  can reduce under  $\lambda$ , for instance  $\lambda y.(H[x][x/u]) \multimap_{1s} \lambda y.(H[u][x/u])$ . Our definition of  $\multimap$  gives a non-deterministic strategy, but its non-determinism is again harmless: a simple case analysis shows that  $\multimap$  has the diamond property.

In [2] it is proved that linear head reduction has the same factorization property enjoyed by head reduction in  $\lambda$ -calculus: if  $t \rightarrow_{\Lambda[\cdot]}^* u$  then  $t \multimap^* \Rightarrow^*$ , where  $\Rightarrow$  is the complement of  $\rightarrow_{\Lambda[\cdot]}$  with respect to  $\multimap$  (i.e.  $\Rightarrow := \rightarrow_{\Lambda[\cdot]} \setminus \multimap$ ).

A term  $u$  is a *box-subterm* of a term  $t$  (resp. of a context  $C$ ) if  $t$  (resp.  $C$ ) has a subterm of the form  $r u$  or of the form  $r[x/u]$  for some  $r$ .

► **Remark.** By definition of head-contexts,  $[\cdot]$  is not a box-subterm of  $H[\cdot]$ , and there is no box-subterm of  $H[\cdot]$  which has  $[\cdot]$  as a subterm.

The following fundamental property of head linear reduction is folklore among specialists, but to our knowledge it has never appeared in print before.

► **Proposition 3.2** (Subterm Property). *If  $t \multimap^* u$  and  $r$  is a box-subterm of  $u$ , then  $r$  is a box-subterm of  $t$ .*

The aforementioned proposition is a key point in our study of cost models. Linear head substitution steps duplicate sub-terms, but the Subterm Property guarantees that only sub-terms of the initial term  $t$  are duplicated, and thus each step can be implemented in time polynomial in the size of  $t$ , which is the size of the input, the fundamental parameter for complexity analysis. This is in sharp contrast with what happens in the  $\lambda$ -calculus, where the cost of a  $\beta$ -reduction step is not even polynomially related to the size of the initial term.

**Proof.** Let  $t \multimap^k u$ . We proceed by induction on  $k$ . Suppose that  $k > 0$ . Then  $t \multimap^* v \multimap u$  and by *i.h.* any box-subterm of  $v$  is a box subterm of  $t$ , so it is enough to show that any box-subterm of  $u$  is a box-subterm of  $v$ . By induction on  $v \multimap u$ . The case  $v = (\lambda x.r)L s \multimap r[x/s]L$  is trivial. If  $v = H[x][x/s] \multimap H[s][x/s]$  by the previous remark the plug of  $s$  in  $H[\cdot]$  does not create any new box-subterm, nor modify a box-subterm of  $H[\cdot]$ . And obviously any box-subterm of  $s$  is a box-subterm of  $v$ . The inductive cases follow from the *i.h.* and the previous remark. ◀

The subterm property does not only allow to bound the cost of implementing any reduction step, but also to bound the size of intermediate terms:

► **Corollary 3.3.** *There is a polynomial  $p : \mathbb{N} \times \mathbb{N} \rightarrow \mathbb{N}$  such that if  $t \multimap^k u$  then  $|u| \leq p(k, |t|)$ .*

Consider a reduction  $t \multimap^* u$  where  $t$  is a  $\lambda$ -term. Another consequence of the Subterm Property is that for every explicit substitution occurring in  $u$ , the substituted term is a  $\lambda$ -term. This is another strong property to be used in the analysis of the next section.

► **Definition 3.4** (Shallow Terms). A  $\Lambda[\cdot]$ -term  $t$  is *shallow* if whenever  $t = C[u[x/r]]$  then  $r$  is a  $\lambda$ -term.

► **Corollary 3.5.** *Let  $t$  be a  $\lambda$ -term and  $t \multimap^* u$ . Then  $u$  is shallow.*

#### 4 On the Relation Between $\Lambda$ and $\Lambda_{[\cdot]}$

In this section, linear explicit substitutions will be shown to be an efficient way to implement head reduction. We will proceed by proving three auxiliary results separately:

1. We show that any  $\rightarrow_{\circ}$ -reduction  $\rho$  projects via unfolding to a  $\rightarrow_{\text{h}}$ -reduction  $\rho\downarrow$  having as length exactly the number of  $\rightarrow_{\text{dB}}$  steps in  $\rho$ ; this is the topic of Section 4.1;
2. We show the converse relation, *i.e.* that any  $\rightarrow_{\text{h}}$ -reduction  $\rho$  can be simulated by a  $\rightarrow_{\circ}$ -reduction having as many  $\rightarrow_{\text{dB}}$ -steps as the steps in  $\rho$ , followed by unfolding; this is in Section 4.2;
3. We show that in any  $\rightarrow_{\circ}$ -reduction  $\rho$  the number of  $\rightarrow_{\text{1s}}$ -steps is  $\mathcal{O}(|\rho|_{\text{dB}}^2)$  where  $|\rho|_{\text{dB}}$  is the number of  $\rightarrow_{\text{dB}}$  steps in  $\rho$ . By the previous two points, there is a quadratic — and thus polynomial — relation between  $\rightarrow_{\text{h}}$ -reductions and  $\rightarrow_{\circ}$ -reduction from a given term; all this is explained in Section 4.3.

##### 4.1 Projection of $\rightarrow_{\circ}$ on $\rightarrow_{\text{h}}$

The first thing one needs to prove about head-linear reduction is whether it is a *sound* way to implement head reduction. This is proved by relatively standard techniques, and requires the following auxiliary lemma, whose proof is by induction on  $t \rightarrow_{\text{h}} u$ :

► **Lemma 4.1.** *Let  $t \in \mathcal{T}_{\lambda}$ . If  $t \rightarrow_{\text{h}} u$  then  $t\{x/r\} \rightarrow_{\text{h}} u\{x/r\}$ .*

► **Lemma 4.2** (Projection of  $\rightarrow_{\circ}$  on  $\rightarrow_{\text{h}}$ ). *Let  $t \in \mathcal{T}$ . If  $\rho : t \rightarrow_{\circ}^k u$  then  $t\downarrow \rightarrow_{\text{h}}^n u\downarrow$  and  $n = |\rho|_{\text{dB}} \leq k$ .*

**Proof.** By induction on  $k$ . If  $k = 0$  it is trivial, so let  $k > 0$  and  $t \rightarrow_{\circ}^{n-1} r \rightarrow_{\circ} u$ . Let  $\tau$  be the reduction  $t \rightarrow_{\circ}^{n-1} r$ . By *i.h.* we get  $t\downarrow \rightarrow_{\text{h}}^m r\downarrow$  and  $m = |\tau|_{\text{dB}} \leq k - 1$ . Now consider  $r \rightarrow_{\circ} u$ . There are two cases. If  $r \rightarrow_{\circ_{\text{1s}}} u$  then  $r\downarrow = u\downarrow$ , by definition of  $(\cdot)\downarrow$ , as the normal form of  $\rightarrow_{\text{s}}$  (which contains  $\rightarrow_{\circ_{\text{1s}}}$ ). If  $r \rightarrow_{\circ_{\text{dB}}} u$  then  $r = H[(\lambda x.v)\text{L } s] \rightarrow_{\circ} H[v[x/s]\text{L}] = u$ . We prove that  $r\downarrow \rightarrow_{\text{h}} u\downarrow$ , from which it follows that  $t\downarrow \rightarrow_{\text{h}}^{m+1} u\downarrow$ , where  $m + 1 = |\tau|_{\text{dB}} + 1 = |\rho|_{\text{dB}} \leq k$ . We go by induction on  $H$ . We use the following notation: if  $t = u\text{L}$  and  $\text{L} = [y_1/w_1] \dots [y_m/w_m]$  then we write  $\sigma_{\text{L}}$  for  $\{y_1/w_1\downarrow\} \dots \{y_m/w_m\downarrow\}$ , thus we can write  $t\downarrow = u\downarrow\sigma_{\text{L}}$ . Cases:

- $H = [\cdot]$ . Then:

$$\begin{aligned} r\downarrow &= ((\lambda x.v)\text{L})\downarrow s\downarrow = (\lambda x.v\downarrow)\sigma_{\text{L}} s\downarrow = (\lambda x.v\downarrow\sigma_{\text{L}}) s\downarrow \\ &\rightarrow_{\text{h}} v\downarrow\sigma_{\text{L}}\{x/s\downarrow\} \stackrel{*}{=} v\downarrow\{x/s\downarrow\}\sigma_{\text{L}} = (v[x/s])\downarrow\sigma_{\text{L}} = (v[x/s]\text{L})\downarrow = u\downarrow. \end{aligned}$$

Step (\*) holds because  $x \notin \text{fv}(w_i)$  for  $i \in \{1, \dots, m\}$ .

- $H = J[y/w]$ . Then by *i.h.* and Lemma 4.1 we get  $r\downarrow = (J[(\lambda x.v)\text{L } s])\downarrow\{y/w\downarrow\} \rightarrow_{\text{h}} (J[v[x/s]\text{L}])\downarrow\{y/w\downarrow\} = u\downarrow$ .
- The cases  $H = J w$  and  $H = \lambda y.J$  follow from the *i.h.*

◀

##### 4.2 Projection of $\rightarrow_{\text{h}}$ on $\rightarrow_{\circ}$

Here we map head  $\beta$ -steps to head linear steps followed by unfolding. In other words, we prove that head-linear reduction is not only a *sound*, but also a *complete* way to implement head reduction. This section is going to be technically more involved than the previous one. First of all, we show that a single head step can be simulated by a step of  $\rightarrow_{\text{dB}}$  followed by unfolding, which is straightforward:

► **Lemma 4.3** (Head Simulation). *Let  $t$  be a  $\lambda$ -term. If  $t \rightarrow_{\text{h}} u$ , then  $t \rightarrow_{\text{dB}} \rightarrow_{\text{s}}^* u$ .*



**Proof.** By induction on  $t \rightarrow_h u$ . The base case: if  $(\lambda x.u) r \rightarrow_h u\{x/r\}$  then  $(\lambda x.u) r \rightarrow_{\text{dB}} u[x/r]$  and  $u[x/r] \rightarrow_s^* u\{x/r\}$  by full composition. The inductive cases follows by the *i.h.*. ◀

We are now going to show that a sequence of  $\rightarrow_{1s}$  reduction steps can be factored into some head-linear substitutions, followed by some linear substitutions under non-head contexts. This allows to refine Lemma 4.3. Define  $\Rightarrow_s$  as the relation  $\rightarrow_s \setminus \rightarrow_{1s}$ , *i.e.*  $\Rightarrow_s$  reduces non-head-linear substitution redexes. Moreover, define the *linear unfolding*  $t \downarrow$  of  $t$  as the normal form of  $t$  with respect to  $\rightarrow_{1s}$  (which exists since  $\rightarrow_{1s} \subseteq \rightarrow_{1s}$  and  $\rightarrow_{1s}$  terminates, and it is unique because  $\rightarrow_{1s}$  is deterministic).

Now, we can prove that any  $\rightarrow_h$  step is simulated in  $\rightarrow_{\text{dB}} \rightarrow_{1s}^* \Rightarrow_s^*$  (actually, in such a sequence there can be at most one  $\rightarrow_{1s}$  step):

► **Lemma 4.4** (Unfolding Factorization). *The following swaps hold:*

1.  $\Rightarrow_s \rightarrow_{1s} \subseteq \rightarrow_{1s}^+ \Rightarrow_s^+$ , *precisely*:  $\Rightarrow_s \rightarrow_{1s} \subseteq \rightarrow_{1s} \Rightarrow_s \cup \rightarrow_{1s} \rightarrow_{1s} \Rightarrow_s \cup \rightarrow_{1s} \Rightarrow_s \Rightarrow_s$ .
2.  $\rightarrow_{1s}^* \subseteq \rightarrow_{1s}^* \Rightarrow_s^*$ , *and in particular*  $t \rightarrow_{1s}^* t \downarrow \Rightarrow_s^* t \downarrow$ .

**Proof.** 1) Formally, the proof is by induction on  $t \Rightarrow_s u$ . Informally,  $\Rightarrow_s$  cannot create, duplicate or alter the head nature of an  $\rightarrow_{1s}$ -step, so that the second step in  $t \Rightarrow_s \rightarrow_{1s} r$  can be traced back to a unique  $\rightarrow_{1s}$  redex. Now, there are two cases: either the two redexes simply permute or the preponement of  $\rightarrow_{1s}$  duplicate the redex reduced by  $\Rightarrow_s$ . The second case splits in two subcases, of which we just give two examples:

$$\begin{array}{ccc} x[x/y][y/z] & \Rightarrow_s & x[x/z][y/z] \\ \downarrow_{1s} & \rightarrow_{1s} & \downarrow_{1s} \\ y[x/y][y/z] & \rightarrow_{1s} & z[x/y][y/z] \Rightarrow_s z[x/z][y/z] \\ x[x/y y][y/z] & \Rightarrow_s & x[x/y z][y/z] \\ (y y) \downarrow_{1s} & \Rightarrow_s & (y z) \downarrow_{1s} \\ (y y)[x/y y][y/z] & \Rightarrow_s & (y z)[x/y y][y/z] \Rightarrow_s (y z)[x/y z][y/z] \end{array}$$

2) There is an abstract lemma (see [4]) which says that if  $\rightarrow_2 \rightarrow_1 \subseteq \rightarrow_1^+ \rightarrow_2^*$  and  $\rightarrow_1$  is strongly normalizing then  $(\rightarrow_1 \cup \rightarrow_2)^* \subseteq \rightarrow_1^* \rightarrow_2^*$ . Now, taking  $\rightarrow_1$  to be  $\rightarrow_{1s}$ ,  $\rightarrow_2$  to be  $\Rightarrow_s$  and since  $\rightarrow_{1s}$  is strongly normalising we get  $\rightarrow_{1s}^* = (\rightarrow_{1s} \cup \Rightarrow_s)^* \subseteq \rightarrow_{1s}^* \Rightarrow_s^*$ . ◀

We know that a  $\rightarrow_h$  step is simulated by a sequence of the form  $\rightarrow_{\text{dB}} \rightarrow_{1s}^* \Rightarrow_s^* \subseteq \rightarrow_{1s}^* \Rightarrow_s^*$ . Consider two (or more)  $\rightarrow_h$ -steps. They are simulated by a sequence of the form  $\rightarrow_{1s}^* \Rightarrow_s^* \rightarrow_{\text{dB}} \rightarrow_{1s}^* \Rightarrow_s^*$ , while we would like to obtain  $\rightarrow_{1s}^* \Rightarrow_s^*$ . What we need to do is to prove that a sequence of the form  $\Rightarrow_s^* \rightarrow_{\text{dB}}$  can always be reorganized as a sequence  $\rightarrow_{\text{dB}} \Rightarrow_s^*$ :

► **Lemma 4.5.** *The following inclusions hold:*

1.  $\Rightarrow_s \rightarrow_{\text{dB}} \subseteq \rightarrow_{\text{dB}} \Rightarrow_s$ .
2.  $\Rightarrow_s^* \rightarrow_{\text{dB}} \subseteq \rightarrow_{\text{dB}} \Rightarrow_s^*$ .

**Proof.** 1) By induction on  $t \Rightarrow_s u$ . The idea is that  $\Rightarrow_s$  cannot create, duplicate nor alter the head nature of  $\rightarrow_{\text{dB}}$  redexes, therefore  $\rightarrow_{\text{dB}}$ -step can be preponed. Conversely,  $\rightarrow_{\text{dB}}$ -steps cannot erase, duplicate nor alter the non-head nature of  $\Rightarrow_s$  redexes, so the two steps commute. 2) Let  $t \Rightarrow_s^k \rightarrow_{\text{dB}} u$ . By induction on  $k$  using point 1 and a standard diagram chasing. ◀

The next lemma is the last brick for the projection.

► **Lemma 4.6.** *If  $t \downarrow \rightarrow_h u$  then there exists  $r$  s.t.  $t \downarrow \rightarrow_{\text{dB}} r \rightarrow_{1s}^* u$ .*

**Proof.** By Lemma 4.3, we get  $t \downarrow \rightarrow_{1s}^* u$ . By Lemma 4.4,  $t \rightarrow_{1s}^* t \downarrow$  factors as  $t \rightarrow_{1s}^* t \downarrow \Rightarrow_s^* t \downarrow$ , and so we get  $t \downarrow \Rightarrow_s^* \rightarrow_{1s}^* u$ . By Lemma 4.5.2, we get  $t \downarrow \rightarrow_{\text{dB}} \Rightarrow_s^* \rightarrow_{1s}^* u$ , *i.e.*  $t \downarrow \rightarrow_{\text{dB}} \Rightarrow_s^* \rightarrow_{1s}^* u$ . ◀

We can then conclude with the result which gives the name to this section:

► **Lemma 4.7** (Projection of  $\rightarrow_{\mathbf{h}}$  on  $\rightarrow_{\circ}$ ). *Let  $t$  be a  $\lambda$ -term. If  $t \rightarrow_{\mathbf{h}}^k u$  then there exists a reduction  $\rho$  s.t.  $\rho : t \rightarrow_{\circ}^* r$ , with  $r \rightarrow_{1\mathbf{s}}^* u$  and  $|\rho|_{\mathbf{dB}} = k$ .*

**Proof.** By induction on  $k$ . The case  $k = 0$  is trivial, so let  $k > 0$  and  $t \rightarrow_{\mathbf{h}}^{k-1} s \rightarrow_{\mathbf{h}} u$ . By *i.h.* there exists a reduction  $\tau$  s.t.  $\tau : t \rightarrow_{\circ}^* v$ ,  $v \rightarrow_{1\mathbf{s}}^* s$  and  $|\tau|_{\mathbf{dB}} = k - 1$ . Since  $s$  is a  $\lambda$ -term we have that  $v \downarrow = s$  and  $v \downarrow \rightarrow_{\mathbf{h}} u$ . By lemma 4.6 there exists  $r$  s.t.  $v \downarrow \rightarrow_{\mathbf{dB}} r \rightarrow_{1\mathbf{s}}^* u$ . Moreover,  $v \rightarrow_{\circ_{1\mathbf{s}}}^* v \downarrow$ ; call  $\gamma$  this reduction. Let  $\rho$  be the reduction obtained by concatenating  $\tau : t \rightarrow_{\circ}^* v$ ,  $\gamma : v \rightarrow_{\circ_{1\mathbf{s}}}^* v \downarrow$  and  $v \downarrow \rightarrow_{\mathbf{dB}} r$ . We have  $|\rho|_{\mathbf{dB}} = |\tau|_{\mathbf{dB}} + 1 = k$  and  $r \rightarrow_{1\mathbf{s}}^* u$ , and so we conclude. ◀

This section and the previous one proved for  $\Lambda_{[\cdot]}$  results proved by more abstract means by Mellès in the context of the  $\lambda\sigma$ -calculus in [16]<sup>1</sup>. The linear substitution calculus is simpler than the  $\lambda\sigma$ -calculus, which is why our analysis — developed independently — is simpler (but also less sophisticated and general) than Mellès'<sup>2</sup>.

### 4.3 Quadratic Relation

The last two sections proved that head reduction can be seen as head linear reduction followed by unfolding, and that the number of head steps is exactly the number of  $\rightarrow_{\mathbf{dB}}$ -steps in the corresponding head linear reduction. To conclude that head and head linear reduction are polynomially related, we need to show that the number of  $\rightarrow_{\circ_{1\mathbf{s}}}$ -steps in a linear head reduction  $\rho$  is polynomially related to the number of  $\rightarrow_{\mathbf{dB}}$ -steps in  $\rho$ .

We do it by giving a precise estimate of the maximal length of a  $\rightarrow_{\circ_{1\mathbf{s}}}$  reduction from a given term. Intuition tells us that any reduction  $t \rightarrow_{\circ_{1\mathbf{s}}}^* u$  cannot be longer than the number of explicit substitutions in  $t$  (number noted  $\mathbf{es}(t)$ ), since any substitution in  $t$  can act at most once on the head variable. However, a formal proof of this fact is not completely immediate, and requires to introduce a measure and prove some (easy) lemmas.

The idea is to statically count the length of the maximum chain of substitutions on the head, and to show that this number decreases at each head linear substitution step. Let us give an example. Consider the reduction:

$$t = (x\ y)[x/y\ r][y/u] \rightarrow_{\circ_{1\mathbf{s}}} ((y\ r)\ y)[x/y\ r][y/u] \rightarrow_{\circ_{1\mathbf{s}}} ((u\ r)\ y)[x/y\ r][y/u].$$

It is easy to establish statically on  $t$  that  $[y/u]$  will give rise to the second  $\rightarrow_{\circ_{1\mathbf{s}}}$ -step, since  $y$  is the head variable of  $y\ r$ , which is what is going to be substituted on the head variable of  $t$ , *i.e.*  $[y/u]$  is an *hereditary* head substitution of  $t$ . We use this idea to define the measure. Note that, according to our reasoning,  $[y/u]$  is an hereditary head substitution also for  $s = (x\ y)[x/(y\ r)[y/u]]$ , but we get around these nested cases because we only have to deal with shallow terms.

► **Definition 4.8.** *Hereditary head contexts* are generated by the following grammar:

$$HH := H \mid HH[x][x/H].$$

<sup>1</sup> Soundness is Theorem 3 (Section 7) in [16] and completeness follows from the fact that  $\lambda\sigma$  enjoys *finite normalization cones*, the abstract rewriting notion studied in that paper.

<sup>2</sup> In particular, it can be shown that  $\Lambda_{[\cdot]}$  enjoys pushouts modulo permutation equivalence exactly as the  $\lambda$ -calculus, which implies—as Mellès says in [16]—that its normalization cones are at most singletons (*i.e.* trivial in comparison with those of the  $\lambda\sigma$ -calculus).

The *head measure*  $|t|_{HH}$  of a shallow term  $t$  is defined by induction on  $t$ :

$$\begin{aligned} |x|_{HH} &= 0; & |t[x/u]|_{HH} &= |t|_{HH}, \text{ if } t \neq HH[x]; \\ |\lambda x.t|_{HH} &= |t|_{HH}; & |t[x/u]|_{HH} &= |t|_{HH} + 1, \text{ if } t = HH[x]; \\ |t u|_{HH} &= |t|_{HH}. \end{aligned}$$

Please notice that  $|t|_{HH} = 0$  for any  $\lambda$ -term  $t$ .

Next lemma proves that  $|t|_{HH}$  correctly captures the number of  $\rightarrow_{1s}$ -reductions from  $t$ , what is then compactly expressed by the successive corollary. The detailed proof of the lemma is in [4].

► **Lemma 4.9** ( $|\cdot|_{HH}$  Decreases with  $\rightarrow_{1s}$ ). *Let  $t$  be a shallow term.*

1.  $t$  is a  $\rightarrow_{1s}$ -normal form iff  $|t|_{HH} = 0$ .
2.  $t \rightarrow_{1s} u$  implies  $|t|_{HH} = |u|_{HH} + 1$ .
3.  $|t|_{HH} > 0$  implies that  $t$  is not  $\rightarrow_{1s}$ -normal.

**Proof.** 1) By induction on  $t$ . 2) By induction on  $t \rightarrow_{1s} u$ . 3) By induction on  $t$ . ◀

Summing up, we get:

► **Corollary 4.10** (Exact Bound to  $\rightarrow_{1s}$ -sequences).  $t \rightarrow_{1s}^n t_0$  iff  $n = |t|_{HH}$ .

**Proof.** By induction on  $n$ . For  $n = 0$  see Lemma 4.9.1, for  $n > 0$  it follows from 4.9.2-3. ◀

Now, we are ready to prove the quadratic relation. The following lemma is the key point for the combinatorial analysis. It shows that if the initial term  $t$  of a reduction  $\rho : t \rightarrow^n u$  is a  $\lambda$ -term, then  $|u|_{HH}$  is bounded by the number of  $\rightarrow_{dB}$  steps in  $\rho$ .

► **Lemma 4.11.** *Let  $t \in \mathcal{T}_\lambda$ . If  $\rho : t \rightarrow^n u$  then  $|u|_{HH} \leq \mathbf{es}(u) = |\rho|_{dB}$ .*

**Proof.** Note that by definition of  $|\cdot|_{HH}$  we get  $|u|_{HH} \leq \mathbf{es}(u)$  for any term  $u$ . So we only need prove that  $\mathbf{es}(u) = |\rho|_{dB}$ . By induction on  $k = |\rho|_{dB}$ . If  $k = 0$  then  $\rho$  is empty, because  $t$  is a  $\lambda$ -term and so it is  $\rightarrow_{1s}$ -normal. Then  $t = u$  and  $\mathbf{es}(u) = 0$ . If  $k > 0$  then  $\rho = \tau; \rightarrow_{dB}; \rightarrow_{1s}^m$  for some  $m$  and some reduction  $\tau$ . Let  $r$  be the end term of  $\tau$  and  $s$  the term s.t.  $r \rightarrow_{dB} s \rightarrow_{1s}^* u$ . By *i.h.*  $\mathbf{es}(r) = |\tau|_{dB} = |\rho|_{dB} - 1$ . Now,  $\mathbf{es}(s) = \mathbf{es}(r) + 1 = |\rho|_{dB}$ , because each  $\rightarrow_{dB}$ -step creates an explicit substitution. By lemma 3.2 we get that any box subterm of  $s$  is a box-subterm of  $t$ , and since  $t$  is a  $\lambda$ -term, the duplication performed by a  $\rightarrow_{1s}$ -step does not increase the number of explicit substitutions. Therefore,  $\mathbf{es}(u) = \mathbf{es}(s) = |\rho|_{dB}$ . ◀

We finally get:

► **Theorem 4.12.** *Let  $t \in \mathcal{T}_\lambda$ . If  $\rho : t \rightarrow^n u$  then  $n = O(|\rho|_{dB}^2)$ .*

**Proof.** There exists  $k \in \mathbb{N}$  s.t.  $\rho = \tau_1; \gamma_1; \dots; \tau_k; \gamma_k$ , where  $\tau_i$  is a non-empty  $\rightarrow_{dB}$ -reduction and  $\gamma_i$  is a  $\rightarrow_{1s}$ -reduction for  $i \in \{1, \dots, k\}$  and it is non-empty for  $i \in \{1, \dots, k-1\}$ .

Let  $r_1, \dots, r_k$  be the end terms of  $\tau_1, \dots, \tau_k$ , respectively. By Corollary 4.10  $|\gamma_j| \leq |r_j|_{HH}$  and by Lemma 4.11  $|r_j|_{HH} \leq \sum_{i \in \{1, \dots, j\}} |\tau_i|$ . Now  $|\rho|_{dB} = \sum_{i \in \{1, \dots, k\}} |\tau_i|$  bounds every  $|r_j|_{HH}$ , hence:

$$\sum_{i \in \{1, \dots, k\}} |\gamma_i| \leq \sum_{i \in \{1, \dots, k\}} |r_i|_{HH} \leq k \cdot |\rho|_{dB}.$$

But  $k$  is bounded by  $|\rho|_{dB}$  too, thus  $\sum_{i \in \{1, \dots, k\}} |\gamma_i| \leq |\rho|_{dB}^2$  and  $n \leq |\rho|_{dB}^2 + |\rho|_{dB} = O(|\rho|_{dB}^2)$ . ◀

Putting together the results from the whole of Section 4, we get:

► **Corollary 4.13** (Invariance, Part I). *There is a polynomial time algorithm that, given  $t \in \mathcal{T}_\lambda$ , computes a term  $u$  such that  $u \downarrow = r$  if  $t$  has  $\rightarrow_h$ -normal form  $r$  and diverges if  $u$  has no  $\rightarrow_h$ -normal form. Moreover, the algorithm works in polynomial time on the unitary cost of the input term.*

One may now wonder why a result like Corollary 4.13 cannot be generalized to, e.g., leftmost-outermost reduction, which is a normalizing strategy. Actually, linear explicit substitutions *can* be endowed with a notion of reduction by levels capable of simulating the leftmost-outermost strategy in the same sense as linear head-reduction simulates head-reduction here. And, noticeably, the subterm property *continues* to hold. What is not true anymore, however, is the quadratic bound we have proved in this section: in the leftmost-outermost linear strategy, one needs to perform *too many* substitutions not related to any  $\beta$ -redex. If one wants to generalize Corollary 4.13, in other words, one needs to further optimize the substitution process. But this is outside the scope of this paper.

## 5 $\Lambda_{[\cdot]}$ as an Acceptable Encoding of $\lambda$ -terms

The results of the last two sections can be summarized as follows: linear explicit substitutions provide both a compact representation for  $\lambda$ -terms and an efficient implementation of  $\beta$ -reduction. Here, efficiency means that the overhead due to substitutions remains under control and is at most polynomial in the unitary cost of the  $\lambda$ -term we start from. But one may wonder whether explicit substitutions are nothing more than a way to *hide* the complexity of the problem under the carpet of compactness: what if we want to get the normal form in the *usual, explicit* form? Counterexamples from Section 2, read through the lenses of Theorem 4.13 tell us that that this is *indeed* the case: there are families of  $\lambda$ -terms with polynomial unitary cost but whose normal form intrinsically requires exponential time to be produced.

In this section, we show that this phenomenon is due to the  $\lambda$ -calculus being a very inefficient way to represent  $\lambda$ -terms: even if computing the unfolding of a term  $t \in \Lambda_{[\cdot]}$  takes exponential time, *comparing* the unfoldings of two terms  $t, u \in \Lambda_{[\cdot]}$  for equality can be done in polynomial time in  $|t| + |u|$ . This way, linear explicit substitutions are proved to be a succinct, acceptable encoding of  $\lambda$ -terms in the sense of Papadimitriou [18]. The algorithm we are going to present is based on dynamic programming: it compares all the *relative unfoldings* of subterms of two terms  $t$  and  $u$  as above, without really computing those unfoldings. Some complications arise due to the underlying notion of equality for  $\lambda$ -terms, namely  $\alpha$ -equivalence, which is coarser than syntactical equivalence. But what is the relative unfolding of a term?

► **Definition 5.1** (Relative Unfoldings). The unfolding  $t \downarrow_C$  of  $t$  relative to context  $C$  is defined by induction on  $C$ :

$$\begin{array}{lll} t \downarrow_{[\cdot]} := t \downarrow; & t \downarrow_{u \ C} := t \downarrow_C; & t \downarrow_{u[x/C]} := t \downarrow_C; \\ t \downarrow_{C \ u} := t \downarrow_C; & t \downarrow_{\lambda x. C} := t \downarrow_C; & t \downarrow_{C[x/u]} := t \downarrow_C \{x/u \downarrow\}. \end{array}$$

Constraining sets allow to give sensible judgments about the equivalence of terms even when their free variables differ:

► **Definition 5.2** (Constraining Sets and Coherence). A *constraining set*  $A$  is a set of pairs  $(x, y)$  of variable names. Two constraining sets  $A$  and  $B$  are *coherent* (noted  $A \sim B$ ) if:

- $(x, y) \in A$  and  $(x, z) \in B$  imply  $y = z$ ;
- $(y, x) \in A$  and  $(z, x) \in B$  imply  $y = z$ .

Moreover,  $A$  is *auto-coherent* if  $A \sim A$ . Observe that  $\sim$  is not reflexive and that a constraining set is auto-coherent iff it is the graph of a bijection.

As an example  $A = \{(x, y), (x, z)\}$  is not auto-coherent, while  $B = \{(w, u)\}$  is auto-coherent, like any singleton set of pairs of variables. Moreover,  $A \sim B$ .

The algorithm we are going to define takes in input a pair  $(a, b)$  of terms. We assume them *preprocessed* as follows: the spaces of substituted, abstracted and free names of  $a$  and  $b$  are all pairwise disjoint and neither  $a$  nor  $b$  contain any subterm in the form  $c[x/d]$ , where  $x \notin \text{fv}(c)$ <sup>3</sup>. We also note  $\mathcal{S}$  the set of substituted variables of both terms. The whole algorithm is built around the notion of an unfolding judgment:

► **Definition 5.3** (Unfolding Judgments and Unfolding Rules). Let  $P = (a, b)$  be a preprocessed pair. An *unfolding judgement* is a triple  $(t, C), v, (u, D)$ , where the *value*  $v$  is either  $\perp$  or a constraining set  $A$ , also noted  $(t, C) \overset{v}{\sim} (u, D)$ . The rules for deriving unfolding judgments are in Figure 2. An operation  $\diamond$  on values is used, which is defined as follows:

1.  $v \diamond w = v \cup w$  if  $v \neq \perp \neq w$  and  $v \sim w$ ;
2.  $v \diamond w = \perp$  if  $v \neq \perp \neq w$  and  $v \not\sim w$ ;
3.  $v \diamond w = \perp$  if  $v = \perp$  or  $w = \perp$ .

The rules in Figure 2 induce a binary relation  $\sqsubset_{a,b}$  on the space of pairs (of pairs) in the form  $((t, C), (u, D))$  such that  $C[t] = a$  and  $D[u] = b$ :  $(P, Q) \sqsubset_{a,b} (R, S)$  if knowing  $v$  such that  $P \overset{v}{\sim} Q$  is necessary to compute  $w$  such that  $R \overset{w}{\sim} S$ .

The meaning of unfolding judgements can be summarized as follows. If  $(t, C) \overset{A}{\sim} (u, D)$ , then  $r = t \downarrow_C$  and  $s = u \downarrow_D$  are  $\alpha$ -equivalent, provided free variables of  $r$  are modified according to  $A$ , seen as a substitution (or, analogously, free variables of  $s$  are modified according to  $A^{-1}$ ). If, instead,  $(t, C) \overset{\perp}{\sim} (u, D)$ , there cannot be any substitution like  $A$  above. The rules in Figure 2 are not only a sound but also complete way of checking equality of terms in the just described sense.

Given a preprocessed pair  $(a, b)$ , checking the equality of  $a \downarrow$  and  $b \downarrow$  through a direct use of the formal system above immediately leads to an exponential blowup, which however can be avoided by way of dynamic programming.

An unfolding matrix for a preprocessed pair  $(a, b)$  is the data structure storing intermediate judgements produced while deriving a judgement  $(a, [\cdot]) \overset{v}{\sim} (b, [\cdot])$ . Relying on unfolding matrices ultimately allows to avoid repeating the same computation many times. Formally, an *unfolding matrix* for a preprocessed pair  $(a, b)$  is a bidimensional array whose rows are indexed by pairs  $(t, C[\cdot])$  such that  $C[t] = a$  and whose columns are indexed by pairs  $(t, C[\cdot])$  such that  $C[t] = b$ . Each element of the unfolding matrix can be either a (possibly empty) constraining sets or  $\perp$  or  $\#$ .

Basically, the unfolding checking algorithm simply proceeds by filling an unfolding matrix with the correct values, following the rules in Figure 2 and starting from an unfolding matrix filled with  $\#$ :

► **Definition 5.4** (Unfolding Checking Algorithm). Let  $P = (a, b)$  be a preprocessed pair. We define the following algorithm, which will be referred to as the *unfolding checking algorithm*:

<sup>3</sup> Any term can be turned in this form in polynomial time, by  $\rightarrow_{gc}$ -normalizing it.

$$\begin{array}{c}
\text{Positive Rules} \\
\hline
\frac{x, y \notin \mathcal{S}}{(x, C)^{\{\overset{(x,y)}{\sim}\}}(y, D)} \text{ var} \quad \frac{(t, C[[\cdot] r]) \overset{v}{\sim} (u, D[[\cdot] s]) \quad (r, C[t \cdot]) \overset{w}{\sim} (s, D[u \cdot])}{(t r, C)^{v \circ w} (u s, D)} @ \\
\frac{(t, C[E[x][x/[\cdot]]) \overset{v}{\sim} (u, D)}{(x, C[E[x/t]]) \overset{v}{\sim} (u, D)} \text{ unf}_l \quad \frac{(t, C) \overset{v}{\sim} (u, D[E[x][x/[\cdot]])}{(t, C) \overset{v}{\sim} (x, D[E[x/u]])} \text{ unf}_r \\
\frac{(t, C[\lambda x. [\cdot]]) \overset{v}{\sim} (u, D[\lambda y. [\cdot]]) \quad (x, y) \in v}{(\lambda x.t, C)^{v \setminus \{(x,y)\}} (\lambda y.u, D)} \lambda_1 \quad \frac{(t, C[[\cdot][x/r]]) \overset{v}{\sim} (u, D)}{(t[x/r], C) \overset{v}{\sim} (u, D)} \text{ sub}_l \\
\frac{(t, C[\lambda x. [\cdot]]) \overset{v}{\sim} (u, D[\lambda y. [\cdot]]) \quad \forall z. (x, z), (z, y) \notin v}{(\lambda x.t, C) \overset{v}{\sim} (\lambda y.u, D)} \lambda_2 \quad \frac{(t, C) \overset{v}{\sim} (t, D[[\cdot][x/r]])}{(t, C) \overset{v}{\sim} (u[x/r], D)} \text{ sub}_r \\
\hline
\text{Error Generation Rules} \\
\hline
\frac{(t, C[\lambda x. [\cdot]]) \overset{v}{\sim} (u, D[\lambda y. [\cdot]]) \quad (\exists z \neq y. \{(x, z)\} \in v) \vee (\exists z \neq x. \{(z, y)\} \in v) \vee v = \perp}{(\lambda x.t, C) \overset{\perp}{\sim} (\lambda y.u, D)} \lambda_3 \\
\frac{}{(\lambda x.t, C) \overset{\perp}{\sim} (u r, D)} \text{ err}_{\lambda @} \quad \frac{}{(u r, C) \overset{\perp}{\sim} (\lambda x.t, D)} \text{ err}_{@ \lambda} \quad \frac{x \notin \mathcal{S}}{(x, C) \overset{\perp}{\sim} (u r, D)} \text{ err}_{x @} \\
\frac{x \notin \mathcal{S}}{(\lambda y.t, C) \overset{\perp}{\sim} (x, D)} \text{ err}_{\lambda x} \quad \frac{x \notin \mathcal{S}}{(x, C) \overset{\perp}{\sim} (\lambda y.t, D)} \text{ err}_{x \lambda} \quad \frac{x \notin \mathcal{S}}{(u r, C) \overset{\perp}{\sim} (x, D)} \text{ err}_{@ x}
\end{array}$$

■ **Figure 2** Unfolding Rules.

1. Initialize all entries in  $\mathbf{M}$  to  $\#$ ;
2. If  $\mathbf{M}$  has no entries filled with  $\#$ , then go to step 7;
3. Choose  $(t, C)$  and  $(u, D)$  such that  $\mathbf{M}[(t, C)][(u, D)] = \#$ , and such that  $\mathbf{M}[P_1][Q_1], \dots, \mathbf{M}[P_n][Q_n]$  are all different from  $\#$ , where  $(P_1, Q_1), \dots, (P_n, Q_n)$  are the immediate predecessors of  $((t, C), (u, D))$  in  $\sqsubset_{a,b}$ ;
4. Compute  $v$  such that  $(t, C) \overset{v}{\sim} (u, D)$ ;
5.  $\mathbf{M}[(t, C)][(u, D)] \leftarrow v$ ;
6. Go to step 2;
7. Return **yes** if  $\mathbf{M}[(a, [\cdot])][(b, [\cdot])]$  is a constraining set which is the identity, otherwise return **no**.

The Unfolding Checking Algorithm has a simple structure: initially  $\mathbf{M}$  only contains  $\#$ , and steps 2 – 5 are repeated until  $\mathbf{M}$  does not contain any  $\#$ . At each iteration, a cell containing  $\#$  is assigned a value  $v$ , while the rest of  $\mathbf{M}$  is left unchanged.

It is now time to prove that the Unfolding Checking Algorithm is correct, i.e., that it gives correct results, if any:

► **Theorem 5.5** (Correctness). *The Unfolding Checking Algorithm, on input  $(a, b)$ , returns **yes** iff  $a \downarrow = b \downarrow$ .*

**Proof.** This can be done by proving the following two lemmas:

- If  $(t, C) \overset{v}{\sim} (u, D)$  and  $v \neq \perp$  then  $v$  induces two renamings  $\sigma$  and  $\theta$  such that  $t \downarrow_C \sigma = u \downarrow_D$  and  $u \downarrow_D \theta = t \downarrow_C$ ;
- If  $(t, C) \overset{\perp}{\sim} (u, D)$ , then there is no such pair of renamings for  $t \downarrow_C$  and  $u \downarrow_D$ .

Both can be proved by induction on the derivation of  $(t, C) \overset{A}{\sim} (u, D)$ . For details, see [4]. ◀

This is not the end of the story, however — one also needs to be sure about the time complexity of the algorithm, which turns out to be polynomial:

► **Proposition 5.6** (Complexity). *The Unfolding Checking Algorithm works in time polynomial in  $|a| + |b|$ .*

**Proof.** The following observations are sufficient to obtain the thesis:

- The number of entries in  $M$  is  $|a||b|$  in total.
- At every iteration, one element of  $M$  changes its value from  $\#$  to some non-blank  $v$ .
- Step 2 can clearly be performed in time polynomial in  $|a| + |b|$ .
- Computing the predecessors of a pair  $P$  can be done in polynomial time, and so Step 3 can itself be performed in time polynomial in  $|a| + |b|$ .
- Rules in Figure 2 can all be applied in polynomial time, in particular because the cardinality of any constraining set produced by the algorithm can be  $|a| + |b|$ , at most. As a consequence, Step 4 can be performed in polynomial time.

This concludes the proof. ◀

## 6 Encoding Turing Machines

A cost model *for computation time* is said to be invariant if it is polynomially related to the standard cost model on Turing machines. In sections 3 and 4, we proved that head reduction of any  $\lambda$ -term  $t$  can be performed on a Turing machine in time polynomial in the number of  $\beta$ -steps leading  $t$  to its normal form (provided it exists). This is proved through explicit substitutions, which in Section 5 are shown to be a reasonable representation for  $\lambda$ -terms: two terms  $t$  and  $u$  in  $\Lambda_{[\cdot]}$  can be checked to have  $\alpha$ -equivalent unfoldings in polynomial time.

The last side of the story is still missing, however. In this section, we will show how head reduction can simulate Turing machine computation in such a way that the unitary cost of the simulating  $\lambda$ -term is polynomially related to the running time of the encoded Turing machine. Results similar to the one we are going to present are common for the  $\lambda$ -calculus, so we will not give all the details, which can anyway be found in [4].

The simplest objects we need to encode in the  $\lambda$ -calculus are finite sets. Elements of any finite set  $A = \{a_1, \dots, a_n\}$  can be encoded as follows:  $[a_i]^A \equiv \lambda x_1. \dots \lambda x_n. x_i$ . Notice that the above encoding induces a total order on  $A$  such that  $a_i \leq a_j$  iff  $i \leq j$ . Other useful objects are finite strings over an arbitrary alphabet, which will be encoded using a scheme attributed to Dana Scott. Let  $\Sigma = \{a_1, \dots, a_n\}$  be a finite alphabet. A string in  $s \in \Sigma^*$  can be represented by a value  $[s]^{\Sigma^*}$  as follows, by induction on the structure of  $s$ :

$$[\varepsilon]^{\Sigma^*} \equiv \lambda x_1. \dots \lambda x_n. \lambda y. y; \quad [a_i r]^{\Sigma^*} \equiv \lambda x_1. \dots \lambda x_n. \lambda y. x_i [r]^{\Sigma^*}.$$

Observe that representations of symbols in  $\Sigma$  and strings in  $\Sigma^*$  depend on the cardinality of  $\Sigma$ . In other words, if  $s \in \Sigma^*$  and  $\Sigma \subset \Delta$ ,  $[s]^{\Sigma^*} \neq [s]^{\Delta^*}$ .

Of course, one should be able to very easily compute the encoding of a string obtained by concatenating another string with a character. Moreover, the way strings are encoded depends on the underlying alphabet, and as a consequence, we also need to be able to convert representations for strings in one alphabet to corresponding representations in another, different, alphabet. This can be done efficiently in the  $\lambda$ -calculus by way of a term  $AC(\Sigma)$  which appends a character to a string (both expressed in the alphabet  $\Sigma$ ) and a term  $CS(\Sigma, \Delta)$  which converts a string  $s \in \Sigma^*$  into another string in  $\Delta^*$  obtained by replacing any character in  $\Sigma - \Delta$  by the empty string.  $AC(\Sigma)$  works in time independent on the size of the input, while  $CS(\Sigma, \Delta)$  works in time *proportional* to the size of the argument.

A configuration  $(u, a, v, q)$  of a Turing machine  $\mathcal{M} = (\Sigma, a_{\text{blank}}, Q, q_{\text{initial}}, q_{\text{final}}, \delta)$  consists of the portion of the tape on the left of the head (namely  $u \in \Sigma^*$ ), the symbol under the head ( $a \in \Sigma$ ), the tape on the right of the head ( $v \in \Sigma^*$ ), and the current state  $q \in Q$ . Such a configuration is represented by the term  $\llbracket (u, a, v, q) \rrbracket^{\mathcal{M}} \equiv \lambda x.x \llbracket u \rrbracket^{\Sigma^*} \llbracket a \rrbracket^{\Sigma} \llbracket v \rrbracket^{\Sigma^*} \llbracket q \rrbracket^Q$ .

We now encode a Turing machine  $\mathcal{M} = (\Sigma, a_{\text{blank}}, Q, q_{\text{initial}}, q_{\text{final}}, \delta)$  in the  $\lambda$ -calculus. Suppose  $\Sigma = \{a_1, \dots, a_{|\Sigma|}\}$  and  $Q = \{q_1, \dots, q_{|Q|}\}$ . The encoding of  $\mathcal{M}$  is defined around three  $\lambda$ -terms:

- First of all, we need to be able to build the initial configuration for  $u$  from  $u$  itself. This can be done in time proportional to  $|u|$  by a term  $\mathcal{I}(\mathcal{M}, \Delta)$ , where  $\Delta$  is the alphabet of  $u$ , which can be different from  $\Sigma$ .  $\mathcal{I}(\mathcal{M}, \Delta)$  simply converts  $u$  into the appropriate format by way of  $CS(\Delta, \Sigma)$ , and then packages it into a configuration.
- Then, we need to extract a string from a final configuration  $C$  for the string. This can be done in time proportional to the size of  $C$  by a term  $\mathcal{F}(\mathcal{M}, \Delta)$ , which makes essential use of  $CS(\Sigma, \Delta)$ .
- Most importantly, we need to be able to simulate the transition function of  $\mathcal{M}$ , i.e. compute a final configuration from an initial configuration (if it exists). This can be done with cost proportional to the number of steps  $\mathcal{M}$  takes on the input, by way of a term  $\mathcal{T}(\mathcal{M})$ . The term  $\mathcal{T}(\mathcal{M})$  just performs case analysis depending on the four components of the input configuration, then manipulating them making use of  $AC(\Sigma)$ .

A peculiarity of the proposed encoding of Turing machines is the use of Scott numerals (instead of Church numerals), which make the simulation work even when head reduction is the underlying notion of computation. Another crucial aspect is *continuation passing*: the  $\lambda$ -terms cited above all take an additional parameter to which the result is applied after being computed. At this point, we can give the desired simulation result:

► **Theorem 6.1** (Invariance, Part II). *If  $f : \Delta^* \rightarrow \Delta^*$  is computed by a Turing machine  $\mathcal{M}$  in time  $g$ , then there is a term  $\mathcal{U}(\mathcal{M}, \Delta)$  such that for every  $u \in \Delta^*$ ,  $\mathcal{U}(\mathcal{M}, \Delta) \llbracket u \rrbracket^{\Delta^*} \rightarrow_{\mathfrak{n}}^n \llbracket f(u) \rrbracket^{\Delta^*}$  where  $n = \mathcal{O}(g(|u|) + |u|)$ .*

**Proof.** Simply define  $\mathcal{U}(\mathcal{M}, \Delta) \equiv \lambda u.\mathcal{I}(\mathcal{M}, \Delta)(\lambda x.\mathcal{T}(\mathcal{M})(\lambda y.\mathcal{F}(\mathcal{M}, \Delta)(\lambda w.w)y)x)u$ . It is routine to prove the thesis. ◀

Noticeably, the just described simulation induces a linear overhead: every step of  $\mathcal{M}$  corresponds to a constant cost in the simulation, the constant cost not depending on the input but only on  $\mathcal{M}$  itself.

## 7 Conclusions

The main result of this paper is the first invariance result for the  $\lambda$ -calculus when reduction is allowed to take place in the scope of abstractions. The key tool to achieve invariance are linear explicit substitutions, which are *compact* but *manageable* representations of  $\lambda$ -terms.

Of course, the main open problem in the area, namely invariance of the unitary cost model for any normalizing strategy (e.g. for the strategy which always reduces the leftmost-outermost redex) remains open. Although linear explicit substitutions cannot be *directly* applied to this problem, the authors strongly believe that this is anyway a promising direction, on which they are actively working at the time of writing.

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## Revision Notice

This is a revised version of the eponymous paper appeared in the proceedings of RTA 2012 (LIPIcs, volume 15, <http://www.dagstuhl.de/dagpub/978-3-939897-38-5>, published in May, 2012), in which rules  $\text{unf}_l$ ,  $\text{unf}_r$ ,  $\lambda_1$ ,  $\text{sub}_l$ ,  $\lambda_2$  and  $\text{sub}_r$  were erroneously absent from Figure 2 (on page 34).

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