

## **RACE: A Rapid, ArChitectural Simulation and Synthesis Framework for Embedded Processors**

Roshan Ragel, Angelo Ambrose, Jorgen Peddersen, Sri Parameswaran

► **To cite this version:**

Roshan Ragel, Angelo Ambrose, Jorgen Peddersen, Sri Parameswaran. RACE: A Rapid, ArChitectural Simulation and Synthesis Framework for Embedded Processors. Mike Hinchey; Bernd Kleinjohann; Lisa Kleinjohann; Peter A. Lindsay; Franz J. Rammig; Jon Timmis; Marilyn Wolf. 7th IFIP TC 10 Working Conference on Distributed, Parallel and Biologically Inspired Systems (DIPES) / 3rd IFIP TC 10 International Conference on Biologically-Inspired Collaborative Computing (BICC) / Held as Part of World Computer Congress (WCC) , Sep 2010, Brisbane, Australia. Springer, IFIP Advances in Information and Communication Technology, AICT-329, pp.137-144, 2010, Distributed, Parallel and Biologically Inspired Systems. <10.1007/978-3-642-15234-4\_14>. <hal-01054480>

**HAL Id: hal-01054480**

**<https://hal.inria.fr/hal-01054480>**

Submitted on 7 Aug 2014

**HAL** is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.

