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# Computation by Interaction for Space-Bounded Functional Programming

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## Abstract

We consider the problem of supporting sublinear space programming in a functional programming language. Writing programs with sublinear space usage often requires one to use special implementation techniques for otherwise easy tasks, e.g. one cannot compose functions directly for lack of space for the intermediate result, but must instead compute and recompute small parts of the intermediate result *on demand*. In this paper, we study how the implementation of such techniques can be supported by functional programming languages.

Our approach is based on modelling computation by interaction using the Int construction of Joyal, Street & Verity. We derive functional programming constructs from the structure obtained by applying the Int construction to a term model of a given functional language. The thus derived core functional language `INTML` is formulated by means of a type system inspired by Baillot & Terui's Dual Light Affine Logic. It can be understood as a programming language simplification of Stratified Bounded Affine Logic. We show that it captures the classes `FLOGSPACE` and `NFLOGSPACE` of the functions computable in deterministic logarithmic space and in non-deterministic logarithmic space, respectively. We illustrate the expressiveness of `INTML` by showing how typical graph algorithms, such a test for acyclicity in undirected graphs, can be represented in it.

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## 1. Introduction

A central goal in programming language theory is to design programming languages that allow a programmer to express efficient algorithms in a convenient way. The programmer should be able to focus on algorithmic issues as much as possible and the programming language should give him or her the means to delegate inessential implementation details to the machine.

In this paper we study how a programming language can support the implementation of algorithms with sublinear space usage. Sublinear space algorithms are characterised by having less memory than they would need to store their input. They are useful for computing with large data that may not fit into memory. Examples of concrete application scenarios are streaming algorithms [35], web crawling, GPU computing, and statistical data mining.

We focus in particular on algorithms with logarithmic space usage. A common intuition of such algorithms is that they access externally stored large data using only a fixed number of pointers. A typical example is that of large graphs, where the pointers are references to graph nodes. To encode a pointer to a node of a graph with  $n$  nodes, one needs space  $O(\log n)$ . Logarithmic space graph algorithms are therefore often thought of as algorithms that work with only a constant number of pointers to graph nodes. Another typical example of large data are long strings. In this case the pointers are character positions.

While logarithmic space algorithms can only store a fixed number of pointers, they can produce large output that does not fit into memory. They do so by producing the output bit by bit. A program that outputs a graph would output the graph nodes one after the other, followed by a stream of the pairs of nodes that are connected by an edge. The program may not have enough memory to store the whole output all at once, but it can produce it bit by bit.

When writing programs with logarithmic space usage, one must often use special techniques for tasks that would normally be simple. Consider for example the composition of two programs. In order to remain in logarithmic space, one cannot run the two algorithms one after the other, as there may not be enough space to store the intermediate result. Instead, one needs to implement composition without storing the intermediate value at all. To compose two programs, one begins by running the second program and executes it until it tries to read a bit from the output of the first program. Only then does one start the first program to compute the requested bit, so that the execution of the second program can

be continued. Running the first program may produce more output than just the required bit, but this output is simply discarded. In this way, the two programs work as coroutines and the output of the first program is only computed on demand, one bit at a time.

Implementing such on-demand recomputation is tedious and error-prone. We believe that programming language support should be very useful for the implementation of algorithms that rely on on-demand recomputation of intermediate values. Instead of implementing composition with on-demand recomputation by hand, the programmer should be able to write function composition in the usual way and have a compiler generate a program that does not store the whole intermediate value.

In this paper we ask: How can the implementation of functions with logarithmic space usage be supported in a programming language? We follow the approach of viewing logarithmic space computation as computation with a fixed number of pointers. Algorithms that use a fixed number of pointer values are not hard to capture by first-order programming languages. Suppose we use a type `int` of unsigned integers to represent pointers. Then computation with a fixed number of pointers simply becomes computation with a fixed number of `int`-values. In imperative programming, this may be captured by a while-language [26]; in functional programming it may be captured by programs that are restricted to first-order data and tail recursion [27].

But how should we represent large data like externally stored graphs or large intermediate results that should only be recomputed in small parts on demand? First, we need some way to use the pointers to actually access some large data. If we work with a large graph, for example, and pointers are just `int`-values, then we need some way of finding out whether or not there is an edge between the nodes reference by two pointers. It would be easy to extend a programming language with primitive operations for this purpose if we only had a single fixed large input graph. But we would like to treat large data much like first-class values that can be passed around and transformed in the program. Therefore, we must explain how to access large data and how to construct it, not just for the input and output, but also for intermediate results computed by the program itself.

In this paper we propose to model large data as interactive entities. The idea is to formalise the intuition that large data is never computed all at once, but that pieces of data are requested when they are needed. For example, a large graph may be represented by an entity that takes pairs of pointers to nodes as requests and that answers with true or false depending on whether or not there is an edge in the graph. Large strings can be represented similarly as entities that take requests for character positions and that answer with the character at that position.

Such interactive entities may be presented to the programmer as functions that map requests to answers. A large string can be represented by its length together with a function of type `int`  $\rightarrow$  `char` that returns the character at the given position (if it is smaller than the length). Graphs can be represented by the number of nodes together with a function `int`  $\times$  `int`  $\rightarrow$  `bool` that tells whether there is an edge between two nodes. Such functional representations of data are suitable for on-demand recomputation. In practical implementations of functional programming languages, the values of type `int`  $\rightarrow$  `char` are not stored by the whole graph of the function. Rather, they are given by code that can compute a character for any given integer when required.

In a higher-order functional programming language, such functional representations of data can be used as first-class values. To represent strings, we may use the type `String` := `int`  $\times$  (`int`  $\rightarrow$  `char`) of pairs of the word length and a function mapping positions to characters. A predicate on strings then becomes a higher-order function of type `String`  $\rightarrow$  `bool`. To work with strings, one may define basic operations directly, e.g. `consc` : `String`  $\rightarrow$  `String` for prepending a character `c`:

$$\text{cons}_c = \lambda x. \text{let } \langle n, w \rangle = x \text{ in } \langle n + 1, \lambda i. \text{if } i = 0 \text{ then } c \text{ else } w(i - 1) \rangle .$$

Having defined a suitable set of such operations, one does not need to know the encoding details of the string type anymore and can use it as if it were a normal data type with operations. It is then not visible that strings may be too large to fit into memory.

In this paper we show that this higher-order functional programming approach can be used for implementing algorithms with logarithmic space usage. We argue that the higher-order approach captures typical algorithms with logarithmic space in a natural way. Suppose we have a program  $p$  of type `String`  $\rightarrow$  `String`; a simple example would be  $\lambda x. \text{cons}_c (\text{cons}_d x)$ . Suppose we have a large externally stored string that we want to use as an input for this program. We can construct a value  $\langle n, w \rangle$  of type `String`, where  $n$  is the length of the string and  $w$  is a function that when invoked with argument  $i$  has the effect of looking up the  $i$ -th character of the externally stored string (perhaps by

sending a network request to a database or similar). Now we can apply  $p$  to  $\langle n, w \rangle$  and obtain a term of type `String`. We use it to compute the characters of the result string one by one by applying the returned function to 0, 1, 2, etc. Each time we ask for a character, the program will request from the input word the characters that it needs to compute the requested character. This corresponds well to the on-demand recomputation that one finds in algorithms with logarithmic space usage. But of course we have to be careful if we want to remain within logarithmic space. With higher-order functions the restriction to tail recursion no longer guarantees that programs only store a constant number of pointers during evaluation. Indeed, with a cps-translation any recursive program can be brought into a tail recursive form.

In this paper we show that higher-order functions are nevertheless suitable for the implementation of functions with logarithmic space usage in a functional language. We show how to combine computation with a fixed number of pointers with higher-order functions in a functional programming language. We explain how one can extend a simple first-order language for programming with a fixed number of pointers with higher-order functions in such a way that programs are still guaranteed to use no more than a fixed number of pointers. The higher-order extensions will in fact be fully definitional in the sense that any higher-order program will be translated to a first-order program. Since our first-order programs cannot use more than a finite number of pointers, this property will then be true for higher-order programs as well.

In this paper we work this out in the form of a minimal functional programming language, `INTML`, for programming with logarithmic space. The `INTML` type system is conceptually simple, and, with the type inference procedure of [11], which is outlined in Section 2.3.1, it is also relatively easy to use. Nevertheless, we argue that the type system is strong enough to allow logarithmic space algorithms to be written in a natural way.

### 1.1. Organising Interactive Computation

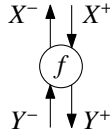
The heart of the definition of `INTML` is the construction of higher-order functions in a first-order language that computes with a fixed number of pointers only. This construction is an instance of the `Int`-construction [28], see Section 5.7. We begin with a simple first-order language that captures computation with a fixed number of pointers. We then study how this language can implement interactive processes, such as the logarithmic space algorithms that access their input by requesting it piece by piece. This model of interactive computation can interpret higher-order functions, which is what `INTML` makes available directly to the programmer. The point is that the whole construction is such that in the end all computation will happen in the first-order language. Even with higher-order functions, programs use no more than a fixed number of pointers and thus remain within logarithmic space.

For an informal explanation of the construction, the details of the first-order base language for computing with a fixed number of pointers are not very important. Assume given a first-order programming language with types `int`, `1` (a unit type), `A + A` and `A × A` that captures a suitable form of computation with a fixed number of pointers. Assume that it has the usual terms for these types and also a loop construct for possibly nonterminating iteration. A first-order program of type  $A \rightarrow B$  is a program that takes a value of the first-order type  $A$  as input and that returns a value of the first-order type output  $B$ . Our first-order programs do not have state and always return the same answer for the same input. For now, the reader may think of programs of type  $A \rightarrow B$  simply as partial functions from the values of type  $A$  to the values of type  $B$ . More details can be found in Section 2.1, but they should not be needed for now.

The construction of higher-order functions is based on a systematic analysis of on-demand interactive computation. The interface of an interactive entity  $X$  can be represented by a pair of first-order types  $(X^-, X^+)$ , where  $X^-$  is the type of all the requests that may be sent to the entity and  $X^+$  is the type of all possible answers. For a representation of long strings, one may choose `String-` to be a type that can encode the questions “What is the length of the string?” and “What is the  $i$ -th character?”. The type `String+` would be chosen to encode the possible corresponding answers. An implementation of the interface  $X$  is a first-order program that maps  $X^-$  to  $X^+$  and thus explains how to answer requests.

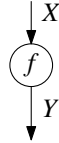
In computation with logarithmic space one often finds the situation that one has access to one interactive entity  $X = (X^-, X^+)$  representing the input and one wants to implement another interactive entity  $Y = (Y^-, Y^+)$  representing the output. This can be done without higher-order types by a first-order program of type  $f: X^+ + Y^- \rightarrow X^- + Y^+$ . It explains how to answer requests to interface  $Y$ , given that one can answer requests to interface  $X$ . To answer a request in  $Y^-$ , we apply  $f$  to it. If the result is in  $Y^+$ , then this is our answer. If it is in  $X^-$ , then we can answer that request with an element of  $X^+$  and then apply  $f$  to this answer. We repeat this until we get an answer in  $Y^+$  (i.e. possibly forever).

Let us think of the first-order program function  $f$  as a message-passing node with two input wires and two output wires as drawn below:

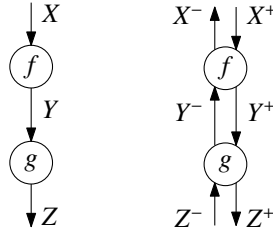


When an input value arrives on an input wire then  $f$  is applied to it and the resulting output value (if any) is passed along the corresponding output wire.

We will draw the two edges for  $X^-$  and  $X^+$  as a single edge in which messages may travel both ways (and likewise for  $Y$ ). Thus we obtain the node depicted below, whose edges are bidirectional in the sense that an edge with label  $X = (X^-, X^+)$  allows any message from  $X^+$  to be passed in the forward direction and any message from  $X^-$  to be passed in the backwards direction:



Message passing nodes can be connected to form message-passing circuits. The composition  $g \circ f: X \rightarrow Z$  of  $f: X \rightarrow Y$  and  $g: Y \rightarrow Z$  as shown on the left below is obtained simply by connecting the two nodes. The underlying first-order program of type  $X^+ + Z^- \rightarrow Z^+ + X^-$  is most easily described in terms of message passing:



An input in  $X^+$  is given to  $f$  and one in  $Z^-$  to  $g$ . If either  $f$  or  $g$  give an output in  $X^-$  or  $Z^+$  then this is the output of  $g \circ f$ . If, however,  $f$  (resp.  $g$ ) outputs a message on  $Y^+$  (resp.  $Y^-$ ), this message is given as an input to  $g$  (resp.  $f$ ). This may lead to a looping computation and  $g \circ f$  may be partial even when  $g$  and  $f$  are both total.

This simple model of interactive computation can be used to support higher-order functions.

*Thunks.* As base type, one may take the interfaces of the form  $[A] := (1, A)$  for any type  $A$ . The single value of type 1 signals an explicit request for a value of type  $A$ , which may be provided as an answer. Thus, one may think of  $[A]$  as a type of thunks that are evaluated on demand.

*Pairs.* The interface  $X \otimes Y := (X^- + Y^-, X^+ + Y^+)$  corresponds to a pair type. It may be used to represent entities that provide both an implementation of  $X$  and one of  $Y$ . The intention is that a query in  $X^-$  is answered with an answer in  $X^+$ , and likewise for  $Y$ . In conjunction with pairs, the empty interface  $I := (\emptyset, \emptyset)$  is sometimes useful, as  $X \otimes I$  and  $X$  can be considered as the same interface.

A message-passing node  $f$  of type  $X \otimes Y \rightarrow Z$  can be understood as a message passing node with two inputs; we depict it as shown below. It corresponds to a program of type  $(X^+ + Y^+) + Z^- \rightarrow (X^- + Y^-) + Z^+$ . It is indicated on the right below, which type of messages should be passed in which direction along which wire.

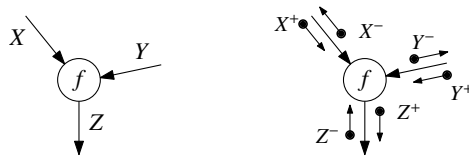


Figure 1: Types of messages

With this understanding, we can construct more complicated message passing circuits. For instance, given  $h: X \rightarrow V \otimes U$ ,  $g: V \otimes W \rightarrow I$  and  $k: Y \otimes Z \rightarrow W$ , we can construct the following circuit.

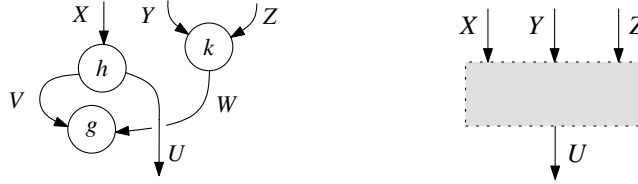


Figure 2: A circuit and its schematic notation

In this circuit, an element of  $U^-$  may be passed to  $h$  against the direction of the output wire. With this input, the node  $h$  may then perhaps decide to pass an element of  $V^+$  along the wire to  $g$  or to return an element of  $X^-$  to the environment. It may also go into an infinite loop, in which case the whole computation will be blocked. Also, message passing inside the circuit may go on indefinitely in an infinite loop.

When only the interface of a circuit, but not its detailed implementation, is important, then we will only show a schematic notation of the circuit, as shown on the right in Fig. 2.

*Functions.* Importantly, it is possible to model higher-order functions as interactive entities. We can define an interface  $X \multimap Y$  for functions from  $X$  to  $Y$  by letting  $(X \multimap Y)^- := X^+ + Y^-$  and  $(X \multimap Y)^+ := X^- + Y^+$ . To understand this, suppose we have an implementation of this interface  $X \multimap Y$  and implementation of interface  $X$ , as shown on the left in the figure below. Then we can connect them to obtain an implementation of interface  $Y$  as shown on the right below. In this figure,  $X^*$  denotes the interface obtained by exchanging question and answer types, i.e.  $(X^-, X^+)^* := (X^+, X^-)$ . The two unlabelled nodes are forwarding nodes. For each message arriving at either of these nodes, there is exactly one possible choice of outgoing wire of the same type, and the message is just sent along this wire. For example, the node of type  $(X \multimap Y) \rightarrow X^* \otimes Y$  must be implemented by a program mapping  $(X \multimap Y)^+ + (X^* \otimes Y)^- = (X^- + Y^+) + (X^+ + Y^-)$  to  $(X \multimap Y)^- + (X^* \otimes Y)^+ = (X^+ + Y^-) + (X^- + Y^+)$  and there is just one canonical program of this type.

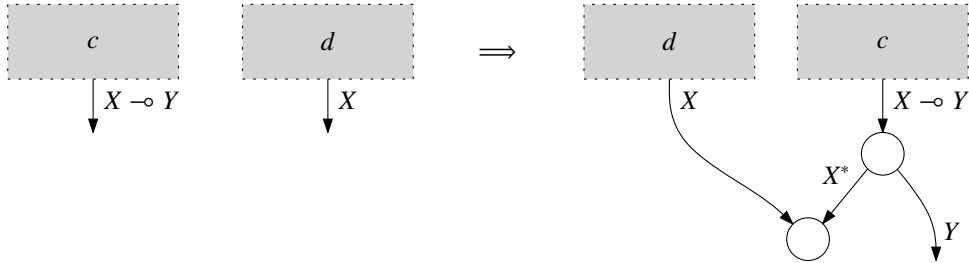


Figure 3: Application

One may think of  $X \multimap Y$  as a way of formalising program parameterisation. A circuit implementing this interface has the property that when it is linked to a circuit implementing interface  $X$  as shown above, then the resulting circuit implements interface  $Y$ . In this sense,  $X \multimap Y$  can be thought of as a type of functions from  $X$  to  $Y$ .

Another way of thinking about the interface  $X \multimap Y = (X^+ + Y^-, X^- + Y^+)$  is in terms of interactive semantics, such as game semantics [2, 24] or the Geometry of Interaction [18]. If one wants to know the result of the function, one sends it a query from  $Y^-$  for the result. The function may directly answer this request with an answer from  $Y^+$ . Or, it may respond with a request in  $X^-$  for its argument. The environment can answer this request by passing the answer in  $X^+$  as a new ‘question’ to the function.

*Subexponentials.* A final important construction in our simple model of interactive computation is that of *subexponentials*. The message-passing nodes that make up circuits are all stateless, being just programs of the stateless first-order language that we started with. They cannot remember any information from one request to the next.

This is a severe restriction on the expressiveness of functions. Suppose, for example, that *String* is an interface that allows us to access long strings by requesting the characters one by one. Predicates on such strings may be given the type  $String \multimap [bool]$ , where `bool` is a type of booleans with values `true` and `false`. Suppose we want to implement such a predicate. Then we cannot store any information between one request to the string and the next. This means that we cannot even decide whether or not the first and the second character of the string are the same. We need to request both characters and there is no way to remember the first character while we request the second one.

Subexponentials are a way of storing first-order data across a request to some entity. For any interface  $X$  and any first-order type  $A$ , we define the subexponential  $A \cdot X$  by  $(A \times X^-, A \times X^+)$ . A request  $\langle a, m \rangle \in A \times X^-$  is intended to mean ‘Please answer the request  $m$  and remind me of  $a$  when you give the answer’. An answer to  $\langle a, m \rangle$  has the form  $\langle a, p \rangle$ , where  $p$  answers  $m$ . The invariant of subexponentials is that  $a$  is always returned unchanged. Because of this invariant, subexponentials can be used for data storage. The interface  $A \cdot X$  in effect allows us to put any element of  $A$  to the side, make a request to  $X$  and retrieve the element of  $A$  when an answer arrives. This is much like local variables are preserved across function calls in machine code by using a call stack.

The above example of checking whether the first two characters of a string are the same, can be defined with type  $((1 + \text{char}) \cdot \text{String}) \multimap [bool]$ . The subexponential  $1 + \text{char}$  is chosen so that we do not store anything when making the first request to the string (hence the 1) and that we store the first returned character across the second request (hence the `char`).

One situation that will often arise is that we have a function of type  $A \cdot X \multimap Y$  that we would like to apply to an argument of type  $X$ . Above we have only explained the application of  $X \multimap Y$  to  $X$ . What we need is to turn an implementation of interface  $X$  into one of interface  $A \cdot X$  in such a way that values of type  $A$  in the first component are always returned unchanged. On the level of circuits this is easy to do. We can change the circuit for  $X$  so that each wire of type  $Z$  becomes a wire of type  $A \cdot Z$ . The nodes pass on the value of type  $A$  unchanged and otherwise behave as before.

## 1.2. Overview

In this paper we develop the functional programming language `INTML`. It is constructed from a simple first-order programming language as a *base language*. `INTML` is obtained by adding to this base language a new class of types and terms that allows us to construct message passing circuits whose nodes are implemented in the base language. We call this the *interactive language*. The new types represent interfaces of wires in message-passing circuits, as outlined above. In `INTML` we use types of the form  $[A]$ ,  $X \otimes Y$  and  $A \cdot X \multimap Y$ . The terms of the interactive language, which are much like in a standard functional programming language, represent the circuits themselves.

By constructing `INTML` in this way we obtain a language that allows the programmer to work both in the base and in the interactive language. This reflects our experience with writing programs with logarithmic space usage. The interactive language allows the programmer to work with large data. The base language can be used work with small data and to manipulate pointers directly. It seems to be useful to keep such a distinction, as restrictions that need to be imposed on the manipulation of large data in order to remain in logarithmic space are not necessary for small values.

We introduce `INTML` in Section 2. The following Sections 3 and 4 contain programming examples and explain how logarithmic space graph algorithms can be represented in `INTML`. In Section 5 we show in detail how higher-order functions in `INTML` are translated to message-passing circuits and thus reduced to first-order programs. In Section 6 we use this reduction to prove soundness and completeness of `INTML` for the functions computable in logarithmic space.

This paper develops and expands the material first presented at ESOP 2010 [10]. It takes into account further developments from APLAS 2010 [11] and APLAS 2011 [43]. We discuss related work in Section 7.

## 1.3. Contribution

The main contribution of this paper is to identify a higher-order functional language and a translation to circuits that can be evaluated using logarithmic space. The idea of translating programs to circuits by means of interpretation in an interactive model is not new. In the literature one can find a number of higher-order functional languages, such as [32, 23], that are translated to circuits using this approach. However, without further restrictions one cannot prove that circuits evaluate in sublinear space. Even in languages without recursion, e.g. [23], one may encounter messages of exponential size during circuit evaluation. What is new in this paper is that we show how to refine language and model in order to obtain an expressive language for sublinear space programming. The main issue is the control

of exponentials, which are responsible for size increases in message passing. In order to obtain space bounds, the exponentials must therefore be restricted. The difficulty is to do so without the programming language becoming too weak. In this paper we argue that `INTML` with its subexponentials represents a good solution to this problem.

Subexponentials make `INTML` an expressive higher-order language. For example, we show that it can type the Kierstead terms, which cannot be typed in similar linear type systems, such as [14]. Moreover, the proof of `FLOGSPACE`-completeness in this paper is completely straightforward. In earlier work, such as [36, 42], the completeness proofs were more involved. The expressiveness of `INTML` is further illustrated by the programming examples, such as in Section 4. Subexponentials not only give us control over space usage, they also afford an efficient treatment of controlled duplication. For example, Ghica and Smith [15] treat copying in the language by actual duplication of terms. Here we show how to allow copying by sharing without the need to duplicate parts of the program. Finally, the language `INTML` presented in this paper is the first higher-order language with logarithmic space bounds that allows one to use (and define!) higher-order combinators such as for tail recursion and call with current continuation.

Compared to [10], this paper includes full proofs of all main results presented there. Moreover, the characterisation of logarithmic space from [10] has been generalised to nondeterminism. The presence not only of higher-order functions but also the ability to define combinators, such as for call/cc or for block-scoped mutable variables like in Idealised Algol, is the main novelty of the language we present here. Finally, the relation with Joyal, Street & Verity's `Int` construction is analysed in more detail and precision. As such, this paper is not just a revised and extended version of previous work, but presents new contributions.

## 2. IntML

In this section we introduce `INTML` and its type system.

### 2.1. Base Language

We begin by fixing the details of the base language. We emphasise that this language was chosen mainly for simplicity and that richer languages can be chosen without affecting the construction of `INTML`. The types of the base language are generated by the grammar

$$A ::= \alpha \mid \text{int} \mid 0 \mid 1 \mid A + A \mid A \times A .$$

They are built from type variables, a type of integers and product and coproduct types. We use lowercase Greek letters  $\alpha, \beta, \gamma$  to range over type variables. The type `int` contains the unsigned integers of some fixed bit-width  $k$ . We define the interactive language without any assumptions on the choice of  $k$ . It shall be possible to evaluate any single program with respect to different choices of  $k$ . Indeed, we shall use `int` as a type of pointers to access large data, which means that we must choose  $k$  depending on the size of the data. In particular, we represent `LOGSPACE`-algorithms by programs that are evaluated with respect to different bit-width, depending on the size of the input; see Section 2.1.1.

The terms of the base language are parameterised by a signature of *constants*  $C$ . For any two types  $A$  and  $B$ , a signature specifies a set  $C(A, B)$  of constants that take an argument of type  $A$  and that have type  $B$ . We assume that any signature contains constants for arithmetic operations  $\{\text{add}, \text{sub}, \text{mul}\} \subseteq C(\text{int} \times \text{int}, \text{int})$  and for (in)equality tests on integers  $\{\text{eq}, \text{lt}\} \subseteq C(\text{int} \times \text{int}, 1 + 1)$ .

The terms of the base language are given by the grammar

$$\begin{aligned} f, g, h ::= & x \mid c(f) \mid * \mid n \mid \langle f, g \rangle \mid \text{fst}(f) \mid \text{snd}(f) \\ & \mid \text{inl}(f) \mid \text{inr}(f) \mid \text{case } f \text{ of } \text{inl}(x) \Rightarrow g \mid \text{inr}(y) \Rightarrow h \\ & \mid \text{let } x = f \text{ loop } g , \end{aligned}$$

in which  $x$  and  $y$  range over variables,  $c$  ranges over constants and  $n$  ranges over integer values. The base language is typed in a standard way. The typing judgement  $\Gamma \vdash f : A$  states that term  $f$  has type  $A$  in context  $\Gamma$ . A context  $\Gamma$  is simply a finite mapping from variables to base types. The typing rules are given in Fig. 4. There are no linearity restrictions and the following weakening and contraction rules are admissible.

$$\frac{\Gamma \vdash f : B}{\Gamma, x : A \vdash f : B} \quad \frac{\Gamma, x : A, y : A \vdash f : B}{\Gamma, z : A \vdash f[z/x, z/y] : B}$$



$$\begin{array}{c}
\frac{}{\Gamma, x: A \vdash x : A} \quad \frac{}{\Gamma \vdash * : 1} \quad \frac{c \in C(A, B) \quad \Gamma \vdash f : A}{\Gamma \vdash c(f) : B} \\
\frac{\Gamma \vdash f : A \quad \Gamma \vdash g : B}{\Gamma \vdash \langle f, g \rangle : A \times B} \quad \frac{\Gamma \vdash f : A \times B}{\Gamma \vdash \text{fst}(f) : A} \quad \frac{\Gamma \vdash f : A \times B}{\Gamma \vdash \text{snd}(f) : B} \\
\frac{\Gamma \vdash f : A}{\Gamma \vdash \text{inl}(f) : A + B} \quad \frac{\Gamma \vdash f : B}{\Gamma \vdash \text{inr}(f) : A + B} \\
\frac{\Gamma \vdash f : A + B \quad \Gamma, x: A \vdash g : C \quad \Gamma, y: B \vdash h : C}{\Gamma \vdash \text{case } f \text{ of } \text{inl}(x) \Rightarrow g \mid \text{inr}(y) \Rightarrow h : C} \\
\frac{\Gamma \vdash f : A \quad \Gamma, x: A \vdash g : B + A}{\Gamma \vdash \text{let } x = f \text{ loop } g : B}
\end{array}$$

Figure 4: Base Language Typing Rules

We explain the base language terms by defining its (unsurprising) operational semantics. It is given by a small-step reduction relation that is parameterised by a natural number  $k$  that represents the bit-width of all integer values. The set of *values* for bit-width  $k$  is defined by the following grammar

$$v, w ::= x \mid * \mid n \mid \langle v, w \rangle \mid \text{inl}(v) \mid \text{inr}(w),$$

in which  $n$  is an integer that can be represented using  $k$  bits.

The loop construct  $\text{let } x = f \text{ loop } g$  is a simple way of capturing iteration in the language. Its operational semantics is defined by the rewrite rule

$$\text{let } x = v \text{ loop } g \rightarrow_k \text{case } g[v/x] \text{ of } \text{inl}(y) \Rightarrow y \mid \text{inr}(z) \Rightarrow (\text{let } x = z \text{ loop } g) .$$

The special case  $(\text{let } x = f \text{ loop } \text{inl}(g))$  evaluates the loop body exactly once. We write  $(\text{let } x = f \text{ in } g)$  as an abbreviation for it. We shall also write  $2$  for  $1 + 1$  and use it as a type of booleans. We write  $\text{true}$  for  $\text{inl}(*)$ ,  $\text{false}$  for  $\text{inr}(*)$  and  $(\text{if } f \text{ then } g \text{ else } h)$  for  $(\text{case } f \text{ of } \text{inl}(x) \Rightarrow g \mid \text{inr}(y) \Rightarrow h)$ , where  $x$  and  $y$  are fresh variables.

The rewrite rules for products and coproducts are standard:

$$\begin{array}{l}
\text{fst}(\langle v, w \rangle) \rightarrow_k v \\
\text{snd}(\langle v, w \rangle) \rightarrow_k w \\
\text{case } (\text{inl}(v)) \text{ of } \text{inl}(x) \Rightarrow f \mid \text{inr}(y) \Rightarrow g \rightarrow_k f[v/x] \\
\text{case } (\text{inr}(v)) \text{ of } \text{inl}(x) \Rightarrow f \mid \text{inr}(y) \Rightarrow g \rightarrow_k g[v/y]
\end{array}$$

The operational semantics of constants depends on the signature  $C$ . We assume that rewrite rules are specified as part of the signature. The reduction rules for the arithmetic constants are:

$$\begin{array}{ll}
\text{add}(\langle v, w \rangle) \rightarrow_k \min(v + w, 2^k - 1) & \text{eq}(\langle v, v \rangle) \rightarrow_k \text{inl}(*) \\
\text{sub}(\langle v, w \rangle) \rightarrow_k \max(0, v - w) & \text{eq}(\langle v, w \rangle) \rightarrow_k \text{inr}(*) \text{ if } v \neq w \\
\text{mul}(\langle v, w \rangle) \rightarrow_k \min(v * w, 2^k - 1) & \text{lt}(\langle v, w \rangle) \rightarrow_k \text{inl}(*) \text{ if } v < w \\
& \text{lt}(\langle v, w \rangle) \rightarrow_k \text{inr}(*) \text{ if } v \geq w
\end{array}$$

For integer constants we let  $n \rightarrow_k 2^k - 1$  for any integer constant  $n$  that exceeds  $2^k - 1$ . Other integer constants are already values and have no reduction. Reduction is allowed in any *evaluation context*. These are contexts defined as

follows:

$$\begin{aligned}
E, F, G ::= & [\cdot] \mid c(v_1, \dots, v_n, E, f_1, \dots, f_m) \mid \langle E, g \rangle \mid \langle v, E \rangle \mid \text{fst}(E) \mid \text{snd}(E) \\
& \mid \text{inl}(E) \mid \text{inr}(E) \mid \text{case } E \text{ of } \text{inl}(x) \Rightarrow g \mid \text{inr}(y) \Rightarrow h \\
& \mid \text{let } x = E \text{ loop } g
\end{aligned}$$

A term  $f$  is said to *reduce* to  $g$  in one step, written  $f \mapsto_k g$  if and only if there are an evaluation context  $E$  and two terms  $h, p$  such that  $h \rightarrow_k p$ ,  $f = E[h]$  and  $g = E[p]$ . We write  $\mapsto_k^*$  for the reflexive transitive closure of  $\mapsto_k$ , as usual.

**Example 1.** The following term  $f$  can be seen to compute the factorial function. For example, we have  $f[4/x] \mapsto_k^* 24$ , provided that  $k$  is large enough that that all numbers can be represented using  $k$  bits.

$$\begin{aligned}
& \text{let } z = \langle x, 1 \rangle \text{ loop } ( \text{let } x = \text{fst}(z) \text{ in} \\
& \quad \text{let } r = \text{snd}(z) \text{ in} \\
& \quad \text{let } x' = \text{sub}(\langle x, 1 \rangle) \text{ in} \\
& \quad \text{case eq}(\langle x', 0 \rangle) \text{ of } \text{inl}(\ast) \Rightarrow \text{inl}(r) \\
& \quad \quad \mid \text{inr}(\ast) \Rightarrow \text{inr}(\langle x', \text{mul}(\langle r, x \rangle) \rangle)
\end{aligned}$$

**Definition 1.** Two terms  $\Gamma \vdash f : A$  and  $\Gamma \vdash g : A$  are *extensionally equal*, if, for any substitution  $\sigma$  that assigns to each variable  $x : B$  in  $\Gamma$  a closed value of type  $B$ , and any closed value  $v$  of type  $A$ , and any  $k$ , we have  $f[\sigma] \mapsto_k^* v$  if and only if  $g[\sigma] \mapsto_k^* v$ .

We shall consider a type  $A$  to be smaller than a type  $B$  when any closed value of type  $A$  can be encoded into a closed value of type  $B$ , and encoding and decoding terms can be written in the base language. Formally, this is defined by means of section-retraction pairs.

**Definition 2.** A *section-retraction pair* between types  $A$  and  $B$  in context  $\Gamma$  is a pair of terms  $s$  and  $r$  such that  $\Gamma, x : A \vdash s : B$  and  $\Gamma, y : B \vdash r : A$  are derivable and such that  $\Gamma, x : A \vdash r[s/y] : A$  and  $\Gamma, x : A \vdash x : A$  are extensionally equal.

**Definition 3.** Type  $A$  is a *retract* of  $B$ , written  $A \triangleleft B$ , if there exists a section-retraction pair between  $A$  and  $B$  in the empty context.

### 2.1.1. On Space Usage

Let us briefly discuss the space usage of base term reduction. In the rest of this paper we shall use values of type `int` to represent pointers into some given input, e.g. as pointers to graph nodes. We will vary the bit-width of base language integers with the size of the input, so that the integer type can represent such pointers. Given an input of size  $n$ , we will choose the bit-width to be  $\lceil \log n \rceil$ , so that `int` can count at least up to  $n$ . This is much like in finite model theory, where logics with counting have a universe as big as the size of the input [25].

With respect to space usage, it is therefore important to understand how much space is needed for term reduction as a function of the bit-width of the `int` type. Suppose the bit-width of `int` is  $k$ . Consider a term  $x : \text{int} \vdash f : A$  and a number  $n$  that can be represented with  $k$  bits. How much space does one need to reduce  $f[n/x] \mapsto_k^* v$ , i.e. to compute a value  $v$ ? It is not hard to see that even in a naive implementation of the reduction relation  $\mapsto_k$  by term rewriting the reduction needs not more than  $O(k)$  space (see Lemma 22). The terms that can appear during reduction all have abstract syntax trees of constant depth, the constant depending only on the term  $f$ . They can therefore contain only a constant number of `int` values, each of which can be encoded using  $O(k)$  bits. One can thus think of base term reduction as computing with a finite number of pointers.

## 2.2. Interactive Language

The base language is the basis of the definition of `INTML`. To complete the definition, we now introduce the interactive part of the language. This part adds higher-order functions to the base language. The interactive language can be understood as a way of constructing message-passing circuits, as outlined in the Introduction, in which each

message-passing node is implemented by a base language term. Nevertheless, the interactive language takes the form of a typed  $\lambda$ -calculus that can be described without reference to circuits and that allows one to write programs following a handier functional paradigm.

Interactive types are defined by the grammar

$$X, Y ::= [A] \mid X \otimes Y \mid A \cdot X \multimap Y ,$$

in which  $A$  ranges over base language types.

The type  $[A]$  represents computations that return a value of base type  $A$  if they terminate. One can think of  $[A]$  much like a monadic type.

The type  $X \otimes Y$  is a type of pairs. We use  $\otimes$  instead of  $\times$  to avoid confusion with the product types in the base language.

The type  $A \cdot X \multimap Y$  is a type of functions from  $X$  to  $Y$ . It has an annotation  $A$ , a base language type, which we call a *subexponential annotation*. This annotation is important for space bound control. It can be understood as a stack shape annotation: Whenever the function makes a call to its argument, it puts a value of base type  $A$  on the stack. This value remains there until the call returns. The caller then retrieves the value from the stack for use in the rest of the computation. We write short  $X \multimap Y$  for the type  $1 \cdot X \multimap Y$ . These are functions that do not need to reserve any stack space when making a call.

We believe that subexponential annotations are best considered as static space usage annotations that are inferred and tracked by a compiler. For a programmer it is usually sufficient to think of the function type  $A \cdot X \multimap Y$  simply as  $X \rightarrow Y$ . Indeed, subexponential annotations do not appear explicitly in terms.

The terms of the interactive language are defined by the following grammar, in which  $f$  ranges over base language terms.

$$\begin{aligned} s, t ::= & x \mid \langle s, t \rangle \mid \text{let } \langle x, y \rangle = s \text{ in } t \mid \lambda x. t \mid s t \mid [f] \mid \text{let } [x] = s \text{ in } t \\ & \mid \text{case } f \text{ of } \text{inl}(x) \Rightarrow s \mid \text{inr}(y) \Rightarrow t \mid \text{copy } s \text{ as } x, y \text{ in } t \mid \text{hack}(x.f) \end{aligned}$$

Although the meaning of the various constructs will be clear only when typing rules and reductions are introduced, we can already introduce a classification of the interactive operators:

- First, there are  $\lambda$ -abstraction and application ( $\lambda x. t$  and  $s t$ , respectively), as well as standard terms for linear pairs ( $\langle t, s \rangle$  and  $\text{let } \langle x, y \rangle = s \text{ in } t$ ).
- Base language terms can appear in interactive terms. First, the term  $[f]$  denotes the computation that on request evaluates  $f$  to a value. The execution of a computation can be forced using the  $\text{let}$ -term  $\text{let } [x] = s \text{ in } t$ . The result of base-language computations can influence interactive terms. The  $\text{case}$  construct

$$\text{case } v \text{ of } \text{inl}(x) \Rightarrow s \mid \text{inr}(y) \Rightarrow t ,$$

behaves as either  $s[w/x]$  or  $t[w/y]$ , depending on whether  $v$  is  $\text{inl}(w)$  or  $\text{inr}(w)$ . There is a third, more intricate, way of making use of the base language in the interactive language, namely the operator  $\text{hack}(x.f)$ , on which we comment at the end of this section and in Section 3.

- The interactive calculus does not allow interactive variables to be used more than once. Controlled duplication is nevertheless possible by means of the term  $\text{copy } t \text{ as } x, y \text{ in } s$ . This term captures a form of explicit sharing. There will be only one copy of  $t$  and any requests from  $s$  to either  $x$  or  $y$  will be handled by the same copy of  $t$ . It would be possible to formulate the type system without explicit  $\text{copy}$  terms, such that  $\text{copy}$  is inserted implicitly when a variable is used more than once. While this may indeed be preferable for practical programming, we use explicit terms here for conceptual clarity.

The type system for the interactive language derives typing judgements of the form

$$\Gamma \mid x_1 : A_1 \cdot X_1, \dots, x_n : A_n \cdot X_n \vdash t : Y . \quad (1)$$

In this judgement  $\Gamma$  is a base language context. The sequence  $x_1 : A_1 \cdot X_1, \dots, x_n : A_n \cdot X_n$  is an *interactive context*. It is a list of variable declarations of the form  $x_i : A_i \cdot X_i$ . This declaration declares  $x_i$  to be a variable of the interactive type  $X_i$ .

$$\begin{array}{c}
\text{Ax} \frac{}{\Gamma \mid x : 1 \cdot X \vdash x : X} \qquad \text{STRUCT} \frac{\Gamma \mid \Phi, x : A \cdot X \vdash t : Y}{\Gamma \mid \Phi, x : B \cdot X \vdash t : Y} A \triangleleft B \\
\text{WEAK} \frac{\Gamma \mid \Phi \vdash t : Y}{\Gamma \mid \Phi, x : A \cdot X \vdash t : Y} \qquad \text{EXCH} \frac{\Gamma \mid \Phi, x : A \cdot X, y : B \cdot Y, \Psi \vdash t : Z}{\Gamma \mid \Phi, y : B \cdot Y, x : A \cdot X, \Psi \vdash t : Z} \\
\text{COPY} \frac{\Gamma \mid \Phi \vdash s : X \quad \Gamma \mid \Psi, x : A \cdot X, y : B \cdot X \vdash t : Z}{\Gamma \mid \Psi, (A + B) \cdot \Phi \vdash \text{copy } s \text{ as } x, y \text{ in } t : Z}
\end{array}$$

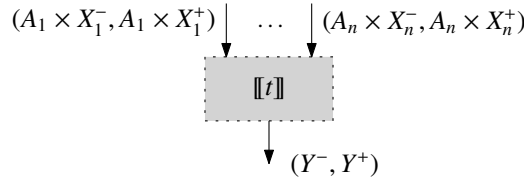
Figure 5: Interactive Typing: Structural Rules

The base language type  $A_i$  is a subexponential annotation that allows the type system to control the space usage of programs. An interactive context may contain at most one declaration for any variable. In the above typing judgement the variables  $x_1, \dots, x_n$  must be therefore be pairwise distinct.

To explain the subexponential annotations, we need to refer to message passing circuits that we have outlined in the Introduction and that will be defined in detail in Section 5. For each interactive type  $X$ , we define two base language types  $X^-$  and  $X^+$  as follows. The informal meaning of these definitions are outlined in the Introduction.

$$\begin{array}{ll}
[A]^- = 1 & [A]^+ = A \\
(X \otimes Y)^- = X^- + Y^- & (X \otimes Y)^+ = X^+ + Y^+ \\
(A \cdot X \multimap Y)^- = A \times X^+ + Y^- & (A \cdot X \multimap Y)^+ = A \times X^- + Y^+
\end{array}$$

The motivation of the interactive type system is that the term  $t$  in the typing judgement (1) will be translated to a message passing circuit  $\llbracket t \rrbracket$  with the following interface.



Each of the message-passing nodes in this circuit is implemented by a base language term, which may make use of the variables from  $\Gamma$ . If the interactive term  $t$  is closed, then the circuit has no incoming wires and a single outgoing wire.

On the wire going out of the circuit we can send requests of base type  $Y^-$  and expect answers of base type  $Y^+$ . To compute the answer, the circuit may need to query any of the  $x_i$ . It does so by sending a request  $\langle a, m \rangle : A_i \times X_i^-$  along the  $i$ -th input wire of the circuit. The circuit expects the answer to be of the form  $\langle a, p \rangle : A_i \times X_i^+$ , i.e. it expects the same  $a$  back with the answer. Since circuits are stateless and cannot store any information, this is useful for remembering the value  $a$  across a call to  $X_i$ .

We next introduce the typing rules. In them we write  $\Phi, \Psi$  for the concatenation of two interactive contexts  $\Phi$  and  $\Psi$ . Note that this is an interactive context only if no variable is declared in both  $\Phi$  and  $\Psi$ . We write  $A \cdot \Phi$  for the context obtained by replacing each declaration  $x : B \cdot X$  in  $\Phi$  with  $x : (A \times B) \cdot X$ . The structural rules in Fig. 5 allow one to manipulate the context  $\Phi$ , which is treated *multiplicatively* in the logical rules. Rule Ax allows one to type a variable  $x$  if the context  $\Phi$  consists of the sole declaration  $x : 1 \cdot X$ . The subexponential annotation 1 means that no additional space is needed when requests are sent to  $x$ . WEAK and EXCH are standard rules for weakening and context reordering. With rule STRUCT it is always possible to increase the subexponential annotations on a variable: in the premise of STRUCT,  $t$  relies on the guarantee that any value of type  $A$  that it sends together with a request of type  $X^-$  to  $x$  will be returned unchanged. If  $A \triangleleft B$ , then replacing  $A$  by  $B$  strengthens this guarantee. Any value of type  $A$  can be encoded into one of type  $B$ , be sent along with the request, and be decoded when it is returned. There may be many ways to encode values of type  $A$  into type  $B$ . It does not matter which one we choose. All that matters for INTML is that by encoding and then decoding we get back the value we started from. The definition of  $A \triangleleft B$  by a retraction is just enough to guarantee this property. Instead of the general semantic definition of  $A \triangleleft B$  by retractions, one may also use syntactic approximations. We refer to [11] for an exploration of possible choices.

$$\begin{array}{c}
\text{[]I} \frac{\Gamma \vdash f : A}{\Gamma \mid \cdot \vdash [f] : [A]} \quad \text{[]E} \frac{\Gamma \mid \Phi \vdash s : [A] \quad \Gamma, x : A \mid \Psi \vdash t : [B]}{\Gamma \mid \Phi, A \cdot \Psi \vdash \text{let } [x] = s \text{ in } t : [B]} \\
\otimes\text{I} \frac{\Gamma \mid \Phi \vdash s : X \quad \Gamma \mid \Psi \vdash t : Y}{\Gamma \mid \Phi, \Psi \vdash \langle s, t \rangle : X \otimes Y} \quad \otimes\text{E} \frac{\Gamma \mid \Phi \vdash s : X \otimes Y \quad \Gamma \mid \Psi, x : A \cdot X, y : A \cdot Y \vdash t : Z}{\Gamma \mid \Psi, A \cdot \Phi \vdash \text{let } \langle x, y \rangle = s \text{ in } t : Z} \\
-\circ\text{I} \frac{\Gamma \mid \Phi, x : A \cdot X \vdash t : Y}{\Gamma \mid \Phi \vdash \lambda x. t : A \cdot X \multimap Y} \quad -\circ\text{E} \frac{\Gamma \mid \Psi \vdash s : X \quad \Gamma \mid \Phi \vdash t : A \cdot X \multimap Y}{\Gamma \mid \Phi, A \cdot \Psi \vdash t s : Y}
\end{array}$$

Figure 6: Interactive Typing: Introductions and Eliminations

$$+\text{E}^c \frac{\Gamma \vdash v : A + B \quad \Gamma, x : A \mid \Phi \vdash s : X \quad \Gamma, y : B \mid \Phi \vdash t : X}{\Gamma \mid \Phi \vdash \text{case } v \text{ of } \text{inl}(x) \Rightarrow s \mid \text{inr}(y) \Rightarrow t : X} \quad v \text{ is a value}$$

Figure 7: Interactive Typing: Cross-Eliminations

The structural rules are set up so that no variable in an interactive context  $\Phi$  may be used more than once. However, the calculus does support explicit duplication. A specific term construct serves this purpose, and can be typed by rule `COPY`. The reader may want to refer to Fig. 12 to get an idea of how the circuits for  $s$  and  $t$  are connected.

The introduction and elimination rules for the three type formers  $[-]$ ,  $\otimes$  and  $\multimap$  appear in Fig. 6. These rules are fairly standard for a linear  $\lambda$ -calculus. Only perhaps rules  $[\ ]\text{I}$  and  $[\ ]\text{E}$  for embedding base language computations and for forcing their execution are of note. In rule  $[\ ]\text{E}$  the term  $(\text{let } [x] = s \text{ in } t)$  allows one to force the computation  $s$  and bind its value to a variable  $x$  in the base language context of  $t$ . The value of the thunk thus becomes available in  $t$  and, as base language contexts are treated additively, this value may be used many times in  $t$ .

Fig. 7 gives the rules for eliminating base language terms over interactive terms. With our simple base language, we only need a single rule  $+\text{E}^c$ , which allows one to define interactive terms by case distinction over a base language term. In this rule  $v$  is restricted to be a value and it will typically be a variable. The typical use of the `case`-term is of the form  $\text{let } [x] = s \text{ in case } x \text{ of } \text{inl}(y) \Rightarrow t \mid \text{inr}(z) \Rightarrow u$ . With the exception of Section 6.2, all results in this paper remain true verbatim also if one allows arbitrary base terms for  $v$  in rule  $+\text{E}^c$ .

The `INTML` type system is completed by rule `HACK` in Fig. 8. Rule `HACK` is the only rule that requires a detailed understanding of the translation of interactive terms to message passing circuits. This rule allows one to define an interactive term of type  $X$  by giving explicitly a base language term  $f$  that implements a message passing node with a single outgoing wire of type  $X$  and no other wires. Therefore, rule `HACK` can only be explained properly once the compilation to circuits that are implemented in the base language has been explained in detail. For now, the reader should consider rule `HACK` like a method for inlining C code in an ML program, whose understanding requires a detailed knowledge of the runtime system.

The other terms can however be understood without any reference to circuits. Below we define an operational semantics for the interactive terms which specifies their meaning. The translation of interactive terms to circuits will then be a space-efficient implementation of this specification.

$$\text{HACK} \frac{\Gamma, x : X^- \vdash f : X^+}{\Gamma \mid \cdot \vdash \text{hack}(x.f) : X}$$

Figure 8: Interactive Typing: Hacking

### 2.3. Typing Examples

We give a first simple example to explain the features of the INTML type system. The term

$$\lambda f. \lambda y. \text{let } [x] = y \text{ in } f [x] [x] : \alpha \cdot (\beta \cdot [\alpha] \multimap \gamma \cdot [\alpha] \multimap [\delta]) \multimap [\alpha] \multimap [\delta]$$

defines a function that evaluates a thunk  $y$ , binds the returned value to variable  $x$  and then invokes a function  $f$ . When  $f$  asks for either of its arguments, the value  $x$  is returned as answer.

In the type,  $\alpha, \beta, \gamma$  and  $\delta$  are type variables that can be instantiated with arbitrary types. That  $\beta$  and  $\gamma$  are type variables means that no restriction is imposed on the subexponentials in the functions that may be given as argument for  $f$ . That the type of the argument  $f$  appears under  $\alpha \cdot (\dots)$  tells us that a value of type  $\alpha$  is sent along with any request to the function  $f$ , and this value is expected to be returned unchanged. In this case, this is the value of the variable  $x$ , which has type  $\alpha$ .

The typing judgement of the term is derivable as follows.

$$\begin{array}{c} \text{Ax} \frac{}{- | y : 1 \cdot [\alpha] \vdash y : [\alpha]} \quad \frac{}{x : \alpha \mid f : 1 \cdot (\beta \cdot [\alpha] \multimap \gamma \cdot [\alpha] \multimap [\delta]) \vdash f [x] [x] : [\delta]} \quad \Pi \\ \text{[ ]E} \frac{}{- | y : 1 \cdot [\alpha], f : (\alpha \times 1) \cdot (\beta \cdot [\alpha] \multimap \gamma \cdot [\alpha] \multimap [\delta]) \vdash \text{let } [x] = y \text{ in } f [x] [x] : [\delta]} \\ \text{EXCH} \frac{}{- | f : (\alpha \times 1) \cdot (\beta \cdot [\alpha] \multimap \gamma \cdot [\alpha] \multimap [\delta]), y : 1 \cdot [\alpha] \vdash \text{let } [x] = y \text{ in } f [x] [x] : [\delta]} \\ \multimap\text{I} \frac{}{- | f : (\alpha \times 1) \cdot (\beta \cdot [\alpha] \multimap \gamma \cdot [\alpha] \multimap [\delta]) \vdash \lambda y. \text{let } [x] = y \text{ in } f [x] [x] : [\alpha] \multimap [\delta]} \\ \text{STRUCT} \frac{}{- | f : \alpha \cdot (\beta \cdot [\alpha] \multimap \gamma \cdot [\alpha] \multimap [\delta]) \vdash \lambda y. \text{let } [x] = y \text{ in } f [x] [x] : [\alpha] \multimap [\delta]} \\ \multimap\text{I} \frac{}{- | - \vdash \lambda f. \lambda y. \text{let } [x] = y \text{ in } f [x] [x] : \alpha \cdot (\beta \cdot [\alpha] \multimap \gamma \cdot [\alpha] \multimap [\delta]) \multimap [\alpha] \multimap [\delta]} \end{array}$$

The sub-derivation  $\Pi$  is shown below. In it we abbreviate  $(\beta \cdot [\alpha] \multimap \gamma \cdot [\alpha] \multimap [\delta])$  by  $T$ .

$$\begin{array}{c} \text{Ax} \frac{}{x : \alpha \mid f : 1 \cdot T \vdash f : (\beta \cdot [\alpha] \multimap \gamma \cdot [\alpha] \multimap [\delta])} \quad \text{[ ]I} \frac{}{x : \alpha \vdash x : \alpha} \\ \multimap\text{E} \frac{}{- | - \vdash \lambda f. \lambda y. \text{let } [x] = y \text{ in } f [x] [x] : \alpha \cdot (\beta \cdot [\alpha] \multimap \gamma \cdot [\alpha] \multimap [\delta]) \multimap [\alpha] \multimap [\delta]} \quad \frac{}{x : \alpha \mid - \vdash [x] : [\alpha]} \quad \text{[ ]I} \frac{}{x : \alpha \vdash x : \alpha} \\ \multimap\text{E} \frac{}{x : \alpha \mid f : 1 \cdot T \vdash f [x] [x] : [\delta]} \quad \frac{}{x : \alpha \mid - \vdash [x] : [\alpha]} \end{array}$$

Note that the similar term  $\lambda f. \lambda y. f y y$  is not well-typed in INTML, as the interactive variable  $y$  is used twice. However, the following term can be typed for any  $X$  and  $Y$ ; a typing derivation is constructed below.

$$\lambda f. \lambda y. \text{copy } y \text{ as } y_1, y_2 \text{ in } f y_1 y_2 : (\beta \cdot X \multimap \gamma \cdot X \multimap Y) \multimap (\beta + \gamma) \cdot X \multimap Y$$

This term passes the same thunk  $y$  to both arguments of  $f$ . If  $f$  forces both its arguments one after the other, then  $y$  will be computed twice. This is in contrast to the above term  $\text{let } [x] = y \text{ in } f [x]$ , which first forces the thunk  $y$ , binds its return value to  $x$  and then passes to  $f$  the thunks that immediately return  $x$ . In this case the computation in  $y$  is executed only once.

#### 2.3.1. Finding Subexponential Annotations

Let us use the typing judgement

$$\lambda f. \lambda y. \text{copy } y \text{ as } y_1, y_2 \text{ in } f y_1 y_2 : (\beta \cdot X \multimap \gamma \cdot X \multimap Y) \multimap (\beta + \gamma) \cdot X \multimap Y$$

as an example to explain how subexponential annotations can be found.

The idea is to first use Hindley-Milner type inference to construct a skeleton of the typing derivation without subexponentials. If we leave out all subexponential annotations and use  $X \rightarrow Y$  in place of  $A \cdot X \multimap Y$ , then we obtain the following skeleton of a derivation for the above term:

$$\begin{array}{c} \frac{}{- | f : X \rightarrow X \rightarrow Y \vdash f : X \rightarrow X \rightarrow Y} \quad \frac{}{- | y_1 : X \vdash y_1 : X} \\ \frac{}{- | f : X \rightarrow X \rightarrow Y, y_1 : X \vdash f y_1 : X \rightarrow Y} \quad \frac{}{- | y_2 : X \vdash y_2 : X} \\ \frac{}{- | f : X \rightarrow X \rightarrow Y, y_1 : X, y_2 : X \vdash f y_1 y_2 : Y} \\ \frac{}{- | f : X \rightarrow X \rightarrow Y, y : X \vdash \text{copy } y \text{ as } y_1, y_2 \text{ in } f y_1 y_2 : Y} \\ \frac{}{- | f : X \rightarrow X \rightarrow Y \vdash \lambda y. \text{copy } y \text{ as } y_1, y_2 \text{ in } f y_1 y_2 : X \rightarrow Y} \\ \frac{}{- | - \vdash \lambda f. \lambda y. \text{copy } y \text{ as } y_1, y_2 \text{ in } f y_1 y_2 : (X \rightarrow X \rightarrow Y) \rightarrow X \rightarrow Y} \end{array}$$

This is not a proper INTML typing derivation. To turn it into one we need to add subexponential annotations. To do so, add fresh type variables for all missing subexponential annotations. In order to obtain a correct typing derivation, we will need to insert rule STRUCT in a number of places. Without loss of generality, we can assume that STRUCT is being applied after every rule to each variable in the interactive context. Let us denote these applications of STRUCT by double lines. We obtain the following annotated skeleton of the derivation.

$$\begin{array}{c}
\frac{}{- \mid f : \alpha_1 \cdot (\beta_1 \cdot X \multimap \beta_2 \cdot X \multimap Y) \vdash f : \beta_1 \cdot X \multimap \beta_2 \cdot X \multimap Y} \\
\frac{}{- \mid y_1 : \alpha_2 \cdot X \vdash y_1 : X} \\
\frac{}{- \mid f : \alpha_3 \cdot (\beta_1 \cdot X \multimap \beta_2 \cdot X \multimap Y), y_1 : \alpha_4 \cdot X \vdash f y_1 : \beta_2 \cdot X \multimap Y} \quad \frac{}{- \mid y_2 : \alpha_5 \cdot X \vdash y_2 : X} \\
\frac{}{- \mid f : \alpha_6 \cdot (\beta_1 \cdot X \multimap \beta_2 \cdot X \multimap Y), y_1 : \alpha_7 \cdot X, y_2 : \alpha_8 \cdot X \vdash f y_1 y_2 : Y} \\
\frac{}{- \mid f : \alpha_9 \cdot (\beta_1 \cdot X \multimap \beta_2 \cdot X \multimap Y), y : \alpha_{10} \cdot X \vdash \text{copy } y \text{ as } y_1, y_2 \text{ in } f y_1 y_2 : Y} \\
\frac{}{- \mid f : \alpha_{11} \cdot (\beta_1 \cdot X \multimap \beta_2 \cdot X \multimap Y) \vdash \lambda y. \text{copy } y \text{ as } y_1, y_2 \text{ in } f y_1 y_2 : \alpha_{10} \cdot X \multimap Y} \\
\frac{}{- \mid - \vdash \lambda f. \lambda y. \text{copy } y \text{ as } y_1, y_2 \text{ in } f y_1 y_2 : \alpha_{11} \cdot (\beta_1 \cdot X \multimap \beta_2 \cdot X \multimap Y) \multimap \alpha_{10} \cdot X \multimap Y}
\end{array}$$

Now we simply gather the constraints on the type variables  $\alpha_i$  and  $\beta_i$  in the side conditions of all the rules in the derivation and we solve them. The side constraints in the above skeleton are:

- $1 \triangleleft \alpha_1$ ,  $1 \triangleleft \alpha_2$  and  $1 \triangleleft \alpha_5$  from the application of STRUCT after the axiom rules.
- $\alpha_1 \triangleleft \alpha_3$  and  $(\beta_1 \times \alpha_2) \triangleleft \alpha_4$  from the application rule with conclusion  $f y_1$ . The conclusion of the application rule has context  $f : \alpha_1 \cdot (\beta_1 \cdot X \multimap \beta_2 \cdot X \multimap Y)$ ,  $y_1 : (\beta_1 \times \alpha) \cdot X$  and applying STRUCT to each of the two variables gives the two constraints.
- $\alpha_3 \triangleleft \alpha_6$ ,  $\alpha_4 \triangleleft \alpha_7$  and  $(\beta_2 \times \alpha_5) \triangleleft \alpha_8$  from the application rule with conclusion  $f y_1 y_2$ .
- $\alpha_6 \triangleleft \alpha_9$  and  $(\alpha_7 + \alpha_8) \triangleleft \alpha_{10}$  from the copying rule.
- $\alpha_9 \triangleleft \alpha_{11}$  from the abstraction rule.

Notice that all constraints have the form  $A \triangleleft \alpha$ , where  $\alpha$  is a type variable that does not appear in  $A$ . A number of such constraints  $A_1 \triangleleft \alpha, \dots, A_n \triangleleft \alpha$  can be solved simply by setting  $\alpha := A_1 + \dots + A_n$ . It is not hard to see that  $A_i \triangleleft (A_1 + \dots + A_n)$  holds for  $i = 1, \dots, n$ . This simple observation can be used to solve the constraints one by one. First we gather all the constraints with  $\alpha_1$  as an upper bound and solve for  $\alpha_1$ , then we continue with  $\alpha_2$  and so on.

Using this simple idea, we can find the following solution to the constraints completely mechanically:

$$\begin{array}{cccccc}
\alpha_1 := 1 & \alpha_2 := 1 & \alpha_3 := 1 & \alpha_4 := \beta_1 \times 1 & \alpha_5 := 1 & \alpha_6 := 1 \\
\alpha_7 := \beta_1 \times 1 & \alpha_8 := \beta_2 \times 1 & \alpha_9 := 1 & \alpha_{10} := (\beta_1 \times 1) + (\beta_2 \times 1) & \alpha_{11} := 1 & 
\end{array}$$

The definition of  $\triangleleft$  further allows us to apply simplifications, such as using  $\beta_1$  instead of  $\beta_1 \times 1$ . We obtain a typing derivation with the claimed type.

#### 2.4. Type Inference

We note that the subexponential annotations in the above examples and all the examples in the rest of this paper were found by the type inference procedure outlined in the example above. It is described in more detail in [11].

We have found that in practice when writing programs one usually does not have to be aware of particular subexponentials in the type. In the above example on copying values, for instance, it is useful to ignore subexponential annotations on functions at first and think of the type as  $([\alpha] \rightarrow [\alpha] \rightarrow [\delta]) \rightarrow [\alpha] \rightarrow [\delta]$ . Keeping the restrictions on variable duplication and the ways of introducing and eliminating  $[\ ]$ -types, it is straightforward to come up with the term  $\lambda f. \lambda y. \text{let } [x] = y \text{ in } f [x] [x]$ . The precise type  $\alpha \cdot (\beta \cdot [\alpha] \multimap \gamma \cdot [\alpha] \multimap [\delta]) \multimap [\alpha] \multimap [\delta]$  with subexponential annotations can then be computed using type inference.

We note that with the general definition of  $\triangleleft$ , type inference is a hard problem. The simple and fast type inference procedure described above is incomplete. It may fail to find a type even when one exists. Moreover, even if it finds a typing derivation, it may not find the best one. Often, there will be many solutions for subexponential constraints. The

$$\begin{aligned}
& (\lambda x.s) t \rightarrow_k s[t/x] \\
& \text{let } \langle x, y \rangle = \langle s, t \rangle \text{ in } u \rightarrow_k u[s/x][t/y] \\
& \text{case inl}(v) \text{ of inl}(x) \Rightarrow s \mid \text{inr}(y) \Rightarrow t \rightarrow_k s[v/x] \quad \text{if } v \text{ is a value} \\
& \text{case inr}(v) \text{ of inl}(x) \Rightarrow s \mid \text{inr}(y) \Rightarrow t \rightarrow_k t[v/y] \quad \text{if } v \text{ is a value} \\
& \quad \text{let } [x] = [v] \text{ in } t \rightarrow_k t[v/x] \quad \text{if } v \text{ is a value} \\
& \text{copy } s \text{ as } x, y \text{ in } u \rightarrow_k u[s/x][s/y] \\
& [f] \rightarrow_k [g] \quad \text{if } f \mapsto_k g
\end{aligned}$$

Figure 9: Interactive Language: Closed Reduction Semantics

choice of the solution will affect the space usage of compiled programs. If we use rule STRUCT with side condition  $A \triangleleft B$ , then in the final implementation the type  $B$  will be used to encode values of type  $A$ . Thus, the smaller we choose  $B$ , the less space will be used in the final program. This should illustrate why with the general definition of  $\triangleleft$ , one should expect the inference of the best type to be hard.

In [11] we study the hardness of type inference. We consider a number of possible syntactic approximations of  $\triangleleft$  and study the computational hardness of type inference with such choices.

### 2.5. Operational Semantics

While the interactive language will be implemented using message passing circuits, we specify its meaning using a standard reduction-based operational semantics. We emphasise that the following operational semantics is a method of specification only.

The operational semantics of the interactive language is formulated as a set of rewrite rules that are applicable in any evaluation context derivable from the following grammar:

$$\begin{aligned}
E, F ::= [\cdot] \mid \langle E, t \rangle \mid \langle t, E \rangle \mid \text{let } \langle x, y \rangle = E \text{ in } t \\
\mid E t \mid t E \mid \text{let } [x] = E \text{ in } t \mid \text{copy } E \text{ as } x, y \text{ in } t
\end{aligned}$$

The evaluation contexts do not uniquely specify an evaluation order. Since we implement reduction only in the base language, it is not necessary to do so. We will show soundness for any choice of evaluation order.

The rewrite rules are given in Fig. 9, where  $s$  and  $t$  are assumed to be *closed* terms, i.e. terms not containing any free occurrence of any variable. Note that the case-terms is restricted to case distinction on values by the type system. A term  $t$  *reduces* to  $s$ , written  $t \mapsto_k s$ , if there are  $u, q$  such that  $u \rightarrow_k q$ ,  $t = E[u]$  and  $s = E[q]$ . As in the base language, the parameter  $k$  represents the bit-width of integers. In Section 5.6, we show that the translation of interactive terms to message passing circuits is sound with respect to reduction:  $t \mapsto_k s$  implies that the circuits for  $t$  and  $s$  have the same message passing behaviour (even if they are not the same).

This completes the definition of the interactive language of INTML. The reader may wonder if in addition to the various ways of embedding base language terms into interactive terms, it is also possible to embed interactive terms into base terms. It is indeed possible to add to the base language a term  $(\text{let } [x] = t \text{ in } f)$  that evaluates a closed interactive term  $t$  and uses the result in a base language term  $f$ . In fact, such a term is already admissible, see Corollary 16 in Section 5.6, which is why we have not included it in the definition.

### 2.6. On Large Data and Space Usage

Base language programs can be understood as programs that store a constant number of pointers. If the bit-width of integers is  $k$ , then any base language program evaluates in space  $O(k)$ . The same is true for the interactive language. We will show that the interactive part of the language can be compiled to the base language. Even though the interactive language allows higher-order functions, it can nevertheless be evaluated in space  $O(k)$ . Thus, INTML can be seen as a higher-order language for the computation with a finite number of pointers.



Let us outline with a simple concrete example how we use higher-order functions in `INTML` to implement sublinear space algorithms. We consider programs that work on long bit vectors, i.e. long sequences of 0s and 1s. The base language type  $2 = 1 + 1$  can be seen as a type of bits, where `inl(*)` stands for 0 and `inr(*)` stands for 1. Bit vectors of length  $n$  can be represented as a base type value of type  $2 \times \dots \times 2$  ( $n$  factors), but this representation would not be space efficient. A value of this type has size  $O(n)$ , i.e. it will need linear space.

A higher-order representation of bit vectors is more space-efficient. If we choose the bit-width of the base language to be  $\log n$ , then the *positions* in the bit vector can be represented by values of type `int`. Bit vectors of length  $n$  can then be represented by functions of type

$$A \cdot [\text{int}] \multimap [2] .$$

(There may be more than  $n$  bits, but we can just ignore superfluous elements.)

First, we explain how large bit vectors can be given as input to `INTML` programs. Suppose we have a program of type  $A \cdot (1 \cdot [\text{int}] \multimap [2]) \multimap [2]$  that represents a predicate on bit vectors. We make no restriction on the subexponential and allow any type  $A$  (we cannot use a type variable as the program may require a type of a particular form, such as  $1 + B$ ). Suppose that we have a concrete large vector (e.g. stored in some database) of length  $n$  that we would like to pass to the program. The program will translate to a circuit with one outgoing wire with interface  $(X^-, X^+)$ .

$$\begin{aligned} X &= A \cdot (1 \cdot [\text{int}] \multimap [2]) \multimap [2] \\ X^- &= A \times (1 \times 1 + 2) + 1 \\ X^+ &= A \times (1 \times \text{int} + 1) + 2 \end{aligned}$$

To evaluate the program with the bit vector as input, we fix the bit-length of integers to be  $\lceil \log n \rceil$  and then use the circuit to evaluate the result of the program. The values of type  $X^-$  are the messages that we can send to the circuit, the values of type  $X^+$  are the possible responses. A typical dialogue with this circuit will be as follows: we send `inr(*)`:  $X^-$  ('What is the value of the predicate?') to the circuit. A typical answer will be `inl((a, inr(*)))`:  $X^+$  ('I would like to know a bit of the input vector. Also, please remind me of  $a$  when you answer.'). We may answer by sending `inl((a, inl((*, *))))`:  $X^-$  ('Which bit would you like to know? Also, here is the  $a$  you asked me to return.'). The reply will be `inl((a, inl((*, n))))`:  $X^+$  ('I would like to know the  $i$ -th bit.'). We may then look up the  $i$ -th bit  $x$  of the large vector and return it by sending `inl((a, inr(x)))`:  $X^-$  ('The requested bit is  $x$ .'). This may continue for a while until we receive the final answer `inr(p)`:  $X^+$  ('The value of the predicate is  $p$ .'). Notice that in  $X^-$  and  $X^+$  the summands for requests and answers line up with the interactive type in  $X$ . For instance, `inr(*)`:  $X^-$  and `inr(a)`:  $X^+$  both pertain the type  $[2]$ .

If large data appears as the output of programs, then it can be requested in a similar way. Suppose we have a closed `INTML` term of type  $B \cdot [\text{int}] \multimap [2]$ , again without restriction on the type  $B$ . The term will be translated to a circuit with a single outgoing wire with interface  $(Y^-, Y^+)$ , in which  $Y^-$  and  $Y^+$  are base language types:

$$\begin{aligned} Y &= B \cdot [\text{int}] \multimap [2] \\ Y^- &= B \times \text{int} + 1 \\ Y^+ &= B \times 1 + 2 \end{aligned}$$

To compute the bit vector we again interact with the circuit. We begin by sending it the message `inr(*)`:  $Y^-$  ('I would like to know the value of the thunk of type  $[2]$  that the function returns.'). The circuit would then typically reply with `inl(b, *)`:  $Y^+$  ('I need to know the argument of the function, i.e. the value of the thunk of type  $[\text{int}]$ . When you answer this request, please also remind me of value  $b$ .'). Suppose we want to know the character in position  $i$ : `int`. We then send the message `inl(b, i)`:  $Y^-$  ('The argument thunk has value  $i$ . And you asked me to remind you of value  $b$ .'). To this, the circuit will typically reply `inr(p)`:  $Y^+$  ('The thunk returned by the function has value  $p$ .'). The whole bit vector represented by the term of type  $A \cdot [\text{int}] \multimap [2]$  can now be reconstructed by repeatedly querying the circuit.

Notice that the messages that we send to and receive from the circuits are all base language values of size  $O(k)$ , where  $k$  is the bit-width of integers. The `INTML` type system guarantees that the same is true for the messages exchanged internally in the circuit. The space needed for messages is thus logarithmic in the length of the bit vectors that are represented, which is  $2^k$ . It is this basic observation that leads to logarithmic space bounds.

Direct interaction with circuits is needed only to supply the input value and to read off the output value. Intermediate values can be defined using the terms of the interactive language. For example the exclusive or of two bit vectors can

be implemented as an interactive program

$$\lambda x. \lambda y. \lambda z. \text{copy } z \text{ as } z_1, z_2 \text{ in let } [u_1] = x \ z_1 \text{ in let } [u_2] = y \ z_2 \text{ in } [\text{xor}(u_1, u_2)]$$

of type

$$(\alpha \cdot [\text{int}] \multimap [2]) \multimap 2 \cdot (\beta \cdot [\text{int}] \multimap [2]) \multimap (\alpha + 2 \times \beta) \cdot [\text{int}] \multimap [2] .$$

In it, `xor` denotes a base language term  $x: 2, y: 2 \vdash \text{xor}(x, y) : 2$  that computes the exclusive or of individual bits, i.e. that satisfies  $\text{xor}(x, y)[a/x, b/y] \mapsto_k^* a \oplus b$  for all (encodings of) bits  $a$  and  $b$ . We omit the standard definition by case distinction. A typing derivation be found in Fig. 17 on page 51. The program for the exclusive or of bit vectors manipulates bit vectors of length  $2^k$ . Its circuit, however, works by passing only messages of size  $O(k)$ . There are other ways of writing the exclusive or function. In the above program the thunk  $z$  will typically be evaluated twice. It is also possible to evaluate it only once up front and keep its value in memory:

$$\lambda x. \lambda y. \lambda z. \text{let } [v] = z \text{ in let } [u_1] = x \ [v] \text{ in let } [u_2] = y \ [v] \text{ in } [\text{xor}(u_1, u_2)].$$

The type reflects the changed memory behaviour:

$$\text{int} \cdot (\alpha \cdot [\text{int}] \multimap [2]) \multimap (\text{int} \times 2) \cdot (\beta \cdot [\text{int}] \multimap [2]) \multimap [\text{int}] \multimap [2].$$

A typing derivation be found in Fig. 17 on page 51.

### 3. Direct Definition of Combinators

The interactive language is a simple linear  $\lambda$ -calculus with restricted copying. As such it is quite weak. However, with rule `HACK` a programmer can define interactive terms directly by giving an implementation of the message passing nodes that implement them. The main intended use of this is to allow the programmer to enrich the interactive language by implementing useful combinators of higher-order type that can then be used as if they were built-in constants. In this section we give examples of how to implement a few useful combinators.

#### 3.1. Iteration

The most important example of such a combinator is an iterator

$$\text{loop} : \alpha \cdot (\gamma \cdot [\alpha] \multimap [\alpha + \beta]) \multimap [\alpha] \multimap [\beta]$$

that informally satisfies  $(\text{loop } s \ t) = [v]$  if  $(s \ t) = [\text{inr}(v)]$  and  $(\text{loop } s \ t) = \text{loop } s \ [v]$  if  $(s \ t) = [\text{inl}(v)]$ . We implement this combinator directly by  $\text{loop} := \text{hack}(x.l)$  for a suitable base language term  $l$  of type  $x: X^- \vdash l : X^+$ , where:

$$\begin{aligned} X &= \alpha \cdot (\gamma \cdot [\alpha] \multimap [\alpha + \beta]) \multimap ([\alpha] \multimap [\beta]) \\ X^- &= \alpha \times (\gamma \times 1 + (\alpha + \beta)) + (\alpha + 1) \\ X^+ &= \alpha \times (\gamma \times \alpha + 1) + (1 + \beta) \end{aligned}$$

The term  $\text{hack}(x.l)$  defines a single message passing node with an outgoing wire labelled  $X$ . It can therefore receive messages of type  $X^-$  and may reply with messages of type  $X^+$ .

The term  $x: X^- \vdash l : X^+$  implements the message passing node. It works by case distinction on  $x$  and gives the following result in the five possible cases:

$$l[\text{inr}(\text{inr}(*))/x] \mapsto_k^* \text{inr}(\text{inl}(*)) \quad (2)$$

$$l[\text{inr}(\text{inl}(a))/x] \mapsto_k^* \text{inl}(\langle a, \text{inr}(*) \rangle) \quad (3)$$

$$l[\text{inl}(\langle a, \text{inr}(\text{inr}(b)) \rangle)/x] \mapsto_k^* \text{inr}(\text{inr}(b)) \quad (4)$$

$$l[\text{inl}(\langle a, \text{inr}(\text{inl}(a')) \rangle)/x] \mapsto_k^* \text{inl}(\langle a', \text{inr}(*) \rangle) \quad (5)$$

$$l[\text{inl}(\langle a, \text{inl}(\langle g, * \rangle) \rangle)/x] \mapsto_k^* \text{inl}(\langle a, \text{inl}(\langle g, a \rangle) \rangle) \quad (6)$$

We leave it as an easy exercise to write out a concrete term  $l$  that implements this specification.

The term  $l$  implements the following strategy: (2) says that a request for the value of `loop s t` results in the value of  $t$  being requested. When an answer from  $t$  arrives, we put it in a memory cell (corresponding to  $\alpha \cdot -$  in the type) and ask  $s$  for its return value (3). Whenever  $s$  asks for its argument, we supply the value from the memory cell (6). If  $s$  returns value `inr(b)`, then we are done and give  $b$  as output (4). If  $s$  returns value `inl(a')`, then we overwrite the memory cell content with  $a'$  and restart the step function  $s$  by asking for its result (5).

In Section 5 we will make precise the circuit implementation of `INTML` and in Section 5.6 we show that it correctly implements the operational semantics. With the techniques of Section 5.6, one can show:

**Proposition 2.** *The following reduction is sound (in the sense of Theorem 13) in the interactive language.*

$$\text{loop } s \ t \ \rightarrow_k \ \text{let } [x] = t \ \text{in let } [y] = s \ [x] \ \text{in case } y \ \text{of } \text{inl}(z) \Rightarrow \text{loop } s \ [z] \\ | \text{inr}(z) \Rightarrow [z]$$

The proof goes by spelling out the circuit implementing the right-hand side of the reduction. The direct implementation of `loop` above is an explicit description of this strategy.

We give a typical example for a use of `loop`. Assume  $A$  is a base language type that we can iterate over in the following sense: there are terms  $\vdash \text{first}_A : A$  and  $x : A \vdash \text{next}_A(x) : A$  and  $x : A \vdash \text{islast}_A(x) : 2$ , such that  $\text{first}_A$  is the first element of  $A$  and, by repeatedly evaluating  $\text{next}_A$ , we get all values of  $A$ . The term  $\text{islast}_A$  indicates whether we have arrived at the last element, in which case its value is `inl(*)`. Otherwise the value is always `inr(*)`. For any variable-free type  $A$  such terms are easily defined.

With these assumptions, we define a combinator  $\text{fold}_A$  with the property that  $\text{fold}_A \ s \ t$  computes  $s \ x_n \ (\dots (s \ x_1 \ (s \ x_0 \ t)))$ , where  $x_0 = \text{first}_A$  and  $x_{i+1} = \text{next}_A(x_i)$  and  $x_n$  is the last element in the enumeration of  $A$ .

$$\text{fold}_A : (A \times \beta \times A \times \beta) \cdot ([A] \multimap [\beta] \multimap [\beta]) \multimap [\beta] \multimap [\beta] \\ \text{fold}_A = \lambda f. \lambda y. \text{loop } (\lambda p. \text{let } [\langle x, y \rangle] = p \ \text{in} \\ \quad \text{let } [y'] = f \ [x] \ [y] \ \text{in} \\ \quad \text{let } [b] = \text{islast}_A(x) \ \text{in} \\ \quad \text{case } b \ \text{of } \text{inl}(\text{true}) \Rightarrow [\text{return}(y')] \\ \quad | \text{inr}(\text{false}) \Rightarrow [\text{continue}(\langle \text{next}_A(x), y' \rangle)]) \\ \quad ) (\text{let } [y_0] = y \ \text{in } [\langle \text{first}_A, y_0 \rangle])$$

In this program we write `let`  $[\langle x, y \rangle] = p$  `in`  $t$  as syntactic sugar for the more verbose `let`  $[p'] = p$  `in`  $t[\text{fst}(p')/x, \text{snd}(p')/y]$ . We use the suggestive abbreviations `return` := `inr` and `continue` := `inl`.

### 3.2. Call with Current Continuation

A second useful combinator is a version of `callcc`. It can be given the following type for any interactive type  $X$ :

$$\text{callcc} : (\gamma \cdot ([\alpha] \multimap X) \multimap [\alpha]) \multimap [\alpha]$$

Its definition is again `callcc` := `hack(x.c)`, where  $c$  is a base language term of type  $x : Y^- \vdash c : Y^+$ :

$$Y = 1 \cdot (\gamma \cdot ([\alpha] \multimap X) \multimap [\alpha]) \multimap [\alpha] \\ Y^- = 1 \times (\gamma \times (\alpha + X^-) + \alpha) + 1 \\ Y^+ = 1 \times (\gamma \times (1 + X^+) + 1) + \alpha$$

We implement  $c$  by a nested case distinction, so that we have the following reductions:

$$c[\text{inr}(*)/x] \mapsto_k^* \text{inl}(\langle *, \text{inr}(*) \rangle) \tag{7}$$

$$c[\text{inl}(\langle *, \text{inr}(a) \rangle)/x] \mapsto_k^* \text{inr}(a) \tag{8}$$

$$c[\text{inl}(\langle *, \text{inl}(g, \text{inr}(v)) \rangle)/x] \mapsto_k^* \text{inl}(\langle *, \text{inl}(\langle g, \text{inl}(*) \rangle) \rangle) \tag{9}$$

$$c[\text{inl}(\langle *, \text{inl}(g, \text{inl}(a)) \rangle)/x] \mapsto_k^* \text{inr}(a) \tag{10}$$

These assignments implement `callcc` as follows: a request for `callcc k` results by (7) in a request for the return value of  $k$ . If  $k$  produces a result value, then (8) applies and the request is forwarded as the return value of `callcc k`. If  $k$  ever uses its argument, which amounts to calling the continuation, then the value passed to the continuation should be returned as the final result. This is done by assignments (9) and (10). Whenever  $k$  requests the result of the continuation, this request is turned into a request to  $k$  for the argument of the continuation (9). Upon supply of the argument to the continuation, the computation is aborted and this argument is returned as the end result (10).

The implementation is similar to the interpretation of `callcc` in game semantics, see e.g. [30]. As an example for `callcc`, we show the implementation of a *tail recursion combinator* `tailfix`.

```
tailfix:  $\alpha \cdot (\gamma \cdot ([\alpha] \multimap [\beta]) \multimap (\delta \cdot [\alpha] \multimap [\beta])) \multimap [\alpha] \multimap [\beta]$ 
tailfix =  $\lambda f. \text{loop } (\lambda x. \text{callcc } (\lambda k. \text{let } [w] = f \ (\lambda y. \text{let } [v] = y \text{ in } k \ [\text{continue}(v)]) \ x \text{ in } [\text{return}(w)]))$ 
```

This combinator may be used to define functions by tail recursion. The tail recursive definition of factorial may then be defined as shown below.

```
fact:  $[\text{int}] \multimap [\text{int}]$ 
fact =  $\lambda x. \text{let } [v] = x \text{ in fact\_aux } [\langle v, 1 \rangle]$ 
fact\_aux:  $[\text{int} \times \text{int}] \multimap [\text{int}]$ 
fact\_aux =  $\text{tailfix } (\lambda \text{fact\_aux}. \lambda x. \text{let } [\langle i, \text{acc} \rangle] = x \text{ in}$ 
 $\text{if eq}(\langle i, 0 \rangle) \text{ then } [\text{acc}] \text{ else fact\_aux } [\langle \text{sub}(i, 1), \text{mul}(i, \text{acc}) \rangle])$ 
```

The definition of `tailfix` using `callcc` may look complicated. It is also possible to define `tailfix` directly using `hack`, in which case the definition is much like that of `loop`.

## 4. Larger Examples

While the syntax of `INTML` is quite minimal, it is already possible to write meaningful programs. In this section we give examples to substantiate this claim.

### 4.1. Logarithmic Space Graph Algorithms

First we show how typical algorithms with logarithmic space usage can be implemented. We implement two classic `LOGSPACE`-complete decision problems [9]: the problem of deciding whether a given undirected graph has a cycle, and the problem of deciding whether two nodes in an undirected forest are connected by a path. We implement the first problem directly and solve the second one by reduction to the first. Section 4.1.2 is based on [11].

We use a higher-order representation of graphs, where a graph is given by its size together with a binary predicate that represents the set of edges. A pair of a number  $n$  and a predicate  $\chi: \mathbb{N} \times \mathbb{N} \rightarrow 2$  represents the graph with vertex set  $V = \{0, \dots, n-1\}$  and edges  $E = \{\langle m, n \rangle \in V^2 \mid \chi(m, n) = \text{true}\}$ . In `INTML` such pairs can be represented by the following interactive type.

$$\text{Graph}_A = [\text{int}] \otimes (A \cdot [\text{int} \times \text{int}] \multimap [2])$$

In the rest of this section we will elide subexponential annotations. Our aim in this section is to show that `FLOGSPACE` functions can be implemented in `INTML`. For this purpose it is sufficient to know that subexponential annotations can be found. The programs in this section were written using a prototype implementation that infers subexponential annotations using the simple method outlined in Section 2.3.1, see also [11]. If one is interested in concrete space bounds, one can use type inference to compute the precise annotations, but in order to know that the implemented algorithm is in `FLOGSPACE` it is enough to know that annotations exist. Therefore, instead of  $\text{Graph}_A$  and  $A \cdot X \multimap Y$  we write just  $\text{Graph}$  and  $X \rightarrow Y$ , with the understanding that it is possible to write down suitable annotations.

To define functions on graphs it is convenient to define boolean functions also on the interactive level. Case distinction  $\text{if}: [2] \rightarrow X \rightarrow X \rightarrow X$  can be defined by  $\lambda b. \lambda x. \lambda y. \text{let } [c] = b \text{ in case } c \text{ of inl}(t) \Rightarrow x \mid \text{inr}(f) \Rightarrow y$  for arbitrary  $X$ . Boolean functions for negation, conjunction and disjunction can similarly be defined with types  $\text{neg}: [2] \rightarrow [2]$ ,  $\text{and}: [2] \rightarrow [2] \rightarrow [2]$  and  $\text{or}: [2] \rightarrow [2] \rightarrow [2]$ .

In graph algorithms we frequently need to iterate over all nodes of a graph. It is possible to do so directly using the `loop` combinator. It may be used to define a fold-like function for this purpose, just like  $fold_{\text{int}}$  in Section 3. In this section we need only a function  $forall$  that checks a given property  $p$  for all nodes of a graph. It can be defined concisely using  $fold_{\text{int}}$ .

$$\begin{aligned} forall &: [\text{int}] \rightarrow ([\text{int}] \rightarrow [2]) \rightarrow [2] \\ forall &= \lambda size. \lambda p. \text{let } [n] = size \text{ in} \\ &\quad fold_{\text{int}} (\lambda i. \lambda r. \text{let } [m] = i \text{ in} \\ &\quad\quad (and r (or [!t(m,n)] (p [m]))) \\ &\quad\quad\quad [true]) \end{aligned}$$

Using higher-order functions we can write functions to manipulate graphs. For example, a function that adds an edge to a graph can be defined as follows.

$$\begin{aligned} addEdge &: Graph \rightarrow [\text{int} \times \text{int}] \rightarrow Graph \\ addEdge &= \lambda graph. \text{let } \langle size, edge \rangle = graph \text{ in} \\ &\quad \lambda newedge. \langle size, \lambda p. \text{let } [\langle m, n \rangle] = p \text{ in} \\ &\quad\quad \text{let } [\langle m', n' \rangle] = newedge \text{ in} \\ &\quad\quad or (edge [\langle m, n \rangle]) \\ &\quad\quad\quad [if eq(\langle m, m' \rangle) then eq(\langle n, n' \rangle) else false] \end{aligned}$$

In this definition we have allowed ourselves pattern matching notation to write  $\text{let } [\langle m, n \rangle] = p \text{ in } t$  instead of the more verbose  $\text{let } [p'] = p \text{ in } t[\text{fst}(p')/m, \text{snd}(p')/n]$ .

#### 4.1.1. Checking for Absence of Self-Loops

A first simple graph algorithm is a test that checks if a given graph contains a node with a self-loop, i.e. an edge to itself. It may be defined as follows:

$$\begin{aligned} noSelfLoop &: Graph \rightarrow [\text{int} \times \text{int}] \rightarrow Graph \\ noSelfLoop &= \lambda graph. \text{let } \langle size, edge \rangle = graph \text{ in} \\ &\quad \text{let } [n] = size \text{ in} \\ &\quad forall [n] (\lambda s'. \text{let } [s] = s' \text{ in} \\ &\quad\quad forall [n] (\lambda d'. \text{let } [d] = d' \text{ in} \\ &\quad\quad\quad neg (and (edge [\langle s, d \rangle]) [eq(\langle s, d \rangle]))) \end{aligned}$$

#### 4.1.2. Checking for Absence of Undirected Cycles

The algorithm of Cook & McKenzie for checking acyclicity of undirected graphs can be explained using the notion of a right-hand walk. A right-hand walk is like a walk in a labyrinth where one always keeps his right hand on the wall. One imagines the graph edges to be corridors and the nodes to be junctions. The edges connected to each node are ordered as if the node were a junction of the arriving corridors. Cook & McKenzie's observation then is that an undirected graph is acyclic if and only if any right-hand walk will return to its starting point by traversing the edge over which it has left this point in the opposite direction. This observation leads to a `FLOGSPACE` algorithm, since this property can be checked by following right-hand walks, and for this one needs to keep in memory just a few graph nodes.

In order to implement right-hand walks, we first need a function

$$nextEdge: Graph \rightarrow [\text{int} \times \text{int}] \rightarrow [\text{int} \times \text{int}]$$

that takes an edge  $\langle s, d \rangle$  and returns the next edge  $\langle s, e \rangle$  emanating from the same source. To implement this function, we first define a base language term  $x: \text{int} \vdash next(x): \text{int}$  by

$$next(x) = \text{let } x' = \text{add}(\langle x, 1 \rangle) \text{ in if eq}(\langle x', x \rangle) \text{ then } 0 \text{ else } x' .$$

It wraps around to 0 once the maximum `int`-value is exceeded.

Then we can define `nextEdge` as follows. Given  $\langle s, d \rangle$ , it starts from  $\langle s, \text{next}(d) \rangle$  and applies `next` to the second component of the pair until it has found an edge.

```
nextEdge: Graph → [int × int] → [int × int]
nextEdge = λgraph. let ⟨size, edge⟩ = graph in
  loop (λedge.
    let [⟨src, dst⟩] = edge in
    let [c'] = [⟨src, next dst⟩] in
    if (edge [c']) [return(c')] [continue(c')])
```

We can then write a function `checkPath`, which follows a right-hand-rule walk starting from some given edge and checks if this walk returns to its origin by walking the given edge in the opposite direction.

```
checkPath: Graph → [int × int] → [2] =
  λgraph. λinputedge. copy graph as graph1, graph2 in
    let ⟨size, edge⟩ = graph1 in
    let [⟨s, d⟩] = inputedge in
    if (edge [⟨s, d⟩])
      (loop (λw. let [⟨s', d'⟩] = w in
        if [eq(d', s)]
          [return(eq(⟨s', d⟩))]
          (let [d] = nextEdge graph2 [⟨d', s'⟩] in [continue(d)])) [⟨s, d⟩])
    [true]
```

Now, writing a function `checkCycle` for testing for the presence of non-trivial cycles in undirected graphs is a simple matter of verifying `checkPath` for all edges in the graph.

```
checkCycle: Graph → [2] =
  λgraph. let ⟨size, edge⟩ = graph in
    let [n] = size in
    copy edge as edge1, edge2 in
    forall [n] (λs'. let [s] = s' in
      forall [n] (λd'. let [d] = d' in
        if (edge1 [⟨s, d⟩])
          (checkPath [n], edge2) [⟨s, d⟩])
        [true]))
```

Finally, an acyclicity check can be defined by combining this test with a test for the absence of trivial cycles, i.e. self-loops.

```
acyclic: Graph → [2] =
  λgraph. copy graph as graph1, graph2 in
    and (noSelfLoop graph1) (checkCycle graph2)
```

#### 4.1.3. Undirected Forest Accessibility

The problem of undirected forest accessibility is to decide whether two given nodes  $u$  and  $v$  in a given undirected forest are connected by a path. Cook & McKenzie [9] show that this is a `LOGSPACE`-complete decision problem. To show

that the problem is in LOGSPACE, they reduce it to the problem of deciding undirected acyclicity. Two nodes  $u$  and  $v$  are connected if and only if they are connected directly by an edge, or if adding an undirected edge between them makes the graph cyclic.

This reduction is easy to implement in INTML.

```

undirectedForestAccessible : Graph → [int × int] → Graph
undirectedForestAccessible = λgraph. copy graph as graph1, graph2 in
    let ⟨size, edge⟩ = graph1 in
    λe. copy e as e1, e2 in
        ⟨size, or (edge e1) (neg (acyclic (addEdge graph2 e2)))⟩

```

This is a simple example of how the use of higher-order functions in INTML allows one to manipulate large data, in this case graphs, as if they were a value in memory.

#### 4.2. Encoding Algol-like Languages

The definition of INTML was guided by the question of how programming with sublinear space can be supported in functional programming languages. However, the direct definition mechanism in INTML is expressive enough to express imperative programming constructs as in Idealised Algol as well.

Reynolds has introduced *Idealised Algol* as a synthesis of functional and imperative programming [41]. Its core is the simply-typed  $\lambda$ -calculus for functional programming, which is extended with constants for imperative programming constructs. Assume the simply-typed  $\lambda$ -calculus with the following types

$$U, V ::= \text{com} \mid \text{exp}_\tau \mid \text{cell}_\tau \mid U \times V \mid U \rightarrow V ,$$

where  $\tau \in \{\text{bool}, \text{int}\}$ . The idea is that terms of type **com** are imperative commands that can be executed and that may have side effects, terms of type  $\text{exp}_\tau$  are expressions that when executed return a value of type  $\tau$ , and that  $\text{cell}_\tau$  represents memory cells with content of type  $\tau$  that may be updated destructively. In order to be able to construct programs of these types, one assumes suitable term constants. In the literature one can find a number of variants of Idealised Algol with various choices of constants. A typical selection is:

<pre> true: exp<sub>bool</sub> neg: exp<sub>bool</sub> → exp<sub>bool</sub> or: exp<sub>bool</sub> × exp<sub>bool</sub> → exp<sub>bool</sub> 0, 1, 2, ...: exp<sub>int</sub> leq: exp<sub>int</sub> × exp<sub>int</sub> → exp<sub>bool</sub> add, sub, times: exp<sub>int</sub> × exp<sub>int</sub> → exp<sub>int</sub> </pre>	<pre> skip: com seq: com × com → com if: exp<sub>bool</sub> × com × com → com while: exp<sub>bool</sub> × com → com newvar<sub>τ</sub>: (cell<sub>τ</sub> → com) → com asg<sub>τ</sub>: (cell<sub>τ</sub> × exp<sub>τ</sub>) → com der<sub>τ</sub>: cell<sub>τ</sub> → exp<sub>τ</sub> </pre>
--	---

The constants for expressions are a selection of standard operations on expressions. For example,  $\text{leq} \langle e_1, e_2 \rangle$  represents the boolean expression  $e_1 \leq e_2$ . The constants for commands represent imperative programs: **skip** is the command that does nothing;  $\text{seq} \langle c_1, c_2 \rangle$  represents the sequential composition of  $c_2$  after  $c_1$ ; **if** and **while** are there for conditional execution and while loops. Finally, the constants **newvar**, **asg** and **der** are there for the allocation of a memory cell, for assignment and for dereferencing. The term  $\text{newvar}_\tau (\lambda x. c)$  can be understood as follows. It first allocates a new memory cell  $x$  that contains some default value of type  $\tau$ , then it executes the command  $c$ , which may make use of the cell  $x$ , and then it deallocates  $x$ . In Java, this corresponds block-scoped variables, as in  $\{\text{int } x; c\}$ . Using the term  $\text{asg} \langle x, e \rangle$  one can write the value of an expression to the cell  $x$ . The term  $\text{der } x$  is the expression that reads the value from  $x$ .

One may think of these constants as constructors for the abstract syntax of a while-language. For example, the Java program on the left below computes the factorial of an **int**-variable  $x$  (which is assumed to be declared before). It is represented by the  $\lambda$ -term on the right, in which  $x$  is a free variable of type  $\text{cell}_{\text{int}}$ . Note how the constant  $\text{newvar}_{\text{int}}$

takes a function as argument and how this is used to represent that the variable  $y$  is bound by the declaration. Definitions of Idealised Algol typically come without the definition of a concrete syntax.

```

int y;
y = 1;
while (1 <= x) {
  y = y * x;
  x = x - 1
};
x = y

```

$$\text{newvar}_{\text{int}} (\lambda y. \text{seq} \langle \text{asg}_{\text{int}} \langle y, 1 \rangle, \text{seq} \langle \text{while} \langle \text{lt} \langle 1, \text{der}_{\text{int}} x \rangle, \text{seq} \langle \text{asg}_{\text{int}} \langle y, \text{mul} \langle \text{der}_{\text{int}} y, \text{der}_{\text{int}} x \rangle, \text{asg}_{\text{int}} \langle x, \text{sub} \langle \text{der}_{\text{int}} x, 1 \rangle \rangle \rangle \rangle \rangle \rangle \rangle) : \text{com}$$

In this section we outline how constants as in Idealised Algol can be defined in  $\text{INTML}$ . The aim is to implement the constants of Idealised Algol so that the abstract syntax representation of imperative programs has the intended behaviour. Since  $\text{INTML}$  does not allow unrestricted copying of variables in the interactive language, we will not be able to embed all of Idealised Algol, of course. However, there are interesting applications of Idealised Algol even without full copying. Ghica [14] uses a restricted variant of Idealised Algol, *Basic Syntactic Control of Interference (bSCI)*, as a language for hardware synthesis. Ghica translates bSCI-programs to hardware circuits in a way that is very similar to our translation of interactive terms to base language terms. Indeed, if one removes type variables from the base language, base language programs become finite functions, which can be encoded directly in hardware. Thus,  $\text{INTML}$  can also be used as a language for hardware synthesis. In this section we show that its expressiveness is comparable to bSCI. We note that the fine-grained control of copying in  $\text{INTML}$  allows one to go beyond bSCI. For example, the Kierstead terms cannot be typed in bSCI [14], but we will show in Section 4.3 how to express them in  $\text{INTML}$ .

We now discuss how one may define an Algol-like language if in Reynolds' definition [41] one replaces the simply-typed  $\lambda$ -calculus with the interactive language of  $\text{INTML}$ . We define Algol-like program constants, the choice of which is guided by those in bSCI [14].

First, we define types for commands, expressions and memory cells by

$$\text{com} := [1] \quad , \quad \text{exp}_{\alpha} := [\alpha] \quad , \quad \text{cell}_{\alpha} := ([\alpha] \multimap \text{com}) \otimes [\alpha] \quad .$$

A command is thus represented by a thunk. Evaluation starts upon request of the thunk and termination is indicated by receipt of the value of type 1. Expressions are modelled analogously, only that the returned value now represents a value of the expression type. A memory cell is modelled as a pair of a thunk of type  $[\alpha]$ , from which the content of the cell can be requested, and an update function of type  $[\alpha] \multimap \text{com}$ , which can be used to overwrite the cell with a new value.

Constants can be defined as follows. We define curried constants, as this allows us to give them more precise subexponential types.

- Logical operations  $\text{op}$  on booleans are represented by terms of type

$$\text{op} : \text{exp}_2 \multimap 2 \cdot \text{exp}_2 \multimap \text{exp}_2 \quad .$$

For instance,  $\text{or}$  can be defined as

$$\lambda b_1. \lambda b_2. \text{let } [x_1] = b_1 \text{ in let } [x_2] = b_2 \text{ in case } x_1 \text{ of inl}(true) \Rightarrow [\text{inl}(*)] \mid \text{inr}(false) \Rightarrow [x_2] \quad .$$

Other operations on integer expressions can be represented similarly.

- For commands there are terms for a skip-command and sequencing:

$$\text{skip} : \text{com} \quad , \quad \text{seq} : \text{com} \multimap \text{com} \multimap \text{com} \quad .$$

These are defined simply by  $\text{skip} := [*]$  and

$$\text{seq} := \lambda c_1. \lambda c_2. \text{let } [x] = c_1 \text{ in let } [y] = c_2 \text{ in skip} \quad .$$



- Branching is available by means of a term

$$\text{if}: \text{exp}_2 \multimap 2 \cdot \text{com} \multimap 2 \cdot \text{com} \multimap \text{com} .$$

Its definition is

$$\text{if} := \lambda b. \lambda c_1. \lambda c_2. \text{let } [x] = b \text{ in case } x \text{ of } \text{inl}(\text{true}) \Rightarrow c_1 \\ | \text{inr}(\text{false}) \Rightarrow c_2 .$$

- For iteration we use a term

$$\text{while}: 2 \cdot \text{exp}_2 \multimap 2 \cdot \text{com} \multimap \text{com}$$

defined by

$$\lambda b. \lambda c. \text{loop } (\lambda s. \text{let } [x] = b \text{ in} \\ \text{case } x \text{ of } \text{inl}(\text{true}) \Rightarrow [\text{return}(*)] \\ | \text{inr}(\text{false}) \Rightarrow \text{let } [y] = c \text{ in } [\text{continue}(y)] \\ ) [*] .$$

- It remains to define the terms for working with the type  $\text{cell}_\alpha$  of memory cells. By definition, a value of type  $\text{cell}_\alpha$  consists of a pair of a function of type  $[\alpha] \multimap \text{com}$  for writing to the cell and a thunk of type  $[\alpha]$  for reading from it. Thus, terms

$$\text{der}: \text{cell}_\alpha \multimap \text{exp}_\alpha , \quad \text{asg}: \text{cell}_\alpha \multimap \text{exp}_\alpha \multimap \text{com}$$

for dereferencing a boolean cell and assigning to it can be defined simply by  $\text{der} := \lambda m. \text{let } \langle x, y \rangle = m \text{ in } y$  and  $\text{asg} := \lambda m. \lambda e. \text{let } \langle x, y \rangle = m \text{ in } x \ e$ . More interesting is a term  $\text{newvar}_A$ , which actually implements a memory cell of type  $A$ . It allocates a new memory cell of type  $A$  and makes it available to a given block:

$$\text{newvar}_A: A \cdot (\beta \cdot \text{cell}_A \multimap \text{com}) \multimap \text{com}.$$

Notice that  $\text{newvar}_A$  has a subexponential annotation  $A$  on its argument. The content of the memory cell will be encoded in the space given by this annotation. We implement  $\text{newvar}_A$  using rule  $\text{Hack}$ . Even though we only need the cases where  $A = 2$  or  $A = \text{int}$ , in order to make clear which assumptions on  $A$  are made, we give the implementation for an arbitrary type  $A$  with a given value  $v_A: A$  that serves as the initial value of the memory cell. We define  $\text{newvar}_A$  as  $\text{hack}(x.n)$ , where  $x: X^- \vdash n: X^+$ :

$$\begin{aligned} X &= A \cdot (\beta \cdot (([A] \multimap \text{com}) \otimes [A]) \multimap \text{com}) \multimap \text{com} \\ X^- &= A \times (\beta \times ((A + 1) + 1) + 1) + 1 \\ X^+ &= A \times (\beta \times ((1 + 1) + A) + 1) + 1 \end{aligned}$$

We implement  $n$  to satisfy the following reductions:

$$n[\text{inr}(*)/x] \mapsto_k^* \text{inl}(\langle v_A, \text{inr}(*) \rangle) \quad (11)$$

$$n[\text{inl}(\langle v, \text{inr}(*) \rangle)/x] \mapsto_k^* \text{inr}(*) \quad (12)$$

$$n[\text{inl}(\langle v, \text{inl}(\langle w, \text{inr}(*) \rangle) \rangle)/x] \mapsto_k^* \text{inl}(\langle v, \text{inl}(\langle w, \text{inr}(v) \rangle) \rangle) \quad (13)$$

$$n[\text{inl}(\langle v, \text{inl}(\langle w, \text{inl}(\text{inr}(*)) \rangle) \rangle)/x] \mapsto_k^* \text{inl}(\langle v, \text{inl}(\langle w, \text{inl}(\text{inl}(*)) \rangle) \rangle) \quad (14)$$

$$n[\text{inl}(\langle v, \text{inl}(\langle w, \text{inl}(\text{inl}(u)) \rangle) \rangle)/x] \mapsto_k^* \text{inl}(\langle u, \text{inl}(\langle w, \text{inl}(\text{inr}(*)) \rangle) \rangle) \quad (15)$$

These can be understood as follows: Reduction (11) means that when the computation of  $\text{newvar}_A \ c$  is started, then value  $v_A$  is put into the new memory cell and computation  $c$  is started. If  $c$  terminates, then, by (12), the whole computation terminates. If, during the evaluation of  $c$  the content of the memory cell is being requested, then (13) applies and the content  $v$  of the memory cell is being returned. A call of  $c$  to the update function results by (14) in an immediate request to  $c$  for the value that should be written into the cell. Once  $c$  replies with a value  $u$  this value is placed in the memory cell by (15), overwriting the previous value, and termination of the update function is indicated to  $c$ .



## 5. Compiling Interactive Terms to Base Terms

Now we explain in detail how closed interactive terms are compiled down to base language terms, so that whole INTML-programs can be evaluated using base language reduction alone. The compilation works by interpreting interactive terms as message passing circuits and then implementing these circuits by base language terms. The construction does not depend on the bit-width of integers. In the following section we fix a bit-width  $k$  and consider all reduction with respect to this bit-width. We note that the translation from interactive terms to base terms does not depend on  $k$ , i.e. for any choice of  $k$  it produces the same base term.

### 5.1. Circuits

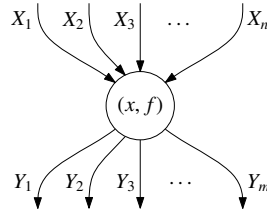
We start by defining the message passing circuits that serve as an intermediate language for the compilation. These circuits implement bidirectional message passing, as explained in the Introduction. Circuits may also be understood as a particular instance of string diagrams for monoidal categories [34]. They are also related to proof-nets, see [34] for a discussion.

Circuits are directed graphs that represent message passing networks. The edges are communication channels along which base language values can be passed. Each edge is labelled with a pair  $(X^-, X^+)$  of base language types. This label specifies that values of type  $X^+$  may be passed from source to target of the edge, while messages of type  $X^-$  may be passed in the opposite direction.

The nodes of a circuit implement the processing and passing on of messages. At any time, only a single message travels in a circuit. When it arrives at a node, it is processed there and as a result a new message may be sent from this node to continue the message passing process. To define the behaviour of the nodes of a circuit, each node is labelled with a base language term that is used to process incoming messages. When a base language value arrives as a message along some edge, the term is used to compute a response, which is then passed on over some edge.

For the formal definition of circuits, we need the notion of a *locally ordered graph*. A local ordering for a graph  $G = (V, E)$  specifies for each node  $v \in V$  a total ordering on both the set  $(V \times \{v\}) \cap E$  of incoming edges to  $v$  and on the set  $(\{v\} \times V) \cap E$  of outgoing edges from  $v$ . The local ordering is used to distinguish edges with the same label, for example in nodes like  $\otimes E$  below.

**Definition 4.** A *circuit over  $\Gamma$*  is a locally ordered directed graph in which each node is labelled with a pair  $(x, f)$  of a base language term  $f$  and a variable  $x$  and each edge is labelled with a pair of base language types. This labelling must be such that if any node is labelled with  $(x, f)$ , its incoming edges are labelled with  $X_1, X_2, \dots, X_n$  and its outgoing edges are labelled with  $Y_1, Y_2, \dots, Y_m$ ,

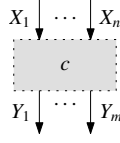


then the following base language typing judgement must be derivable:

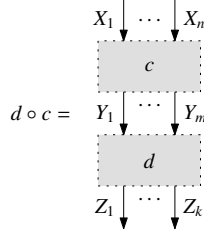
$$\Gamma, x: Y_1^- + \dots + Y_m^- + X_1^+ + \dots + X_n^+ \vdash f : Y_1^+ + \dots + Y_m^+ + X_1^- + \dots + X_n^-$$

Here,  $X^-$  and  $X^+$  denote the first and second component of the pair  $X$  respectively.

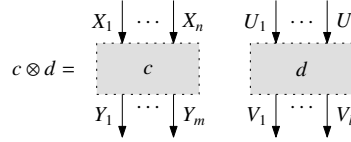
For each interactive type  $X$ , we have defined a pair of base language types  $X^-$  and  $X^+$  in Section 2.2. We will use an interactive type  $X$  to stand for the edge label  $(X^-, X^+)$ . It is useful to allow circuits to have a number of input and output ports. We formalise these by means of two distinguished nodes: a source and a sink. Edges from the distinguished source are input edges and edges to the sink are output edges. We write  $c: [X_1, \dots, X_n] \rightarrow [Y_1, \dots, Y_m]$  for such a circuit  $c$  with input edges of type  $X_1, \dots, X_n$  and output edges of type  $Y_1, \dots, Y_m$  (note that they are ordered because of the local ordering for source and sink). We usually draw  $c$  as shown below.



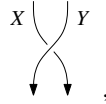
Given two circuits  $c: [X_1, \dots, X_n] \rightarrow [Y_1, \dots, Y_m]$  and  $d: [Y_1, \dots, Y_m] \rightarrow [Z_1, \dots, Z_k]$ , their sequential composition  $d \circ c: [X_1, \dots, X_n] \rightarrow [Z_1, \dots, Z_k]$  is defined as depicted below: the  $i$ -th incoming edge to the sink of  $c$  and the  $i$ -th outgoing edge from the source of  $d$  are joined to a single edge. The sink of  $c$  and the source of  $d$  are removed.



Given  $c: [X_1, \dots, X_n] \rightarrow [Y_1, \dots, Y_m]$  and  $d: [U_1, \dots, U_k] \rightarrow [V_1, \dots, V_l]$ , we write  $c \otimes d$  for the circuit obtained by putting  $c$  and  $d$  in parallel.



Notice that in a composition the local ordering on the intermediate edges (the ones labelled above with  $Y_1, \dots, Y_m$ ) disappears. For example, if we let  $swap_{X,Y}: [X, Y] \rightarrow [Y, X]$  be the circuit



then  $swap_{Y,X} \circ swap_{X,Y}$  is  $id_X \otimes id_Y: [X, Y] \rightarrow [X, Y]$ , where  $id_X: [X] \rightarrow [X]$  is a single edge from input to output with label  $X$ .

## 5.2. Message Passing

Let us now make precise the message passing in circuits. Write  $\mathcal{V}_A$  for the set of all closed base language values of type  $A$  with respect to bit-width  $k$  (recall the we have fixed  $k$  at the beginning of this section). Write  $\mathcal{E}_\Gamma$  for the set of  $\Gamma$ -environments consisting of all functions that map the variables in  $\Gamma$  to closed values of their declared types. Given  $\sigma \in \mathcal{E}_\Gamma$  and a term  $\Gamma \vdash f: A$ , we write  $f[\sigma]$  for the closed term obtained by simultaneous substitution with the assignments in  $\sigma$ .

The set  $M_{\Gamma,X}$  of messages that can be passed along an edge labelled with  $X$  in a circuit over  $\Gamma$  is defined by  $M_{\Gamma,X} = \mathcal{E}_\Gamma \times (\mathcal{V}_{X^+} \times \{+\} \cup \mathcal{V}_{X^-} \times \{-\})$ . A message  $w \in M_{\Gamma,X}$  is either a *question* or an *answer* depending on whether its third component is ‘-’ or ‘+’. Answers travel in the direction of the edge, while questions travel in the opposite direction. Messages are essentially the same as the contexts in context semantics [33].

To define message passing for a circuit  $c$  on  $\Gamma$ , let the set  $M_c$  of messages on  $c$  consist of all pairs  $(e, w)$  of an edge  $e$  in  $c$  and a message  $w \in M_{\Gamma,X(e)}$ , where  $X(e)$  is the label of  $e$ . First, we define how each node in  $c$  locally reacts to arriving messages. For each node  $v$  we define a partial function  $\chi_v: M_c \rightarrow M_c$  as follows: Let  $(x, f)$  be the label of  $v$  and let  $i_1, \dots, i_n$  and  $o_1, \dots, o_m$  be the incoming and outgoing edges connected to  $v$  respectively. Recall that  $f$  must by definition have type

$$\Gamma, x: X(o_1)^- + \dots + X(o_m)^- + X(i_1)^+ + \dots + X(i_n)^+ \vdash \\ f: X(o_1)^+ + \dots + X(o_m)^+ + X(i_1)^- + \dots + X(i_n)^- .$$

The function  $\chi_v$  is then defined by

$$\chi_v(o_k, (\sigma, w, -)) = \begin{cases} (o_{k'}, (\sigma, w', +)) & \text{if } f[\sigma][\text{in}_k(w)/x] \mapsto_k^* \text{in}_{k'}(w') \text{ and } k' \leq m, \\ (i_{k'-m}, (\sigma, w', -)) & \text{if } f[\sigma][\text{in}_k(w)/x] \mapsto_k^* \text{in}_{k'}(w') \text{ and } k' > m, \end{cases}$$

$$\chi_v(i_k, (\sigma, w, +)) = \begin{cases} (o_k, (\sigma, w', +)) & \text{if } f[\sigma][\text{in}_{m+k}(w)/x] \mapsto_k^* \text{in}_{k'}(w') \text{ and } k' \leq m, \\ (i_{k'-m}, (\sigma, w', -)) & \text{if } f[\sigma][\text{in}_{m+k}(w)/x] \mapsto_k^* \text{in}_{k'}(w') \text{ and } k' > m, \end{cases}$$

where we write  $\text{in}_k$  as a short-hand for the injection into the  $k$ -th summand of a sum type.

The message passing behaviour of the circuit as a whole is the partial function  $\chi_c: M_c \rightarrow M_c$  defined by repeatedly applying the local functions  $\chi_v$  for all nodes  $v$ :

$$\chi_c = Tr\left(\bigcup_{v \in V(c)} \chi_v\right) \quad Tr(f)(w) = \begin{cases} Tr(f)(f(w)) & \text{if } f(w) \text{ defined,} \\ w & \text{if } f(w) \text{ undefined.} \end{cases}$$

It is not hard to see that message passing cannot end at an internal edge, i.e.  $\chi_c(e, w) = (e', w')$  implies that  $e'$  is an input or an output edge. In fact, we will forget about the internal structure of  $c$  and consider just the restriction of  $\chi_c$  to messages on input or output edges of  $c$ . For a circuit  $c: [X_1, \dots, X_n] \rightarrow [Y_1, \dots, Y_m]$ , this restriction yields a partial function  $\hat{\chi}_c$  of type

$$\mathcal{E}_\Gamma \times \mathcal{V}_{Y_1^- + \dots + Y_m^- + X_1^+ + \dots + X_n^+} \longrightarrow \mathcal{V}_{Y_1^+ + \dots + Y_m^+ + X_1^- + \dots + X_n^-}.$$

We call  $\hat{\chi}_c$  the behaviour of  $c$  and consider circuits with the same behaviour to be equal.

**Definition 5** (Equality of Circuits). Two circuits  $c$  and  $d$  of type  $[X_1, \dots, X_n] \rightarrow [Y_1, \dots, Y_m]$  over  $\Gamma$  are *equal* if their behaviour is the same, i.e. if  $\hat{\chi}_c = \hat{\chi}_d$  holds. We write  $c = d$  for equality of circuits.

### 5.3. Implementing Message Passing

The message passing behaviour of any circuit can be implemented by a base language term. For any circuit  $c: [X_1, \dots, X_n] \rightarrow [Y_1, \dots, Y_m]$  we can construct a base language term  $\hat{c}$  that implements the behaviour  $\hat{\chi}_c$  in the following sense. Its type is

$$\Gamma, x: Y_1^- + \dots + Y_m^- + X_1^+ + \dots + X_n^+ \vdash \hat{c}: Y_1^+ + \dots + Y_m^+ + X_1^- + \dots + X_n^-$$

and  $\hat{\chi}_c(\sigma, v) = w$  holds if and only if  $\hat{c}[\sigma][v/x] \mapsto_k^* w$  does.

In essence, the construction of  $\hat{c}$  works in the same way as the definition of  $\hat{\chi}_c$  above. First note that we can represent the set of messages  $M_{\Gamma, X}$  by the base language type  $(A_1 \times \dots \times A_n) \times (X^- + X^+)$ , where  $\Gamma$  is  $x_1: A_1, \dots, x_n: A_n$ . Then,  $M_c$  can be represented in the base language by a big sum type and the function  $\bigcup_{v \in V(c)} \chi_v: M_c \rightarrow M_c$  can be implemented easily by a big case distinction. Using a single loop one can construct from this a base language term that implements  $\hat{\chi}_c$ .

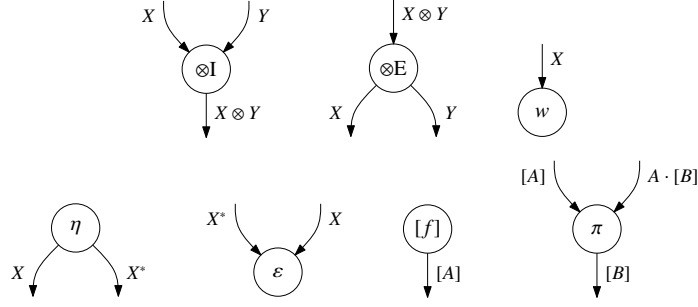
### 5.4. Constructing Circuits

To organise the compilation of interactive terms to circuits and the soundness proof, we next identify some structure of circuits. First we observe that sequential and parallel composition have the expected properties. Recall that the equalities in these lemmas mean that the circuits have the same message-passing behaviour, cf. Definition 5.

**Lemma 3.** *The equations  $c = c \circ id = id \circ c$  and  $(c \circ d) \circ e = c \circ (d \circ e)$  hold for all circuits  $c, d$  and  $e$  for which the terms in these equations are defined.*

**Lemma 4.** *The equations  $id \otimes id = id$  and  $(c \otimes d) \circ (c' \otimes d') = (c \circ c') \otimes (d \circ d')$  hold for all circuits  $c, c', d$  and  $d'$  for which the terms in these equations are defined.*

For the construction of circuits, we use a set of predefined nodes. We define the following nodes



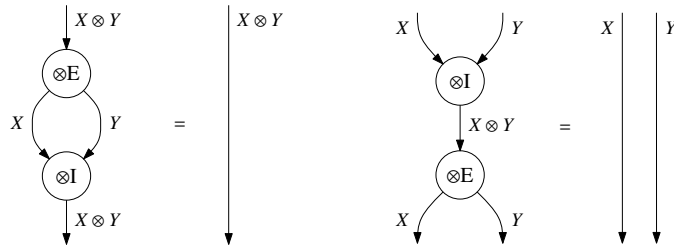
over  $\Gamma$  for any  $X, Y, A$  and  $B$  and all  $f$  with  $\Gamma \vdash f : A$ . Rather than spelling out the base language terms for these nodes explicitly, we just specify their (very simple) message-passing behaviour and note that it is easily implemented. The behaviour of the above nodes is specified as the least functions satisfying the following equations. In these equations we write  $i_1$  and  $i_2$  for the incoming edges of a node (ordered from left to right) and likewise  $o_1$  and  $o_2$  for the outgoing edges (in the same order). There is no equation for  $\chi_w$ , as this must be the empty function.

$$\begin{aligned}
\chi_{\otimes I}(i_1, (\sigma, v, +)) &= (o_1, (\sigma, \text{inl}(v), +)) & \chi_{\otimes E}(i_1, (\sigma, \text{inl}(v), +)) &= (o_1, (\sigma, v, +)) \\
\chi_{\otimes I}(i_2, (\sigma, v, +)) &= (o_1, (\sigma, \text{inr}(v), +)) & \chi_{\otimes E}(i_2, (\sigma, \text{inr}(v), +)) &= (o_2, (\sigma, v, +)) \\
\chi_{\otimes I}(o_1, (\sigma, \text{inl}(v), -)) &= (i_1, (\sigma, v, -)) & \chi_{\otimes E}(o_1, (\sigma, v, -)) &= (i_1, (\sigma, \text{inl}(v), -)) \\
\chi_{\otimes I}(o_2, (\sigma, \text{inr}(v), -)) &= (i_2, (\sigma, v, -)) & \chi_{\otimes E}(o_2, (\sigma, v, -)) &= (i_2, (\sigma, \text{inr}(v), -)) \\
\chi_{\eta}(o_1, (\sigma, v, -)) &= (o_2, (\sigma, v, +)) & \chi_{\varepsilon}(i_1, (\sigma, v, +)) &= (i_2, (\sigma, v, -)) \\
\chi_{\eta}(o_2, (\sigma, v, -)) &= (o_1, (\sigma, v, +)) & \chi_{\varepsilon}(i_2, (\sigma, v, +)) &= (i_1, (\sigma, v, -)) \\
\chi_{[f]}(o_1, (\sigma, *, -)) &= (o_1, (\sigma, v, +)) \text{ if } f[\sigma] \mapsto_k^* v \text{ and } v \text{ is a value} \\
\chi_{\pi}(i_1, (\sigma, v, +)) &= (i_2, (\sigma, (v, *), -)) \\
\chi_{\pi}(i_2, (\sigma, (v, w), +)) &= (o_1, (\sigma, w, +)) \\
\chi_{\pi}(o_1, (\sigma, *, -)) &= (i_1, (\sigma, *, -))
\end{aligned}$$

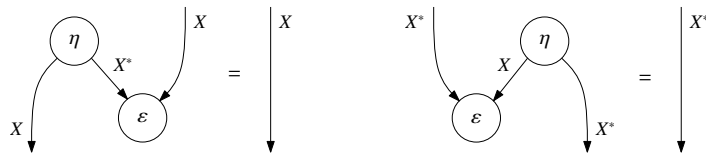
Informally, the nodes  $\otimes I$  and  $\otimes E$  serve for packing two message passing wires into one. Node  $w$  discards all messages; it is used for weakening. Nodes  $\eta$  and  $\varepsilon$  serve to exchange the direction of messages; we use them to implement functions. Node  $\pi$  is useful for sequencing thunks. If a question arrives at its output, this node queries its left input. If an answer  $v$  arrives from this input, node  $\pi$  queries its second input and passes the value  $v$  along with the query. The second input thus gets access to the result of the first input.

The following lemmas are immediate.

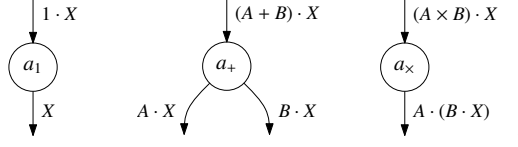
**Lemma 5.**



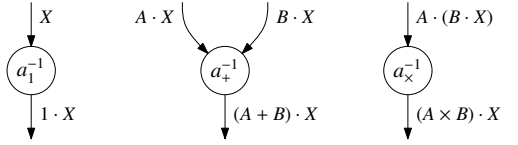
**Lemma 6.**



In addition to the general nodes defined above, we also need *administrative nodes* to deal with the subexponentials. A subexponential can be understood as an iterated multiplication. We have administrative nodes that correspond to the familiar high-school identities for exponentiation  $x^1 = x$ ,  $x^{a+b} = x^a \cdot x^b$  and  $x^{a \cdot b} = (x^b)^a$



along with their inverses

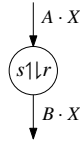


The behaviour of these nodes is given by the canonical terms of the respective type. We spell out the behaviour of  $a_+$  and  $a_\times$ :

$$\begin{aligned} \chi_{a_+}(i_1, (\sigma, (\mathbf{inl}(v), w), +)) &= (o_1, (\sigma, (v, w), +)) \\ \chi_{a_+}(i_1, (\sigma, (\mathbf{inr}(v), w), +)) &= (o_2, (\sigma, (v, w), +)) \\ \chi_{a_+}(o_1, (\sigma, (v, w), -)) &= (i_1, (\sigma, (\mathbf{inl}(v), w), -)) \\ \chi_{a_+}(o_2, (\sigma, (v, w), -)) &= (i_1, (\sigma, (\mathbf{inr}(v), w), -)) \\ \chi_{a_\times}(i_1, (\sigma, ((u, v), w), +)) &= (o_1, (\sigma, (u, (v, w)), +)) \\ \chi_{a_\times}(o_1, (\sigma, (u, (v, w)), -)) &= (i_1, (\sigma, ((u, v), w), -)) \end{aligned}$$

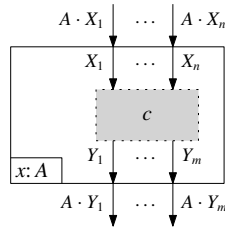
For the reader familiar with Linear Logic, nodes  $a_1$ ,  $a_+$  and  $a_\times$  play a role similar to dereliction, contraction and digging nodes (respectively) in proof-nets for the exponentials.

For the manipulation of subexponentials, we use a reindexing node. For any two terms  $\Gamma, b: B \vdash s: A$  and  $\Gamma, a: A \vdash r: B$  we have a node



over  $\Gamma$ . This node modifies just the index in the subexponential. When a message travels from top to bottom,  $r$  is applied to the index; in the other direction  $s$  is applied. We shall use the reindexing node in particular to interpret rule **STRUCT**. In this case  $s$  and  $r$  will be a section-retraction pair.

Finally, we use a box construction that allows us to bind base language variables in circuits. Given a circuit  $c: [X_1, \dots, X_n] \rightarrow [Y_1, \dots, Y_m]$  over  $(\Gamma, x: A)$ , we define a circuit  $(x: A) \cdot c: [A \cdot X_1, \dots, A \cdot X_n] \rightarrow [A \cdot Y_1, \dots, A \cdot Y_m]$  over  $\Gamma$ , which we depict graphically as follows:



Informally, when a value  $(v, w)$  arrives from the outside to the box, the value  $v$  is bound to the variable  $x$  and the message  $w$  is passed into the box. If a message  $w'$  reaches the border from inside, then the value  $v$  is retrieved again by looking up the variable  $x$  in the environment and then  $(v, w')$  travels out from the box. The value of  $x$  does not change inside the box.

More formally, the behaviour of  $(x : A) \cdot c$  can be specified as follows. Let  $e_1, \dots, e_{m+n}$  be the list of input and output edges of  $(x : A) \cdot c$ , in which first all the input edges appear in their order and then the output edges. Let  $e'_1, \dots, e'_{m+n}$  be the input and output edges of  $c$ , listed in the same way. The behaviour of  $(x : A) \cdot c$  is then specified by:  $\hat{\chi}_{((x:A) \cdot c)}(\sigma, (v, u), p) = (e_j, (\rho, (v, w), r))$  if and only if  $\hat{\chi}_c(e'_i, (\sigma[v/x], u, p)) = (e'_j, (\tau, w, r))$  and  $\rho$  is the restriction of  $\tau$  to variables appearing in  $\Gamma$ . Concretely, such a circuit  $(x : A) \cdot c$  can be constructed simply as a single node, obtained from implementation of the behaviour  $\hat{\chi}_c$ .

The variable  $x$  should be considered being bound in the above box; we have the usual  $\alpha$ -conversion law  $(x : A) \cdot c = (y : A) \cdot c[y/x]$  for any fresh variable  $y$ . Here,  $c[y/x]$  denotes substitution of  $y$  for  $x$  in each node in  $c$ . We write  $A \cdot c$  instead of  $(x : A) \cdot c$  if  $x$  does not appear free in any of the nodes in  $c$ .

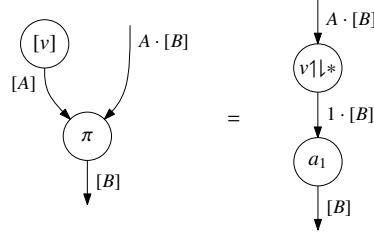
Boxes are easily seen to have the following properties:

**Lemma 7.** *The equations  $A \cdot id = id$ ,  $(x : A) \cdot (c \circ d) = ((x : A) \cdot c) \circ ((x : A) \cdot d)$  and  $(x : A) \cdot (c \otimes d) = ((x : A) \cdot c) \otimes ((x : A) \cdot d)$  hold for all circuits  $c$  and  $d$ , all variables  $x$  and all types  $A$  for which the circuits in the equations are well-defined.*

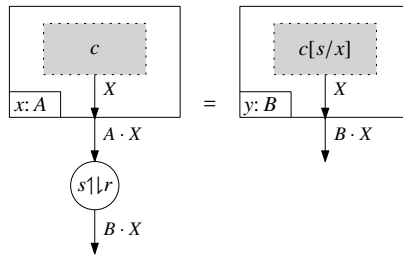
Having introduced all the building blocks for the construction of circuits, we state a few results that allow us to prove soundness of the translation from the interactive to the base language.

**Lemma 8.** *For any circuit  $c$  with one outgoing edge and no incoming edges, we have  $a_1 \circ (1 \cdot c) = c$  and  $a_+ \circ ((A + B) \cdot c) = (A \cdot c) \otimes (B \cdot c)$  and  $a_\times \circ ((A \times B) \cdot c) = A \cdot (B \cdot c)$ .*

**Lemma 9.** *For any value  $\vdash v : A$ , the following equality of circuits holds.*

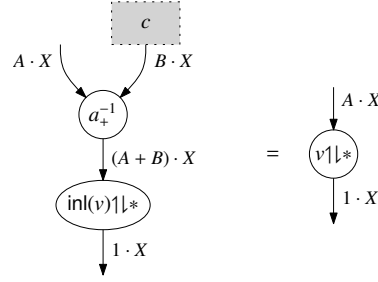


**Lemma 10.** *Let  $\Gamma, y : B \vdash s : A$  and  $\Gamma, x : A \vdash r : B$  be a section-retraction pair. For any circuit  $c$  over  $\Gamma, x : A$  with no inputs and a single output labelled  $X$  we have:*

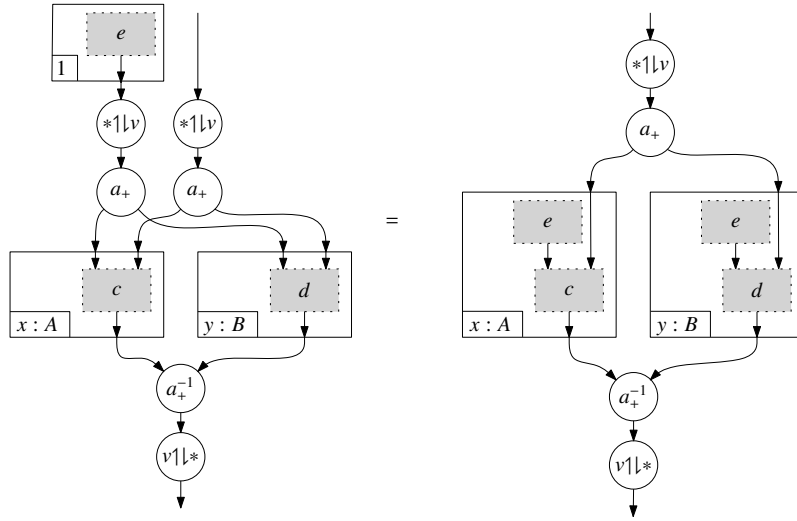


**Lemma 11.** *For any circuit  $c : [] \rightarrow [B \cdot X]$  and any value  $\vdash v : A$ , the following equality of circuits holds.*





**Lemma 12.** For all circuits  $c: [X, Y] \rightarrow [Z]$  over  $\Gamma$ ,  $x: A$  and  $d: [X, Y] \rightarrow [Z]$  over  $\Gamma$ ,  $y: B$  and  $e: [] \rightarrow X$  over  $\Gamma$ , and all values  $\Gamma \vdash v: A + B$ , the following is true:



*Proof.* Consider what form a message can have that leaves the box containing  $c$  over an incoming edge. Because all input and output edges of the circuit are guarded by  $(*\Downarrow v)$  and  $(v\Downarrow *)$ , such a message must have the form  $(\sigma, (w_1, w_2), -)$  where  $\text{inl}(w_1) = v[\sigma]$ . In this case, the message  $(\sigma, w_2, -)$  will be passed to the circuit  $e$ . An answer  $(\sigma, w_3, +)$  from  $e$  will result in  $(\sigma, (w_1, w_2), +)$  being passed into the box containing  $c$ . This process can be shortcut by moving  $e$  inside the box containing  $c$ . A similar argument applies to the box containing  $d$ .  $\square$

### 5.5. Translating Interactive Terms to Circuits

We now interpret interactive terms by circuits. To each derivation  $\delta$  ending with sequent  $\Gamma \mid x_1: A_1 \cdot X_1, \dots, x_n: A_n \cdot X_n \vdash s: Y$  we assign a circuit  $\llbracket \delta \rrbracket: [A_1 \cdot X_1, \dots, A_n \cdot X_n] \rightarrow [Y]$  on  $\Gamma$ . Each variable declaration in the interactive context thus becomes an input wire of the translated circuit.

The definition of  $\llbracket \delta \rrbracket$  goes by induction on derivations and is given in Fig. 10. Therein we use the following notation: We denote the premises of  $\delta$  by  $\delta_s$  and  $\delta_t$  depending on the term in the premise. Given an interactive context  $\Phi = x_1: A_1 \cdot X_1, \dots, x_n: A_n \cdot X_n$ , we write  $\Phi$  also for the list  $[A_1 \cdot X_1, \dots, A_n \cdot X_n]$  of input/output wires of a circuit. We write  $\text{id}_\Phi$  for  $(\text{id} \otimes \dots \otimes \text{id}): \Phi \rightarrow \Phi$  and use similar notation for the evident circuits  $(a_+)_\Phi: (A + B) \cdot \Phi \rightarrow (A \cdot \Phi, B \cdot \Phi)$ ,  $(a_\times)_\Phi: (A \times B) \cdot \Phi \rightarrow A \cdot B \cdot \Phi$ , etc.

To make it easier to read the interpretation of the rules, we spell out the more complicated cases in graphical form in Figs. 11–15. We give full details, including all edge labels, in the case of  $\otimes E$  in Fig. 11, and omit the edge labels otherwise. For well-definedness of the interpretation, notice that the side condition on rule **STRUCT** ensures that it is always possible to choose a section-retraction pair. As an example we show in Fig. 16 the circuit that is obtained by interpreting the derivation of the typing judgement  $\vdash \lambda f. \lambda y. \text{let } [x] = y \text{ in } f [x] [x]: \alpha \cdot ([\alpha] \multimap [\alpha] \multimap [\beta]) \multimap [\alpha] \multimap [\beta]$  from Section 2.3.

AX	$\llbracket \delta \rrbracket = a_1$
STRUCT	$\llbracket \delta \rrbracket = \llbracket \delta_r \rrbracket \circ (id_\Phi \otimes (s \upharpoonright r))$ , where $\Gamma, x: A \vdash s: B$ and $\Gamma, y: B \vdash r: A$ are an arbitrarily chosen section-retraction pair witnessing $A \triangleleft B$ .
WEAK	$\llbracket \delta \rrbracket = \llbracket \delta_r \rrbracket \circ (id_\Phi \otimes w)$
EXCH	$\llbracket \delta \rrbracket = \llbracket \delta_r \rrbracket \circ (id_\Phi \otimes swap \otimes id_\Psi)$
COPY	$\llbracket \delta \rrbracket = \llbracket \delta_r \rrbracket \circ (id_\Psi \otimes (a_+ \circ ((A + B) \cdot \llbracket \delta_s \rrbracket)) \circ (a_\times)_\Phi)$
[ ]I	$\llbracket \delta \rrbracket = \llbracket f \rrbracket$
[ ]E	$\llbracket \delta \rrbracket = \pi \circ (\llbracket \delta_s \rrbracket \otimes ((x: A) \cdot \llbracket \delta_r \rrbracket) \circ (a_\times)_\Psi)$
⊗I	$\llbracket \delta \rrbracket = \otimes I \circ (\llbracket \delta_s \rrbracket \otimes \llbracket \delta_r \rrbracket)$
⊗E	$\llbracket \delta \rrbracket = \llbracket \delta_r \rrbracket \circ (id_\Psi \otimes ((A \cdot \llbracket \delta_s \rrbracket) \circ (a_\times)_\Phi))$
→I	$\llbracket \delta \rrbracket = \otimes I \circ swap \circ (\llbracket \delta_r \rrbracket \otimes id_X) \circ (id_\Psi \otimes \eta)$
→E	$\llbracket \delta \rrbracket = (id_Y \otimes \varepsilon) \circ ((swap \circ \otimes E \circ \llbracket \delta_r \rrbracket) \otimes ((A \cdot \llbracket \delta_s \rrbracket) \circ (a_\times)_\Phi))$
+E <sup>c</sup>	$\llbracket \delta \rrbracket = a_1 \circ (v \upharpoonright *) \circ a_+^{-1} \circ (((x: A) \cdot \llbracket \delta_s \rrbracket) \otimes ((y: B) \cdot \llbracket \delta_r \rrbracket)) \circ (a_+)_\Phi \circ (* \upharpoonright v)_\Phi \circ (a_1^{-1})_\Phi$
HACK	$\llbracket \delta \rrbracket$ is the single node with label $(x, f)$ .

Figure 10: Interpretation of Interactive Derivations

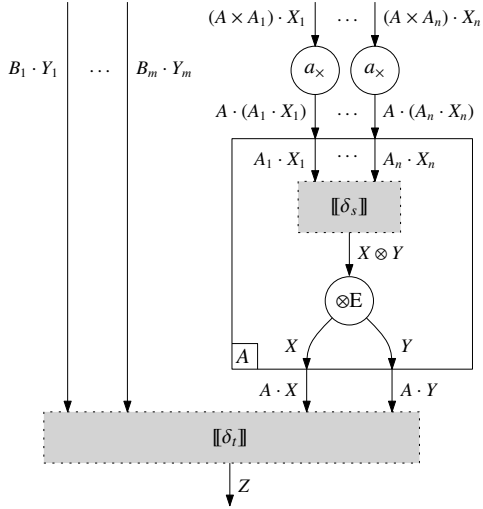


Figure 11: Circuit for  $\otimes E$

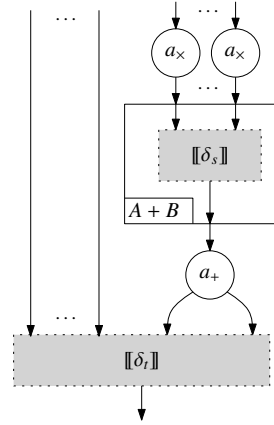


Figure 12: Circuit for Copy

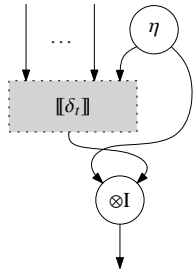


Figure 13: Circuit for  $\neg oI$

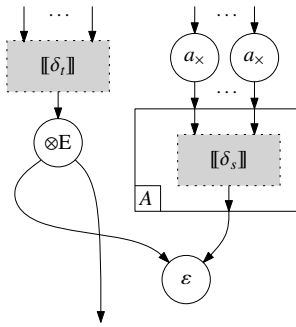


Figure 14: Circuit for  $\neg oE$

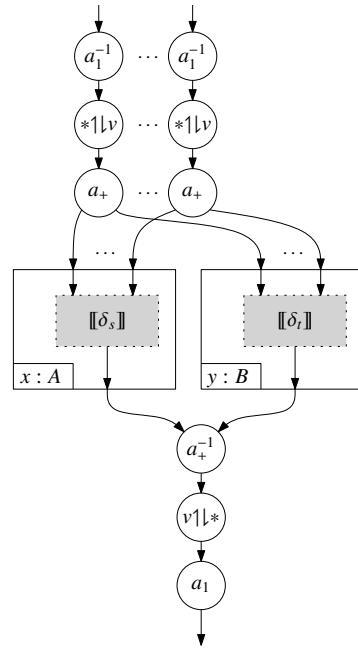


Figure 15: Circuit for  $+E^c$

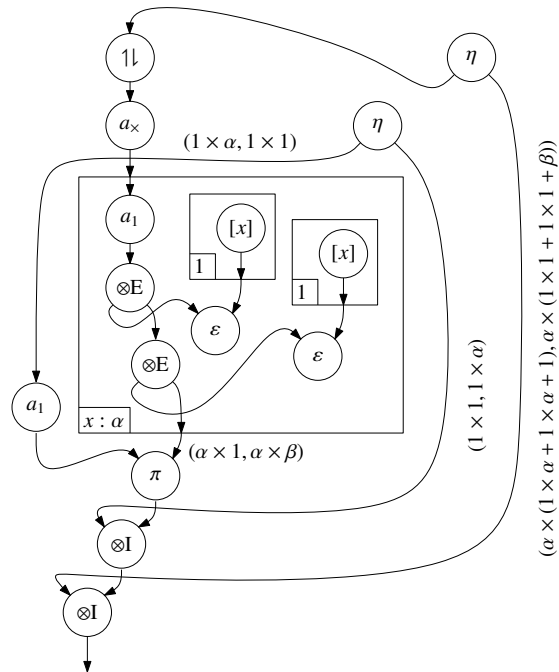


Figure 16: Example Circuit

### 5.6. Soundness

Having defined a translation from interactive terms to base language ones by translation to circuits and their implementation, it now remains to show soundness of the translation with respect to the reduction semantics from Section 2.2. We prove the following result:

**Theorem 13** (Soundness). *If  $\delta$  derives  $\cdot \mid \cdot \vdash s : X$  and  $s \mapsto_k t$ , then there exists a derivation  $\rho$  of  $\cdot \mid \cdot \vdash t : X$  with  $\llbracket \delta \rrbracket = \llbracket \rho \rrbracket$ .*

For the proof of the soundness theorem we need substitution lemmas.

**Lemma 14** (Substitution). *If  $\rho$  derives  $\Gamma \mid \Phi, x : A \cdot X, \Psi \vdash t : Y$  and  $\delta$  derives  $\Gamma \mid \cdot \vdash s : X$ , then there exists a derivation  $\rho[\delta/x]$  of  $\Gamma \mid \Phi, \Psi \vdash t[s/x] : Y$  that satisfies  $\llbracket \rho[\delta/x] \rrbracket = \llbracket \rho \rrbracket \circ (id_\Phi \otimes (A \cdot \llbracket \delta \rrbracket)) \otimes id_\Psi$ .*

*Proof.* The proof goes by induction on  $\rho$ . We consider representative cases:

- **AX:** If  $\rho$  ends in an application of **AX** and thus derives  $x : 1 \cdot Y \vdash x : Y$ , then we have  $\llbracket \rho \rrbracket = a_1$ . We let  $\rho[\delta/x] = \delta$ . We have  $\llbracket \rho[\delta/x] \rrbracket = \llbracket \delta \rrbracket = a_1 \circ (1 \cdot \llbracket \delta \rrbracket) = \llbracket \rho \rrbracket \circ (1 \cdot \llbracket \delta \rrbracket)$ , as required.
- **STRUCT:** If  $\rho$  ends with an application of **STRUCT**, two cases are possible:
  1. The context  $\Psi$  is empty and  $\rho$  has the form:

$$\text{STRUCT} \frac{\rho_1 : \Gamma \mid \Phi, x : B \cdot X \vdash t : Y}{\Gamma \mid \Phi, x : A \cdot X \vdash t : Y} B \triangleleft A$$

In this case  $\llbracket \rho \rrbracket$  is  $\llbracket \rho_1 \rrbracket \circ (id_\Phi \otimes (s \mid r))$  for some section-retraction pair  $s$  and  $r$  from  $B$  to  $A$ . We let  $\rho[\delta/x]$  be  $\rho_1[\delta/x]$ . We have:

$$\begin{aligned} \llbracket \rho[\delta/x] \rrbracket &= \llbracket \rho_1[\delta/x] \rrbracket = \llbracket \rho_1 \rrbracket \circ (id_\Phi \otimes (B \cdot \llbracket \delta \rrbracket)) && \text{by defn. and i.h.} \\ &= \llbracket \rho_1 \rrbracket \circ (id_\Phi \otimes ((s \mid r) \circ (A \cdot \llbracket \delta \rrbracket))) && \text{by Lemma 10} \\ &= \llbracket \rho_1 \rrbracket \circ (id_\Phi \otimes (s \mid r)) \circ (id_\Phi \otimes (A \cdot \llbracket \delta \rrbracket)) \\ &= \llbracket \rho \rrbracket \circ (id_\Phi \otimes (A \cdot \llbracket \delta \rrbracket)) \end{aligned}$$

2. The context  $\Psi$  has the form  $\Psi_1, y : D \cdot Z$  and  $\rho$  has the form:

$$\text{STRUCT} \frac{\rho_1 : \Gamma \mid \Phi, x : A \cdot X, \Psi_1, y : C \cdot Z \vdash t : Y}{\Gamma \mid \Phi, x : A \cdot X, \Psi_1, y : D \cdot Z \vdash t : Y} C \triangleleft D$$

In this case we let  $\rho[\delta/x]$  be:

$$\text{STRUCT} \frac{\rho_1[\delta/x] : \Gamma \mid \Phi, \Psi_1, y : C \cdot Z \vdash t[s/x] : Y}{\Gamma \mid \Phi, \Psi_1, y : D \cdot Z \vdash t[s/x] : Y} C \triangleleft D$$

With this choice we have

$$\begin{aligned} \llbracket \rho[\delta/x] \rrbracket &= \llbracket \rho_1[\delta/x] \rrbracket \circ (id_{(\Phi, \Psi_1)} \otimes (s \mid r)) \\ &= \llbracket \rho_1 \rrbracket \circ (id_\Phi \otimes \llbracket \delta \rrbracket \otimes id_{(\Psi_1, y : C \cdot Z)}) \circ (id_{(\Phi, \Psi_1)} \otimes (s \mid r)) \\ &= \llbracket \rho_1 \rrbracket \circ (id_{(\Phi, x : A \cdot X, \Psi_1)} \otimes (s \mid r)) \circ (id_\Phi \otimes \llbracket \delta \rrbracket \otimes id_{(\Psi_1, y : D \cdot Z)}) \\ &= \llbracket \rho \rrbracket \circ (id_\Phi \otimes \llbracket \delta \rrbracket \otimes id_{(\Psi_1, y : D \cdot Z)}), \end{aligned}$$

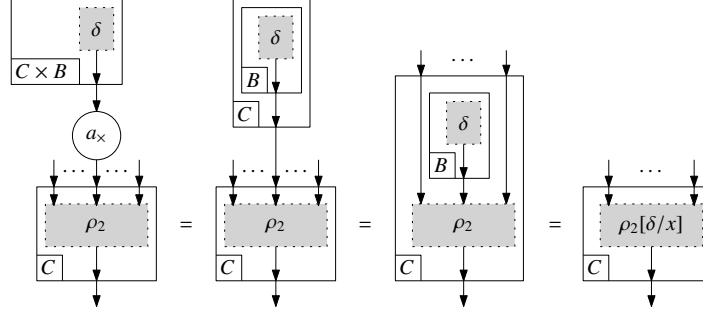
as was required to show.

- The cases for the other structural rules follow by similar arguments.
- $\multimap$ E: Suppose  $\rho$  ends in an application of rule  $\multimap$ E:

$$\multimap\text{E} \frac{\rho_1 : \Gamma \mid \Phi_1 \vdash t : C \cdot Z \multimap Y \quad \rho_2 : \Gamma \mid \Phi_2 \vdash s : Z}{\Gamma \mid \Phi_1, C \cdot \Phi_2 \vdash t s : Y}$$

The context  $\Phi_1, C \cdot \Phi_2$  must be the same as  $\Phi, x : A \cdot X, \Psi$ . If the declaration  $x : A \cdot X$  appears in  $\Phi_1$ , then the assertion follows directly by applying the induction hypothesis to  $\rho_1$ .

Suppose then that  $x : A \cdot X$  appears in  $C \cdot \Phi_2$ . In this case,  $x : B \cdot X$  appears in  $\Phi_2$  for some  $B$  and we have  $A = C \times B$ . We define  $\rho[\delta/x]$  to be the derivation obtained by applying  $\multimap E$  to  $\rho_1$  and  $\rho_2[\delta/x]$ . We have



by Lemmas 8 and 7 and the induction hypothesis. But now the leftmost circuit in the above equation appears in  $\llbracket \rho \rrbracket \circ (id_\Phi \otimes (A \cdot \llbracket \delta \rrbracket)) \otimes id_\Psi$ . If we replace it by the rightmost circuit in the equation, then we obtain just  $\llbracket \rho[\delta/x] \rrbracket$ . We have thus shown the required equality.

- The cases for rules  $\multimap I$ ,  $\otimes I$ ,  $\otimes E$  and  $[\ ]E$  follow by similar arguments.
- The rules  $[\ ]I$ ,  $0E$  and  $HACK$  cannot be the conclusion of  $\rho$ , as they all have an empty interactive context.
- If  $\rho$  ends with an application of  $+E^c$

$$+E^c \frac{\Gamma \vdash f : B + C \quad \rho_1 : \Gamma, x : B \mid \Theta \vdash s : Y \quad \rho_2 : \Gamma, y : C \mid \Theta \vdash t : Y}{\Gamma \mid \Theta \vdash \text{case } f \text{ of } \text{inl}(x) \Rightarrow s \mid \text{inr}(y) \Rightarrow t : Y}$$

then we use Lemma 12 to move  $\llbracket \delta \rrbracket$  into the two inner boxes for  $\rho_1$  and  $\rho_2$ , similarly to the case for  $\multimap E$  above. (Strictly speaking, we have formulated Lemma 12 only with one output and two input wires. It can be used if  $c$  and  $d$  has a number of input and output wires by packing all these wires into a single one, which is possible because of  $(r \upharpoonright s) \circ (A \cdot (\otimes I)) = (B \cdot (\otimes I)) \circ ((r \upharpoonright s) \otimes (r \upharpoonright s))$  and  $(B \cdot (\otimes E)) \circ (r \upharpoonright s) = ((r \upharpoonright s) \otimes (r \upharpoonright s)) \circ (A \cdot (\otimes E))$ ). Then, by induction hypothesis, the box contents are  $\rho_1[\delta/x]$  and  $\rho_2[\delta/x]$  respectively, which completes this case.  $\square$

**Lemma 15.** *Suppose  $\rho$  derives  $\Gamma, x : A \mid \Phi \vdash t : Y$  and  $v$  is a closed value of type  $A$ . Then there exists a derivation  $\rho[v/x]$  of  $\Gamma \mid \Phi \vdash t[v/x] : Y$  with  $\llbracket \rho[v/c] \rrbracket = \llbracket \rho \rrbracket[v/x]$ .*

*Proof.* A straightforward induction on  $\rho$ .  $\square$

With these substitution lemmas, we can now prove the Soundness Theorem:

*Proof of Theorem 13.* For any reduction  $s \mapsto_k t$  there exist decompositions  $s = E[s']$  and  $t = E[t']$  such that  $s' \rightarrow_k t'$  is an instance of one of the basic reductions from Fig. 9.

The proof then goes by induction on the structure of  $E$ . The base case, where  $E$  is empty, amounts to showing the assertion for the basic reductions. We spell out representative cases:

- $s \rightarrow_k t$  is  $(\lambda x. q) p \rightarrow_k q[p/x]$ .

Since the derivation  $\delta$  of  $s$  ends in a sequent with an empty context, it cannot end with a structural rule. Hence, the last two rules in  $\delta$  must be  $\multimap E$  after  $\multimap I$ . Let  $\sigma$  and  $\tau$  be the derivations of the premises  $x : B \cdot Z \vdash q : Y$  and  $\cdot \vdash p : Z$  of these rules. Then we use Lemmas 5 and 6 and the substitution lemma to calculate:



The induction step follows straightforwardly from the induction hypothesis. If, for example,  $E$  is  $\langle F, u \rangle$ , then  $\delta$  must end in rule  $\otimes I$  with two premises  $\cdot \vdash F[s'] : Y$  and  $\cdot \vdash u : Z$ , derived by  $\sigma$  and  $\tau$ . We apply the induction hypothesis to  $\sigma$  to obtain  $\sigma'$ . Then we note that replacing  $\sigma$  with  $\sigma'$  in  $\delta$  gives us a derivation  $\rho$  of the required term. The assertion then follows because we have  $\llbracket \delta \rrbracket = \otimes I \circ (\llbracket \sigma \rrbracket \otimes \llbracket \tau \rrbracket) = \otimes I \circ (\llbracket \sigma' \rrbracket \otimes \llbracket \tau \rrbracket) = \llbracket \rho \rrbracket$ .  $\square$

**Corollary 16.** *For any interactive term  $t$  and any base language term  $f$  there exists a base language term  $(\text{let } [x] = t \text{ in } f)$  such that the typing rule*

$$[\ ]E^c \frac{\Gamma \mid \cdot \vdash t : [A] \quad \Gamma, x : A \vdash f : B}{\Gamma \vdash \text{let } [x] = t \text{ in } f : B}$$

is admissible and there are reductions

$$\begin{aligned} \text{let } [x] = [v] \text{ in } f &\mapsto_k^* f[v/x] \text{ if } v \text{ is a value ,} \\ \text{let } [x] = s \text{ in } f &\mapsto_k^* \text{let } [x] = t \text{ in } f \text{ if } s \mapsto_k t . \end{aligned}$$

*Proof.* Given  $\Gamma \mid \cdot \vdash t : [A]$ , the term  $\hat{t}$  implementing the message passing circuit  $\llbracket t \rrbracket$  has type  $\Gamma, x : 1 \vdash \hat{t} : A$ . We can then define the term  $(\text{let } [x] = t \text{ in } f)$  to be  $(\text{let } y = \hat{t}[*]/x \text{ in } f)$ .  $\square$

### 5.7. Relation to the Int-Construction

At the beginning of this article we have a construction of message passing circuits from the base language. We have derived this construction from the categorical structure obtained by applying the Int construction to a term model of a basic functional language. We have spelled out the details of  $\text{INTML}$  without reference to this categorical structure, as we believe this presentation to be more broadly accessible. Nevertheless, we would like to explain how  $\text{INTML}$  and the compilation from interactive to base terms can be understood as a model construction *within* the Int construction.

In this section we explain in which sense the circuits defined earlier can be understood as morphisms in a category  $\text{Int}(\mathbb{C})$  obtained by Int construction from a suitable term model  $\mathbb{C}$ . The main difficulty with this is that circuits may contain free variables and so must appear in a suitably indexed variant of the Int construction, rather than the standard one. In this section we show how to capture this indexing in a way that remains close to the original Int construction.

We start by organising the base language into a term model of computations indexed by (complex) values. This structure is derived from the structure of the Enriched Effect Calculus [13] and that of Call-by-Push-Value [31], see [43].

#### 5.7.1. Complex Values

**Definition 6.** A *complex value* is any base language term that can be formed by the following grammar:

$$\begin{aligned} f, g ::= x \mid * \mid \langle f, g \rangle \mid \text{fst}(f) \mid \text{snd}(f) \mid \text{impossible}(f) \mid \text{inl}(f) \mid \text{inr}(g) \\ \mid \text{case } f \text{ of } \text{inl}(x) \Rightarrow g \mid \text{inr}(y) \Rightarrow h \end{aligned}$$

These terms have the same meaning and typing rules as in the base language. The term  $\text{impossible}(f)$  is to be typed with the following elimination rule for type 0.

$$\frac{\Gamma \vdash f : 0}{\Gamma \vdash \text{impossible}(f) : A}$$

In the base language,  $\text{impossible}(f)$  may be seen as an abbreviation for the looping term  $\text{let } x = * \text{ loop inr}(*)$ , which is why we have not included it in the definition of the base language.

Complex values can be organised into a category  $\mathbb{V}$ : the objects are the base language types; the set  $\mathbb{V}(A, B)$  of morphisms from  $A$  to  $B$  consists of complex values of type  $x : A \vdash f : B$ , identified up to extensional equality of terms and up to renaming of  $x$ .

**Proposition 17.** *The category  $\mathbb{V}$  has finite products and coproducts. Moreover, finite products distribute over finite coproducts, i.e. the canonical map  $A \times B + A \times C \rightarrow A \times (B + C)$  has an inverse.*

We define *complex evaluation contexts* by the grammar obtained by modifying the grammar for base language evaluation contexts such that complex values may be used instead of values.

### 5.7.2. Enriched Categories

For the rest of this section we assume that the reader is familiar with the basic notions of enriched category theory, see e.g. [29]. Let  $\widehat{\mathbb{V}}$  be the category  $\mathbf{Set}^{\mathbb{V}^{\text{op}}}$  of functors from  $\mathbb{V}^{\text{op}}$  to  $\mathbf{Set}$ . For a functor  $F: \mathbb{V}^{\text{op}} \rightarrow \mathbf{Set}$  we write  $F_A$  for the set it assigns to  $A$ ; given  $f \in \mathbb{V}(B, A)$  we write  $(-)[f]$  for the function  $Ff: F_A \rightarrow F_B$ . With this notation, the functoriality laws for  $F$  become  $x[id] = x$  and  $x[f \circ g] = x[f][g]$  for all  $x \in F_A$ ,  $f \in \mathbb{V}(B, A)$  and  $g \in \mathbb{V}(C, B)$ .

We work with  $\widehat{\mathbb{V}}$ -enriched categories. Recall that a  $\widehat{\mathbb{V}}$ -enriched category  $\mathbb{C}$  is given by a collection of objects, a hom-object  $\mathbb{C}(A, B)$  in  $\widehat{\mathbb{V}}$  for any two objects  $A$  and  $B$ , and maps in  $\widehat{\mathbb{V}}$  for identity  $id_A: 1 \rightarrow \mathbb{C}(A, A)$  and composition  $(-) \circ (-): \mathbb{C}(A, B) \times \mathbb{C}(B, C) \rightarrow \mathbb{C}(A, C)$  satisfying the usual laws. The enrichment is thus taken with respect to the cartesian product on  $\widehat{\mathbb{V}}$ .

Since the enrichment is in  $\widehat{\mathbb{V}} = \mathbf{Set}^{\mathbb{V}^{\text{op}}}$ , the above data specifies an ordinary category  $\mathbb{C}_\Gamma$  for each object  $\Gamma$  of  $\mathbb{V}$ : The objects of  $\mathbb{C}_\Gamma$  are those of  $\mathbb{C}$  and the set of morphisms  $\mathbb{C}_\Gamma(A, B)$  is defined to be  $\mathbb{C}(A, B)_\Gamma$ .

### 5.7.3. Computations

The  $\widehat{\mathbb{V}}$ -category of computations  $\mathbb{C}$  has the same objects as  $\mathbb{V}$ . The set of morphisms  $\mathbb{C}(A, B)_\Gamma$  is defined to be the set of all complex evaluation contexts  $E$ , for which  $\Gamma, x: A \vdash E[x]: B$  is derivable, identified up to extensional equality. The functor action of  $\mathbb{C}(A, B)$  is given by substitution. The composition of two maps is given by the complex evaluation contexts  $\Gamma, x: A \vdash E[x]: B$  and  $\Gamma, x: B \vdash F[x]: C$  is defined to be the map given by  $\Gamma, x: A \vdash F[E[x]]: C$ . The identity morphism is given by  $[\cdot]$ .

**Proposition 18.** *The  $\widehat{\mathbb{V}}$ -category  $\mathbb{C}$  has  $\widehat{\mathbb{V}}$ -enriched finite coproducts.*

*Proof.* We need to define an isomorphism  $\psi: \mathbb{C}(A, C) \times \mathbb{C}(B, C) \rightarrow \mathbb{C}(A + B, C)$  that is  $\widehat{\mathbb{V}}$ -natural in  $C$ . It can be defined by  $\psi_\Gamma(E[\cdot], F[\cdot]) = \text{case } [\cdot] \text{ of } \text{inl}(x) \Rightarrow E[x] \mid \text{inr}(y) \Rightarrow F[y]$ . An inverse is  $\psi_\Gamma^{-1}(E[\cdot]) = \langle E[\text{inl}([\cdot])], E[\text{inr}([\cdot])] \rangle$ .  $\square$

The Int construction can be applied to any traced monoidal category [28]. We next show that  $\mathbb{C}$  is traced with respect to coproducts.

**Definition 7.** A uniform  $\widehat{\mathbb{V}}$ -Conway operator is a map

$$(-)^\dagger: \mathbb{C}(A, B + A) \rightarrow \mathbb{C}(A, B)$$

satisfying the following equations:

1. (Fixpoint)  $[id_B, f^\dagger] \circ f = f^\dagger$  for all  $f \in \mathbb{C}(A, B + A)_\Gamma$ .
2. (Naturality)  $g \circ f^\dagger = ((g + id_A) \circ f)^\dagger$  for all  $f \in \mathbb{C}(A, B + A)_\Gamma$  and all  $g \in \mathbb{C}(B, C)_\Gamma$ .
3. (Dinaturality)  $([inl, g] \circ f)^\dagger = [id_C, ([inl, f] \circ g)^\dagger] \circ f$  for all  $f \in \mathbb{C}(A, C + B)_\Gamma$  and all  $g \in \mathbb{C}(B, C + A)_\Gamma$ .
4. (Diagonal)  $(f^\dagger)^\dagger = ([id_B, inr] \circ f)^\dagger$  for all  $f \in \mathbb{C}(A, (C + A) + A)_\Gamma$ .
5. (Uniformity) For all  $g \in \mathbb{C}(A, C + A)_\Gamma$ ,  $h \in \mathbb{C}(B, C + B)_\Gamma$  and  $k \in \mathbb{C}(A, B)_\Gamma$ , if  $h \circ k = (id_C + k) \circ g$  then  $g^\dagger = h^\dagger \circ k$ .

**Lemma 19.** *The  $\widehat{\mathbb{V}}$ -category  $\mathbb{C}$  has a  $\widehat{\mathbb{V}}$ -Conway operator.*

*Proof.* The operator is defined by mapping  $E \in \mathbb{C}(A, B + A)_\Gamma$  to  $(\text{let } y = [\cdot] \text{ loop } E[y])$ . We verify the dinaturality law, leaving the other cases to the reader. The two morphism  $([inl, g] \circ f)^\dagger$  and  $[id_C, ([inl, f] \circ g)^\dagger] \circ f$  in  $\mathbb{C}$  are given by the following complex evaluation contexts  $E_1$  and  $E_2$ :

$$\begin{aligned} E_1 &:= \text{let } x = [\cdot] \text{ loop case } F[x] \text{ of } \text{inl}(z) \Rightarrow \text{inl}(z) \mid \text{inr}(y) \Rightarrow G[y] \\ E_2 &:= \text{case } F \text{ of } \text{inl}(z) \Rightarrow z \mid \text{inr}(y) \Rightarrow E'_2[y] \\ &\quad \text{where } E'_2 := \text{let } y = [\cdot] \text{ loop case } G[y] \text{ of } \text{inl}(x) \Rightarrow \text{inl}(x) \\ &\quad \quad \quad \mid \text{inr}(y) \Rightarrow F[y] \end{aligned}$$

We have to show extensional equality of  $E_1$  and  $E_2$ , that is for all closed values  $v$  and  $w$ , we have  $E_1[v] \mapsto_k^* w$  if and only if  $E_2[v] \mapsto_k^* w$ . We show by well-founded induction on  $i$  that  $E_1[v] \mapsto_k^i w$  implies  $E_2[v] \mapsto_k^* w$  for all  $v$  and  $w$ . The reduction  $E_1[v] \mapsto_k^* w$  takes one of the following two forms:



- The reduction of  $E_1[v]$  has the following form, in which  $F[v]$  reduces to  $\text{inl}(w)$ :

$$\begin{aligned}
E_1[v] &\mapsto_k \text{case } (\text{case } F[v] \text{ of } \text{inl}(z) \Rightarrow \text{inl}(z) \mid \text{inr}(y) \Rightarrow G[y]) \text{ of } \text{inl}(z) \Rightarrow z \\
&\quad \mid \text{inr}(y) \Rightarrow E_1[y] \\
&\mapsto_k^* \text{case } (\text{case } \text{inl}(w) \text{ of } \text{inl}(z) \Rightarrow \text{inl}(z) \mid \text{inr}(y) \Rightarrow G[y]) \text{ of } \text{inl}(z) \Rightarrow z \\
&\quad \mid \text{inr}(y) \Rightarrow E_1[y] \\
&\mapsto_k \text{case } \text{inl}(w) \text{ of } \text{inl}(z) \Rightarrow z \mid \text{inr}(y) \Rightarrow E_1[y] \\
&\mapsto_k w
\end{aligned}$$

In this case, we get  $E_1[v] \mapsto_k^* w$  and immediately also  $E_2[v] \mapsto_k^* w$ , which is what we had to prove.

- The reduction of  $E_1[v]$  starts as follows, where  $F[v]$  reduces to  $\text{inr}(u)$ :

$$\begin{aligned}
E_1[v] &\mapsto_k \text{case } (\text{case } F[v] \text{ of } \text{inl}(z) \Rightarrow \text{inl}(z) \mid \text{inr}(y) \Rightarrow G[y]) \text{ of } \text{inl}(z) \Rightarrow z \\
&\quad \mid \text{inr}(y) \Rightarrow E_1[y] \\
&\mapsto_k^* \text{case } (\text{case } \text{inr}(u) \text{ of } \text{inl}(z) \Rightarrow \text{inl}(z) \mid \text{inr}(y) \Rightarrow G[y]) \text{ of } \text{inl}(z) \Rightarrow z \\
&\quad \mid \text{inr}(y) \Rightarrow E_1[y] \\
&\mapsto_k^* \text{case } G[u] \text{ of } \text{inl}(z) \Rightarrow z \mid \text{inr}(y) \Rightarrow E_1[y]
\end{aligned}$$

The reduction then continues by reduction of  $G[u]$ . We next make a case distinction on the value  $G[u]$  reduces to. But note first that we also have

$$E_2[v] \mapsto_k E_2'[u] \mapsto_k^* \text{case } G[u] \text{ of } \text{inl}(x) \Rightarrow \text{inl}(x) \mid \text{inr}(y) \Rightarrow E_2'[y] .$$

- $G[u]$  is reduced to  $\text{inl}(w)$ , i.e. the reduction of  $E_1[v]$  continues as follows:

$$\begin{aligned}
\text{case } G[u] \text{ of } \text{inl}(z) \Rightarrow z \mid \text{inr}(y) \Rightarrow E_1[y] &\mapsto_k^* \text{case } \text{inl}(w) \text{ of } \text{inl}(z) \Rightarrow z \mid \text{inr}(y) \Rightarrow E_1[y] \\
&\mapsto_k w
\end{aligned}$$

In this case we also have

$$\begin{aligned}
E_2[v] &\mapsto_k^* \text{case } G[u] \text{ of } \text{inl}(x) \Rightarrow \text{inl}(x) \mid \text{inr}(y) \Rightarrow E_2'[y] \\
&\mapsto_k^* \text{case } \text{inl}(w) \text{ of } \text{inl}(x) \Rightarrow \text{inl}(x) \mid \text{inr}(y) \Rightarrow E_2'[y] \\
&\mapsto_k w ,
\end{aligned}$$

as was required to show.

- $G[u]$  is reduced to  $\text{inr}(u')$ , i.e. the reduction of  $E_1[v]$  continues as follows:

$$\begin{aligned}
\text{case } G[u] \text{ of } \text{inl}(z) \Rightarrow z \mid \text{inr}(y) \Rightarrow E_1[y] &\mapsto_k^* \text{case } \text{inr}(u') \text{ of } \text{inl}(z) \Rightarrow z \mid \text{inr}(y) \Rightarrow E_1[y] \\
&\mapsto_k E_1[u']
\end{aligned}$$

The rest of the reduction then is  $E_1[u'] \mapsto_k^j w$  for some  $j < i$ . By induction hypothesis we get  $E_2[u'] \mapsto_k^* w$ . We can then conclude this case by observing the following reduction sequence:

$$\begin{aligned}
E_2[v] &\mapsto_k^* \text{case } G[u] \text{ of } \text{inl}(x) \Rightarrow \text{inl}(x) \mid \text{inr}(y) \Rightarrow E_2'[y] \\
&\mapsto_k^* \text{case } \text{inr}(u') \text{ of } \text{inl}(x) \Rightarrow \text{inl}(x) \mid \text{inr}(y) \Rightarrow E_2'[y] \\
&\mapsto_k E_2'[u'] \mapsto_k E_2[u'] \mapsto_k^* w
\end{aligned}$$

□

**Proposition 20.** *The  $\widehat{\mathbb{V}}$ -category  $\mathbb{C}$  has a uniform trace with respect to coproducts. Precisely, this means that for all  $A$ ,  $B$  and  $C$  there is a map*

$$\text{Tr}(A, B, C): \mathbb{C}(A + C, B + C) \rightarrow \mathbb{C}(A, B)$$

*such that, for all  $\Gamma$ ,  $(\text{Tr}(A, B, C))_\Gamma$  is a uniform trace in  $\mathbb{C}_\Gamma$  in the standard sense.*

*Proof.* The natural transformation is given such that  $Tr(A, B, C)_\Gamma$  maps  $E$  with  $\Gamma, x: A + C \vdash E[x]: B + C$  to

$$F = \text{case } E[\text{inl}(x)] \text{ of } \text{inl}(x) \Rightarrow x \\ | \text{inr}(y) \Rightarrow \text{let } z = y \text{ loop } E[\text{inr}(z)]$$

with  $\Gamma, x: A \vdash F[x]: B$ . This defines a uniform trace in each  $\mathbb{C}_\Gamma$ , see [20]. Naturality, i.e. closure under substitution with complex values, follows immediately.  $\square$

#### 5.7.4. Int-Construction

We can now explain in which sense the circuits defined earlier in this section appear as morphisms in an instance of the Int construction.

We use the following evident generalisation of the Int construction to  $\widehat{\mathbb{V}}$ -enriched categories. The  $\widehat{\mathbb{V}}$ -category  $\text{Int}(\mathbb{C})$  has as objects pairs  $(X^-, X^+)$  of  $\mathbb{C}$ -objects. The hom-objects are defined by

$$\text{Int}(\mathbb{C})((X^-, X^+), (Y^-, Y^+)) = \mathbb{C}(X^+ + Y^-, Y^+ + X^-) .$$

Composition and identity are uniquely determined by requiring that  $\text{Int}(\mathbb{C})_\Gamma$  agrees with the usual Int construction on  $\mathbb{C}_\Gamma$ . In this  $\widehat{\mathbb{V}}$ -enriched variant one finds the structure well-known from the Int construction, see [28]. For example, a compact closed structure is given on objects by  $X \otimes Y = (X^- + Y^-, X^+ + Y^+)$  and  $(X^-, X^+)^* = (X^+, X^-)$ . We do not spell out this structure in detail here, because we have already defined it in terms of circuits earlier in this section.

A circuit over  $\Gamma$  with interface  $[X_1, \dots, X_n] \rightarrow [Y_1, \dots, Y_m]$  is a representation of a map of type  $X_1 \otimes \dots \otimes X_n \rightarrow Y_1 \otimes \dots \otimes Y_m$  in  $\text{Int}(\mathbb{C})_\Gamma$ . The compact closed structure of  $\text{Int}(\mathbb{C})$  is given in Lemmas 5 and 6. For further information on the categorical properties of the Int construction we refer to [28, 21], as the intention in this section is merely to relate the construction of circuits to the standard Int construction.

## 6. Space Bounds

The translation of interactive terms to circuits is a compilation method that allows us to establish interesting space bounds in a simple way. How much space does the circuit implementation of an interactive term use? Recall that a circuit implements message passing. Messages travel from node to node; they are transformed by each node they pass. Our circuit implementation takes any message that may be passed into the circuit and traces it through the circuit, until it is returned to the environment. The space used by this implementation is proportional to the size of the circuit and the maximum size that a message can attain. Both sizes are easy to estimate in our case. The circuit size depends only on the term and will be fixed. The maximum message size too can be read off from the circuit. As circuit wires are labelled with types, we know in advance all the possible (base language) types that the messages can have. So, a simple bound on the maximum size of a message is the maximum of the sizes of all the values of any type that appears in the circuit. Notice that this simple space bound can already be computed given only an `INTML` typing derivation.

If we restrict our attention to types not containing `int` or type variables, all these types will be finite, and we obtain finite size bounds. We immediately obtain:

**Proposition 21** (Constant Space). *If  $\delta$  derives  $\Gamma \mid \Phi \vdash t : X$  and  $\delta$  does not contain any type variable of the type `int`, then the behaviour of the circuit  $\llbracket \delta \rrbracket$  can be implemented in constant space.*

This proposition may perhaps seem trivial, but the constant space circuit implementation of `INTML` terms is close to Ghica's method of hardware synthesis [14]. As `INTML` is more expressive than Ghica's `bSCI`, it may be interesting to study the constant space variant of `INTML`, e.g. for hardware synthesis.

In this section we show that this simple idea of analysing the space usage of circuit evaluation can be used to obtain interesting space bounds for `INTML` programs. We show that `INTML` characterises the classes of functions computable in logarithmic space (`FLOGSPACE`) and non-deterministic logarithmic space (`NFLOGSPACE`).

### 6.1. Logarithmic Space

We describe a precise correspondence between the class of functions representable in  $\text{INTML}$  and the class of functions computable in logarithmic space. We begin by analysing the space requirements of base language reduction.

We consider the space usage of a simple direct implementation of term reduction for the base language. The size needed to encode a term will be proportional to the size of its abstract syntax trees, provided that the size of  $\text{int}$ -constants is counted appropriately. We define the size of values to be  $|x| = |*| = 1$ ,  $|n| = \lceil \log n \rceil$ ,  $|\langle v, w \rangle| = |v| + |w|$  and  $|\text{inl}(v)| = |\text{inr}(v)| = 1 + |v|$ . With this definition values can be encoded such that  $v$  needs space  $O(|v|)$ . This definition is extended to base terms in the evident way by counting any inner node in the abstract syntax tree as one, e.g.  $|\text{case } f \text{ of } \text{inl}(x) \Rightarrow g \mid \text{inr}(y) \Rightarrow h| = 1 + |f| + |g| + |h|$ , etc.

For the purposes of relating  $\text{INTML}$  to  $\text{FLOGSPACE}$ , it suffices to prove the following lemma on the space requirements of base language reduction. It states that the space needed for base language reduction is linear in bit-width of integers.

**Lemma 22.** *Let  $x : A \vdash f : B$  be derivable for variable-free types  $A$  and  $B$ . There exist constants  $c \in \mathbb{N}$  and  $d \in \mathbb{N}$  such that for any  $k$  and any closed value  $v : A$  containing only  $k$ -bit integers, we have that  $f[v/x] \mapsto_k^* g$  implies  $|g| \leq c \cdot k + d$ .*

*Proof.* Define the *potential* of a term  $\Gamma \vdash f : A$  to be the size of the largest term that can appear when we first substitute closed values for the free variables in  $f$  and then apply an arbitrary number of reduction steps:

$$\text{pot}(\Gamma \vdash f : A) = \max\{|g| \mid f[\sigma] \mapsto_k^* g \text{ for some value-substitution } \sigma \in \mathcal{E}_\Gamma\}$$

Notice that  $\text{pot}(\Gamma \vdash f : A)$  is thus defined if and only if  $\Gamma \vdash f : A$  is derivable.

For any variable-free base type  $A$ , we can define an upper bound  $|A|$  on the size of its values as follows:  $|0| = |1| = 1$  and  $|\text{int}| = k$ ,  $|A \times B| = |A + B| = 1 + |A| + |B|$ . We have  $|v| \leq |A|$  for any closed value  $v$  of type  $A$  that contains only  $k$ -bit integers.

Next we show that each of the following inequalities holds, given that the terms on both of its sides are defined.

$$\text{pot}(\Gamma \vdash x : A) \leq |A| \tag{16}$$

$$\text{pot}(\Gamma \vdash * : 1) \leq 1 \tag{17}$$

$$\text{pot}(\Gamma \vdash \langle f, g \rangle : A \times B) \leq 1 + \text{pot}(\Gamma \vdash f : A) + \text{pot}(\Gamma \vdash g : B) \tag{18}$$

$$\text{pot}(\Gamma \vdash \text{fst}(f) : A) \leq 1 + \text{pot}(\Gamma \vdash f : A \times B) \tag{19}$$

$$\text{pot}(\Gamma \vdash \text{snd}(f) : B) \leq 1 + \text{pot}(\Gamma \vdash f : A \times B) \tag{20}$$

$$\text{pot}(\Gamma \vdash \text{inl}(f) : A + B) \leq 1 + \text{pot}(\Gamma \vdash f : A) \tag{21}$$

$$\text{pot}(\Gamma \vdash \text{inr}(g) : A + B) \leq 1 + \text{pot}(\Gamma \vdash g : B) \tag{22}$$

$$\text{pot}(\Gamma \vdash \text{case } f \text{ of } \text{inl}(x) \Rightarrow g \mid \text{inr}(y) \Rightarrow h : C) \leq 1 + \text{pot}(\Gamma \vdash f : A + B) \tag{23}$$

$$+ \text{pot}(\Gamma, x : A \vdash g : C)$$

$$+ \text{pot}(\Gamma, y : B \vdash h : C)$$

$$\text{pot}(\Gamma \vdash \text{let } x = f \text{ loop } g : A) \leq 3 + \text{pot}(\Gamma \vdash f : B) \tag{24}$$

$$+ 2\text{pot}(\Gamma, x : B \vdash g : A + B)$$

$$\tag{25}$$

We consider (23) as a representative case: Any reduction of  $\text{case } f[\sigma] \text{ of } \text{inl}(x) \Rightarrow g[\sigma] \mid \text{inr}(y) \Rightarrow h[\sigma]$  must, by definition, start with reducing  $f[\sigma]$ . In this stage all intermediate terms have the form  $\text{case } f' \text{ of } \text{inl}(x) \Rightarrow g[\sigma] \mid \text{inr}(y) \Rightarrow h[\sigma]$  for some  $f'$  with  $f[\sigma] \mapsto_k^* f'$ . Clearly, the size of this term is bounded by  $1 + \text{pot}(\Gamma \vdash f : A + B) + \text{pot}(\Gamma, x : A \vdash g : C) + \text{pot}(\Gamma, y : B \vdash h : C)$ . Once  $f[\sigma]$  has been reduced to a value  $\text{inl}(v)$  or  $\text{inr}(w)$ , reduction continues with a contraction of the case and then with either  $g[\sigma][v/x]$  or  $h[\sigma][w/y]$ . In either case, the size of the intermediate terms are bounded by the claimed bound.

To establish the bound claimed in the lemma, consider now a derivable typing judgement  $\Gamma \vdash f : A$ . By unfolding the above inequality repeatedly as long as possible, we obtain an inequality  $\text{pot}(\Gamma \vdash f : A) \leq c_1 \cdot |A_1| + c_2 \cdot |A_2| + \dots +$

$c_n \cdot |A_n| + d$  for some suitable types  $A_1, \dots, A_n$  that all appear in the derivation of  $\Gamma \vdash f : A$  and some natural numbers  $c_1, \dots, c_n, d$ . But by definition of the size of types, this means that there exists some  $c$ , such that

$$\text{pot}(\Gamma \vdash f : A) \leq c \cdot k + d$$

holds for all  $k$ . By definition of the potential, this is what we have to prove.  $\square$

Having proved a linear space bound for reduction in the base language, we now show that the interactive language can capture the functions computable in logarithmic space. Let  $\Sigma$  be a fixed, finite alphabet. We show that  $\text{INTML}$  captures the  $\text{FLOGSPACE}$ -computable functions  $\Sigma^* \rightarrow \Sigma^*$ .

First, we must define how to represent functions from  $\Sigma^*$  to  $\Sigma^*$  in  $\text{INTML}$ . We choose an encoding that represents functions in a space efficient way, making essential use of the interactive nature of  $\text{INTML}$ -computation. Any finite set can be represented as a variable-free base language type, e.g. by a sum  $1 + \dots + 1$  with one summand for each element. We shall use finite sets as if they were base language types, with the understanding that such an encoding has been applied implicitly.

For the representation of words we will assume that the bit width of integers is chosen to be  $\lceil \log n \rceil$ , where  $n$  is the length of the input word. This means that the type  $\text{int}$  contains just enough values to point to any position in the input word. A little care needs to be taken with the trivial cases where  $n \leq 1$ , so we will choose the bit-width to be  $k(n)$ , defined as follows.

$$k(n) = \begin{cases} 1 & \text{if } n \leq 1, \\ \lceil \log n \rceil & \text{otherwise.} \end{cases}$$

Since logarithmic space functions can produce output of polynomial length, we need to be able to represent numbers of up to  $n^m$  for any constant  $m$ . We do so in the standard way by using the type  $\text{int}^m := \text{int} \times \dots \times \text{int}$  ( $m$  factors). If  $n$  is the maximum value of type  $\text{int}$ , then  $\text{int}^m$  can represent values up to  $n^m$ . We define the least value of  $\text{int}^m$  to be the vector of all zeros and order the values lexicographically. The basic arithmetic operations on  $\text{int}^m$  can be implemented in the base language. For any number  $i$  that can be encoded in  $\text{int}^m$ , we write  $\langle i \rangle_m$  for its representation as a value of type  $\text{int}^m$ .

Words over the alphabet  $\Sigma$  can be represented as functions that map positions to characters. That is, the word  $w = w_0 w_1 \dots w_n$  is represented by a function mapping  $i$  to  $w_i$ . Any position that goes beyond the end of the word is mapped to a special blank symbol  $\square$ . We write  $\Sigma_\square$  for  $\Sigma \uplus \{\square\}$ .

Formally, we represent  $\Sigma$ -words using types of the form  $\mathbb{B}^m := 1 \cdot [\text{int}^m] \multimap [\Sigma_\square]$ :

**Definition 8.** A closed interactive term  $t$  of type  $\mathbb{B}^m$  represents a word  $w_0 w_1 \dots w_n \in \Sigma^*$  with respect to  $k$ -bit integers if and only if  $n$  can be represented as a value of type  $\text{int}^m$  (using  $k$ -bit integers) and we have

$$t \langle i \rangle_m \mapsto_k^* [c] \iff (i \leq n \wedge c = w_i) \vee (i > n \wedge c = \square)$$

for all numbers  $i$  representable in  $\text{int}^m$  (using  $k$ -bit integers).

We have defined  $\mathbb{B}^m$  to be  $1 \cdot [\text{int}^m] \multimap [\Sigma_\square]$ . The reader may wonder if the choice to 1 in the subexponential is a restriction. It is certainly not a restriction for a program that is given a variable of such a type as an input. The program can assume that the program does not need to store anything when it requests its argument. It is also not a restriction for programs defining values of this type, as the  $A$  can be pulled out. If  $t$  has type  $A \cdot [B] \multimap X$ , then  $\lambda x. \text{let } [b] = x \text{ in } t [b]$  has type  $1 \cdot [B] \multimap X$ . The value  $b$  of type  $B$  is computed up front and stored in the environment, so that when  $t$  requests its argument the value can be returned right away without having to query  $x$  again.

**Lemma 23.** For any  $w \in \Sigma^*$  there exists a closed interactive term  $\langle w \rangle_m : \mathbb{B}^m$  that represents  $w$  with respect to  $k(|w|)$ -bit integers.

*Proof.* We can define a suitable term by  $\langle w \rangle_m = \lambda x. \text{let } [i] = x \text{ in } [w(i)]$ , where  $w(i)$  is a base language term that implements the pseudo-code `if  $i = \langle 0 \rangle_m$  then  $w_0$  else if  $i = \langle 1 \rangle_m$  then  $w_1 \dots$`   $\square$

**Definition 9.** We say that a term  $\vdash t : A \cdot \mathbb{B}^m \multimap \mathbb{B}^r$  represents a function  $\varphi : \Sigma^* \rightarrow \Sigma^*$  if and only if for every word  $w \in \Sigma^*$  the term  $(t \langle w \rangle_m)$  encodes  $\varphi(w)$  with respect to  $k(|w|)$ -bit integers.

We remark that one may also use an alternate definition that does not refer to reduction in the interactive language, without affecting the truth of the following theorem. Instead of requiring  $t \langle w \rangle_m \langle i \rangle_r$  to reduce to one of  $c_i$  or  $\square$ , one may ask for the circuit for this term to have the same behaviour as either  $[c_i]$  or  $[\square]$ , depending on  $i$ .

**Theorem 24** (FLOGSPACE Soundness). *If  $\varphi: \Sigma^* \rightarrow \Sigma^*$  is represented by  $t$ , then  $\varphi$  is computable in logarithmic space. Moreover, a FLOGSPACE algorithm computing  $\varphi$  is given by circuit-evaluation of  $t$ .*

*Proof.* Compiling  $t$  to a base language term yields a term  $x: X^- \vdash f: X^+$ , where  $X$  has the form  $A \cdot \mathbb{B}^m \multimap \mathbb{B}^r$ . By Lemma 22, reduction of  $f$  with respect to  $k$ -bit integers (i.e. computation of  $\varphi$  on strings of length up to  $(2^k)^m$ ) can be implemented using space linear in  $k$ .

Using this observation, we now construct a logarithmic space Turing Machine  $M_\varphi$  for  $\varphi$ . Given input  $w$ , we choose the bit-width of integers to be  $k(|w|)$ . With this choice the machine simulates the circuit for the term  $(t \langle w \rangle_m \langle i \rangle_r): [\Sigma \square]$  with respect to  $k$ -bit integers, first for  $i = 0$ , then  $i = 1$ , etc. This will result in a sequence of characters, which the machine writes on its output tape, as they are returned from the circuit. The machine stops once a blank symbol is returned.

The machine  $M_\varphi$  works as follows. Given input word  $w$ , it writes down the term  $f$  with all integers represented using  $k := k(|w|)$  bits. It then reduces  $f \text{ inr}(\text{inr}(*))$  with respect to  $k$ -bit integers. This amounts to asking the function for a character. Depending on the result value, it continues as follows: (i) if the value is  $\text{inr}(\text{inr}(c))$  (informally: ‘The requested character is  $c$ .’) then it outputs  $c$ , advances the output head and if  $c$  was not a blank symbol it begins from the start; (ii) if the value is  $\text{inr}(\text{inl}(v, *))$  (informally: ‘Which character of  $\varphi(w)$  should be computed?’) it reduces  $f \text{ inr}(\text{inl}(v, \langle i \rangle_r))$ , where  $i$  is the position of the output head, and continues with the same case distinction; (iii) if the value is  $\text{inl}(v, \text{inr}(*))$  (informally: ‘A character from the input is requested.’), then it reduces  $f \text{ inl}(v, \text{inl}(*, *))$  (informally: ‘What is the position of the requested character?’) and continues with the same case distinction; (iv) if the value is  $\text{inl}(v, \text{inl}(*, \langle i \rangle_r))$  (informally: ‘It is the  $i$ -th input character that is requested.’), then it moves its input head to position  $i$  of the input tape, reads the character  $w_i$  there and reduces  $f \text{ inl}(v, \text{inr}(w_i))$  (informally: ‘The requested character is  $w_i$ .’) and continues with the same case distinction.  $\square$

**Theorem 25** (FLOGSPACE Completeness). *Any function  $\varphi: \Sigma^* \rightarrow \Sigma^*$  computable in logarithmic space is represented by some interactive term  $t_\varphi$ .*

*Proof.* The proof goes by a simple encoding of logarithmic space Turing Machines in INTML.

Let  $M$  be a logarithmic space Turing Machine computing  $\varphi$ . We can assume that it has one work tape. The content of the work tape and the position of the work tape head can be encoded using two stacks that contain the characters that one would see when reading from the work tape head to the left and to the right respectively. With a binary encoding, these stacks can be encoded by two natural numbers. As the machine only uses logarithmic space, these numbers can be bounded by a polynomial in the size of the input, as can the output length of the machine.

We can assume that the bit width of integers is large enough, so that  $\text{int}$  has enough elements to encode the positions in an input word. The state of  $M$  can be encoded by values of a base language type of the form

$$S = Q \times \text{int} \times \text{int}^m \times \text{int}^m \times \text{int}^r,$$

where  $Q$  is the set of states of  $M$ . The component of type  $\text{int}$  encodes the position of the input head. The two components of type  $\text{int}^m$  encode the content of the work tape of  $M$  to the left and right of  $M$ ’s work head. Finally, the component of type  $\text{int}^r$  encodes the position of the output head. Notice that  $m$  and  $r$  can always be found, as we have defined  $k(n)$  so that  $\text{int}$  always has at least two elements.

The initial configuration of the machine  $M$  is readily encoded as a base language value  $\vdash \text{init}_0 : S$ .

The step function of  $M$  is implemented as a base language term

$$c: \Sigma, n: P, s: S \vdash \text{step}_0(c, s, n) : S + \Sigma \square$$

that formalises the step of  $M$ , given that the currently scanned input character is  $c$ , the current state is  $s$  and that the aim is to compute the character at position  $n$  on the output tape. The result of  $\text{step}_0(c, s, n)$  is  $\text{inl}(s')$  if  $M$  moves from  $s$  to  $s'$  in one step and the output head of  $M$  has not moved beyond the position represented by  $n$ . The result of  $\text{step}_0(c, s, n)$  is  $\text{inr}(d)$  if in this step  $M$  writes the character  $d$  the position addressed by  $n$  on the output tape. We omit the details of the definition of  $\text{step}_0$ , which is straightforward in the presence of iteration.

To simulate the computation of the Turing Machine  $M$ , we must read the character  $c$  from the input word, which is encoded as a function in the interactive language. We define the step function as an interactive term

$$n : P \mid w : A \cdot [\text{int}] \multimap [\Sigma_{\square}] \vdash \text{step}(w, n) : B \cdot [S] \multimap [S + \Sigma_{\square}] ,$$

for suitable  $A$  and  $B$ :

$$\begin{aligned} \text{step}(w, n) = \lambda s. \text{let } [\langle q, \text{pos}, \text{tapeleft}, \text{taperright}, \text{outpos} \rangle] = s \text{ in} \\ \text{let } [c] = w [\text{pos}] \text{ in} \\ [\text{step}_0(c, \langle q, \text{pos}, \text{tapeleft}, \text{taperright}, \text{outpos} \rangle, n)] \end{aligned}$$

With these definitions, a term  $t_{\varphi}$  representing  $\varphi$  can be defined by

$$\begin{aligned} t_{\varphi} : A \cdot ([\text{int}] \multimap [\Sigma_{\square}]) \multimap ([\text{int}'] \multimap [\Sigma_{\square}]) \\ t_{\varphi} = \lambda w. \lambda n'. \text{let } [n] = n' \text{ in loop step}(w, n) [\text{init}_0] \end{aligned}$$

for some suitable  $A$ . □

## 6.2. Nondeterministic Logarithmic Space

The functions computable in non-deterministic logarithmic space ( $\text{NFLOGSPACE}$ ) can be captured much in the same way as those in  $\text{FLOGSPACE}$ . Just as for Turing Machines one moves from deterministic to non-deterministic transition functions, one can in  $\text{INTML}$  allow non-determinism in the base language and capture non-deterministic logarithmic space in this way.

The class  $\text{NFLOGSPACE}$  is defined to consist of all functions  $f : \Sigma^* \rightarrow \Sigma^*$  for which there exists a non-deterministic Turing Machine  $M_f$  with logarithmic space usage and the following property:  $f(w) = v$  if and only if  $M_f$  accepts input  $w$  and with  $v$  written on its output tape. Note that this implies that  $M_f$  is single-valued. Given input  $w$ , it may accept only with output  $f(w) = v$ ; it must not accept with any other output. In the literature, the class of functions definable in non-deterministic logarithmic space is also called  $\text{FNL}$  [3] and is defined to be  $\text{FLOGSPACE}^{\text{NLOGSPACE}}$  – the set of functions that can be computed in logarithmic space with access to an  $\text{NLOGSPACE}$ -oracle. It is not hard to see that this definition is in fact equivalent to the definition of  $\text{NFLOGSPACE}$  above. A proof of the equivalence can be found in [4, Prop. 3.1].

We extend the base language with non-determinism by adding a new constant  $\vdash \text{ndchoice} : 1 + 1$ : Its two reduction rules make  $\text{ndchoice}$  a non-deterministic choice operator:

$$\text{ndchoice} \mapsto_k \text{inl}(\ast) \qquad \text{ndchoice} \mapsto_k \text{inr}(\ast)$$

The reader should reconsider the definition of what it means for a term  $t$  of type  $\vdash t : A \cdot \mathbb{B}^m \multimap \mathbb{B}^r$  to represent a function  $\varphi : \Sigma^* \rightarrow \Sigma^*$  in the light of the non-determinism in the base language: For any word  $w$  and any  $c \in \Sigma$ , if we choose the bit-width of integers such that  $|w|$  can be represented as an  $\text{int}$  value, then there exists a reduction sequence from  $t \langle w \rangle_m \langle i \rangle_r$  to  $[c]$  if and only if  $c$  is the character at position  $i$  in the word  $\varphi(w)$  (when considered padded with blank symbols). This corresponds to the computation of a single-valued non-deterministic Turing Machine: the character at position  $i$  on its output tape is  $c$  if and only if it has a run that outputs  $c$  on this position.

**Theorem 26** ( $\text{NFLOGSPACE}$  Soundness). *If  $\varphi : \Sigma^* \rightarrow \Sigma^*$  is represented by  $t$ , then  $\varphi$  is computable in non-deterministic logarithmic space.*

*Proof.* The proof goes just like that of Theorem 24 for  $\text{LOGSPACE}$  soundness. For the reduction of  $\text{ndchoice}$  we use non-deterministic choice. □

**Theorem 27** ( $\text{NFLOGSPACE}$  Completeness). *Any function  $\varphi : \Sigma^* \rightarrow \Sigma^*$  computable in non-deterministic logarithmic space is represented by some interactive term  $t_{\varphi}$ .*

*Proof.* Let  $M_{\varphi}$  be a  $\text{NFLOGSPACE}$  Turing Machine that computes  $\varphi$ . We use the formulation of non-deterministic Turing Machines where a machine has two deterministic transition functions and decides non-deterministically in each step which of the two functions to apply.

We construct a term  $t_\varphi$  just as in the proof to Theorem 25. The state  $S$  of the machine is exactly as in Theorem 25. For the two step functions of  $M_\varphi$  we define terms  $\text{step}_0^1(c, s, n)$  and  $\text{step}_0^2(c, s, n)$  with

$$c : \Sigma, n : P, s : S \vdash \text{step}_0^i(c, s, n) : S + \Sigma_\square$$

as in Theorem 25. From this we define the non-deterministic step function  $\text{step}_0(c, s, n)$  by:

$$c : \Sigma, n : P, s : S \vdash \text{if ndchoice step}_0^1(c, s, n) \text{ else step}_0^2(c, s, n) : S + \Sigma_\square$$

The rest of the construction of  $t_\varphi$  is exactly as in Theorem 25.

Non-deterministic choices are thus accounted for in the definition of  $\text{step}_0(c, s, n)$  by using the constant `ndchoice`. That is, we define  $\text{step}_0(c, s, n)$  such that, when given concrete values for  $c$ ,  $s$  and  $n$ , there exist reduction sequences leading to each possible successor configuration, and only to those configurations.  $\square$

## 7. Related Work

### 7.1. Linear Logic

Linear Logic [16] has played an important role in the implicit characterisation of complexity classes by functional programming languages. *Intuitionistic Linear Logic* has the formulae  $\alpha$ ,  $X \otimes Y$ ,  $X \multimap Y$ ,  $!X$  and  $\forall \alpha. X$ . Under the Curry-Howard correspondence, these can be seen as types of a polymorphic  $\lambda$ -calculus. The resulting type system refines System F.

Girard, Scedrov and Scott [19] realised that by restricting the exponential  $!X$ , it is possible to carve out a class of System F terms that captures the functions computable in polynomial time. They introduced *Bounded Linear Logic* (BLL) [19], which replaces the exponential  $!X$  with a bounded exponential  $!_{x < p} X$ , where  $p$  is a polynomial. While the unbounded exponential  $!X$  represents an arbitrary number of copies of  $X$ , the bounded exponential  $!_{x < p} X$  represents a polynomial number of copies of  $X$ , one for each number less than  $p$ .

If one further restricts universal quantification to  $\forall \alpha \leq p. X$ , then one can identify the functions computable in logarithmic space. This has led to *Stratified Bounded Affine Logic* (SBAL) [42] with formulae of the form  $\alpha(\vec{p})$ ,  $X \otimes Y$ ,  $X \multimap Y$ ,  $!_{x < p} X$  and  $\forall \alpha \leq p. X$ .

The definition of INTML can be seen as the result of a simplifying SBAL for programming purposes. The formulae of SBAL play the same role as the interactive types in INTML. In order to make the type system as simple as possible, INTML does not have polymorphism, and the resource polynomials have been enriched to become base language types.

The removal of polymorphism is motivated by programming language practice. In System F and the above-mentioned linear logics, data types such as natural numbers are defined using impredicative encodings [17]. In SBAL it is possible to represent the natural numbers  $\mathbb{N}_p = \{0, \dots, p\}$  using a type of the form  $\forall \alpha \leq q. !_{x < p} (\alpha(x) \multimap \alpha(x+1)) \multimap \alpha(0) \multimap \alpha(p)$ . In practical programming languages it is more common to find such data types as primitive base types. INTML removes polymorphism for simplicity and adds primitive data types instead. After removing polymorphism from SBAL, one is left with types of the form  $X \otimes Y$ ,  $X \multimap Y$  and  $!_p X$ . The now missing type of natural numbers may be added as a primitive type of the form  $[\mathbb{N}_p]$ , which is to be understood like `[int]` in INTML. The bounded exponential  $!_p X$  corresponds to the subexponential  $\mathbb{N}_p \cdot X$  in INTML. Indeed, if we consider  $\mathbb{N}_p$  as a base language type, then the interactive interpretation of  $[\mathbb{N}_p]$ ,  $X \otimes Y$ ,  $X \multimap Y$  and  $\mathbb{N}_p \cdot X$  described in this paper is essentially the same as the interactive interpretation of SBAL in [42].

But adding a base type  $[\mathbb{N}_p]$  alone is not enough. We also need to add terms to be able to work with it. For example, one may expect there to be an addition constant `add`:  $[\mathbb{N}_p] \multimap \mathbb{N}_p \cdot [\mathbb{N}_p] \multimap [\mathbb{N}_{p+q}]$ . But a good choice of primitive constants is not obvious. Since values of type  $\mathbb{N}_p$  can be stored in space  $\log p$  using a binary encoding, they can be stored fully in memory. The programmer should expect to be able to work with them without any language constraint. At the same time the language must guarantee that the use of large data does not lead beyond logarithmic space. Allowing unconstrained manipulation of small values that fully fit in memory was one motivation for making the base language visible to the programmer in INTML.

That INTML uses base types in place of polynomials is also a result of a simplification. While it is possible to formulate INTML just with types of the form  $[\mathbb{N}_p]$ ,  $X \otimes Y$ ,  $X \multimap Y$  and  $\mathbb{N}_p \cdot X$ , the need for manipulation of polynomials would make the language more complicated. Irrelevant encoding details would become visible in the type system. For

example, the `INTML` type system works with  $(A \times B) \cdot X$  instead of  $A \cdot (B \cdot X)$ . If we allow only base types of the form  $\mathbb{N}_p$ , then  $\mathbb{N}_p \cdot (\mathbb{N}_q \cdot X)$  could become  $\mathbb{N}_{p \cdot q} \cdot X$ , as it is possible to represent pairs of numbers in  $\mathbb{N}_p$  and  $\mathbb{N}_q$  by a single number in  $\mathbb{N}_{p \cdot q}$ . But in an implementation one may like to use a fast and easy-to-implement pairing function that just concatenates the bits of the two numbers. Then one would need to replace  $\mathbb{N}_{p \cdot q}$  by  $\mathbb{N}_{r \cdot q}$ , where  $r$  is an upper bound for next the power of two that is larger than  $p$  (it is not even obvious how to write a good bound as a polynomial). This means that small changes in the implementation would influence the type system. It seems more natural to separate concerns by writing  $(\mathbb{N}_p \times \mathbb{N}_q) \cdot X$  in the type system and by handling the encoding details on the base language level.

*Dual Light Affine Logic.* The concrete definition of the interactive language of `INTML` is inspired by Baillot and Terui’s Dual Light Affine Logic (`DLAL`) [6]. `INTML` treats the subexponential  $A \cdot X$  much like `DLAL` treats the exponential modality  $!X$ . Just as the exponential modality may only appear in negative positions in `DLAL`, i.e. one can have  $!X \multimap Y$  but not  $X \multimap !Y$ , `INTML` only accounts for types of the form  $A \cdot X \multimap Y$ . The restriction to negative occurrences of  $A \cdot X$  much simplifies the type system without being limiting in applications.

*Subexponentials.* We use the term subexponential by analogy with the subexponentials introduced by Nigam and Miller [37]. Nigam and Miller study a logic with labelled subexponentials. Depending on the label  $L$ , weakening and contraction may or may not be allowed for  $!_L$  and it may be possible to go from two subexponentials with a certain labels  $!_L !_K X$  to one with a third label  $!_R X$ .

In `INTML`, the subexponential  $A \cdot X$  is labelled with a base type  $A$ . All `INTML` subexponentials allow weakening. Since our base language does not contain infinite types, they do not allow contraction. If one added a base type  $\mathbb{N}$  of unbounded natural number, then  $\mathbb{N} \cdot X$  would support contraction. Moreover, the phenomenon of combining two subexponentials into a third one appears in that  $A \cdot (B \cdot X)$  becomes  $(A \times B) \cdot X$ . Rule `STRUCT` also allows conversion between different subexponentials.

## 7.2. Interaction Semantics

*Geometry of Interaction Situations.* Abramsky, Haghverdi & Scott [1] study an axiomatic framework for Girard’s Geometry of Interaction (`GoI`) [18]. They introduce *Geometry of Interaction situations* to capture the structure of the `GoI` abstractly. In particular, they show that the `Int` construction gives rise to one such `GoI` situation. The main difference to our work in this paper is that in a `GoI` situation one assumes enough structure to model the exponentials of Linear Logic. In our case this would amount to assuming a type  $\mathbb{N}$  of unbounded natural numbers with properties such as  $\mathbb{N} + \mathbb{N} \triangleleft \mathbb{N}$ . We consider it a main contribution of `INTML` that we do not need to make such assumptions, as this enables programming with strongly limited space, such as constant or logarithmic space, see Proposition 21 and Theorems 24 and 26.

*Game Semantics.* The idea of viewing  $\lambda$ -terms as message passing circuits which communicate with their environment by exchanging questions and answers is also reminiscent of game semantics. This comes with no surprise, given the strong relation existing between the latter and geometry of interaction [5, 12]. Indeed, our construction is quite similar to `AJM` games for multiplicative and exponential linear logic [2], the main difference being the way we treat the exponentials. While we use a simple construction of subexponentials  $A \cdot X$ , in `AJM`-games the exponentials are constructed from  $\mathbb{N} \cdot X$  by identifying a set of possible plays and by identifying different plays that represent the same computation. In `INTML` there is no provision, such as the plays in `AJM`-games, that restricts the possible interactions with a circuit. One may see `INTML` as a programming language for implementing the (possibly ill-formed) raw strategies of `AJM`-games.

*Geometry of Synthesis.* The way to see  $\lambda$ -terms as circuits is similar to Ghica’s Geometry of Synthesis [14], which is defined on `bSCI` as described in Section 4.2. The main difference to our approach is the treatment of duplication. In `INTML`, duplication is handled through subexponential types, similarly to what happens with indexes in Abramsky-Jagadeesan-Malacaria (`AJM`) games. This does not require circuits to have internal states, but forces tokens to be more complex: whenever you interact with an object which could possibly be duplicated, you need to specify which specific copy of it you are querying. In Ghica’s work, on the other hand, the target language consists of so-called handshake circuits, which do have an internal state. This has of course the advantage of keeping tokens simple, but has a price: the implementation of non-linear  $\lambda$ -terms is not always possible, Kierstead’s terms being an example.



*Geometry of Implementation.* Many years before the advent of the Geometry of Synthesis, Mackie had the idea of turning the Geometry of Interaction into an abstract machine implementing  $\lambda$ -calculus reduction [32]: the source language was `pcf`, while the object language took the form of a minimal assembly language for an abstract register machine. Mackie’s translation is similar to ours, but no bounds on the space consumption of programs is are considered. Mackie’s treatment of copying amounts to what happens when one adds to the base language a type of binary trees  $T(\alpha)$ :

$$\begin{aligned} \text{type } T(\alpha) = & \text{Empty of } 1 \\ & | \text{Leaf of } \alpha \\ & | \text{Node of } T(\alpha) \times T(\alpha) \end{aligned}$$

With such a type one has  $T(\alpha) \times T(\alpha) \triangleleft T(\alpha)$  and  $T(\alpha) + T(\alpha) \triangleleft T(\alpha)$ . Then, one can use  $T(1)$  for all subexponentials and use the `STRUCT` to maintain this form after each rule application. Thus, arbitrary copying can be allowed. Of course, the type system then does not yield interesting space bounds anymore. In this paper we have shown that this approach to copying can be refined and controlled, so that space bounds can be proven.

*Defunctionalization.* We have developed `INTML` directly from from an analysis of the demand-driven computation that one finds in logarithmic space algorithms. However, it turns out that the translation from the interactive language to the base language is closely related to the more traditional compilation techniques of `cps`-translation [22] and defunctionalization [40]. These two techniques allow the translation of a higher-order functional language to a first-order language with tail recursion. Given a higher-order language, one may first apply a `cps`-translation to fix the evaluation order and then use defunctionalization to reduce the resulting higher-order program to a first-order program.

In `INTML` we have used as the base language a simple first-order language with a loop construct. It is equally possible to use a first-order language with tail recursion instead. For each message passing node and each incoming wire we could define a first-order function that takes the incoming message as argument, that computes the output message and that then invokes the first-order function of the destination node with the resulting output message with a tail call. The translation from the higher-order interactive language to the first-order language with tail recursion that one obtains in this way is closely related to the program one obtains by call-by-name `cps`-translation [22] followed by a particular defunctionalization procedure [7]. Details are described in [44].

### 7.3. Implicit Characterisations of Logarithmic Space

The work in this paper builds on earlier work on implicit characterisations of logarithmic space complexity classes. While implicit characterisations of complexity classes often focus on minimal formal systems, such as function algebras [36, 38, 39] or while-languages [26], the results are useful in obtaining a better understanding of the resource usage properties of programming languages. Bonfante [8] shows how to capture `LOGSPACE` by a first-order functional language. In `INTML` we work towards extending such results to higher-order functional programming languages. For example, we believe that Møller-Neergaard’s function algebra  $\text{BC}_\varepsilon^-$  can be implemented in `INTML`. The `LOGSPACE` soundness of this function algebra depends on a complicated implementation of recursion with computational amnesia [36]. An `INTML` program for this can be found as an example in the experimental implementation of `INTML`<sup>1</sup>. An interesting direction for further work is to consider lazy data structures and corecursion in `INTML` and the relation to the work of Ramyaa and Leivant [39]. Where we use functions to represent long strings, Ramyaa and Leivant use streams.

## 8. Conclusion

We have found that an interactive approach to computation, as formalised by the `Int` construction, is a good way of structuring space-bounded computation that can help us to understand the principles of space-bounded functional programming. This view has guided the design of `INTML`, a simple, expressive language capturing `FLOGSPACE`. Practical experience with an experimental implementation of `INTML` suggests that, with suitable type inference, `INTML` can be made to be quite usable [11].

<sup>1</sup>Available from: <http://www.github.com/uelis/IntML>

We believe that INTML is not only interesting as a language for sublinear space computation. The interactive computation model appears to have a wide range of applications – we have already commented on the connection to hardware synthesis, for example – and the INTML type system is general enough to be instantiated for different applications. Regarding the INTML type system itself, we intend to investigate the relation to the use of subexponentials in the proof theory of linear logic, e.g. in [37].

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$\frac{-   x : 1 \cdot \mathbb{V}_\alpha \vdash x : \mathbb{V}_\alpha \quad -   z_1 : 1 \cdot [\text{int}] \vdash z_1 : [\text{int}]}{-   x : 1 \cdot \mathbb{V}_\alpha, z_1 : (\alpha \times 1) \cdot [\text{int}] \vdash x z_1 : [2]}$	$\frac{}{;}$	$\frac{}{;}$	$\frac{}{;}$
$\frac{-   x : 1 \cdot \mathbb{V}_\alpha, z_1 : \alpha \cdot [\text{int}] \vdash x z_1 : [2]}{-   x : 1 \cdot \mathbb{V}_\alpha, z_1 : \alpha \cdot [\text{int}], y : (2 \times 1) \cdot \mathbb{V}_\beta, z_2 : 2 \times \beta \cdot [\text{int}] \vdash \text{let } [u_1] = x z_1 \text{ in } [\text{xor}(u_1, u_2)] : [2]}$	$\frac{}{;}$	$\frac{}{;}$	$\frac{}{;}$
$\frac{-   x : 1 \cdot \mathbb{V}_\alpha, y : 2 \cdot \mathbb{V}_\beta, z : (\alpha + 2 \times \beta) \cdot [\text{int}] \vdash \text{copy } z \text{ as } z_1, z_2 \text{ in } \text{let } [u_1] = x z_1 \text{ in } \text{let } [u_2] = y z_2 \text{ in } [\text{xor}(u_1, u_2)] : [2]}{-   x : 1 \cdot \mathbb{V}_\alpha, y : 2 \cdot \mathbb{V}_\beta, z : (\alpha + 2 \times \beta) \cdot [\text{int}] \vdash \text{copy } z \text{ as } z_1, z_2 \text{ in } \text{let } [u_1] = x z_1 \text{ in } \text{let } [u_2] = y z_2 \text{ in } [\text{xor}(u_1, u_2)] : [2]}$	$\frac{}{;}$	$\frac{}{;}$	$\frac{}{;}$
$\frac{-   x : 1 \cdot \mathbb{V}_\alpha, y : 1 \cdot \mathbb{V}_\beta \vdash \lambda z. \text{copy } z \text{ as } z_1, z_2 \text{ in } \text{let } [u_1] = x z_1 \text{ in } \text{let } [u_2] = y z_2 \text{ in } [\text{xor}(u_1, u_2)] : \mathbb{V}_{(\alpha+2 \times \beta)}}{-   x : 1 \cdot \mathbb{V}_\alpha \vdash \lambda y. \lambda z. \text{copy } z \text{ as } z_1, z_2 \text{ in } \text{let } [u_1] = x z_1 \text{ in } \text{let } [u_2] = y z_2 \text{ in } [\text{xor}(u_1, u_2)] : 2 \cdot \mathbb{V}_\beta \multimap \mathbb{V}_{(\alpha+2 \times \beta)}}$	$\frac{}{;}$	$\frac{}{;}$	$\frac{}{;}$
$\frac{-   - \vdash \lambda x. \lambda y. \lambda z. \text{copy } z \text{ as } z_1, z_2 \text{ in } \text{let } [u_1] = x z_1 \text{ in } \text{let } [u_2] = y z_2 \text{ in } [\text{xor}(u_1, u_2)] : \mathbb{V}_\alpha \multimap 2 \cdot \mathbb{V}_\beta \multimap \mathbb{V}_{(\alpha+2 \times \beta)}}{-   - \vdash \lambda x. \lambda y. \lambda z. \text{let } [v] = z \text{ in } \text{let } [u_1] = x [v] \text{ in } \text{let } [u_2] = y [v] \text{ in } [\text{xor}(u_1, u_2)] : [2]}$	$\frac{}{;}$	$\frac{}{;}$	$\frac{}{;}$
$\frac{-   z : 1 \cdot [\text{int}] \vdash z : [\text{int}]}{-   x : 1 \cdot \mathbb{V}_\alpha, y : (\text{int} \times 2) \cdot \mathbb{V}_\beta, z : 1 \cdot [\text{int}] \vdash \text{let } [v] = z \text{ in } \text{let } [u_1] = x [v] \text{ in } \text{let } [u_2] = y [v] \text{ in } [\text{xor}(u_1, u_2)] : [2]}$	$\frac{}{;}$	$\frac{}{;}$	$\frac{}{;}$
$\frac{-   x : \text{int} \cdot \mathbb{V}_\alpha, y : (\text{int} \times 2) \cdot \mathbb{V}_\beta, z : 1 \cdot [\text{int}] \vdash \text{let } [v] = z \text{ in } \text{let } [u_1] = x [v] \text{ in } \text{let } [u_2] = y [v] \text{ in } [\text{xor}(u_1, u_2)] : [2]}{-   x : \text{int} \cdot \mathbb{V}_\alpha, y : (\text{int} \times 2) \cdot \mathbb{V}_\beta \vdash \lambda z. \text{let } [v] = z \text{ in } \text{let } [u_1] = x [v] \text{ in } \text{let } [u_2] = y [v] \text{ in } [\text{xor}(u_1, u_2)] : \mathbb{V}_1}$	$\frac{}{;}$	$\frac{}{;}$	$\frac{}{;}$
$\frac{-   - \vdash \lambda x. \lambda y. \lambda z. \text{let } [v] = z \text{ in } \text{let } [u_1] = x [v] \text{ in } \text{let } [u_2] = y [v] \text{ in } [\text{xor}(u_1, u_2)] : (\text{int} \times 2) \cdot \mathbb{V}_\beta \multimap \mathbb{V}_1}{-   - \vdash \lambda x. \lambda y. \lambda z. \text{let } [v] = z \text{ in } \text{let } [u_1] = x [v] \text{ in } \text{let } [u_2] = y [v] \text{ in } [\text{xor}(u_1, u_2)] : \text{int} \cdot \mathbb{V}_\alpha \multimap (\text{int} \times 2) \cdot \mathbb{V}_\beta \multimap \mathbb{V}_1}$	$\frac{}{;}$	$\frac{}{;}$	$\frac{}{;}$

Abbreviation:  $\mathbb{V}_\alpha$  denotes  $\alpha \cdot [\text{int}] \multimap [2]$ .  
Applications of rule EXCH have been hidden.

Figure 17: Typing Derivations for Judgements from Section 2.6