

Preface to the Special Section Issue on Improving Software Quality through Formal Methods

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Preface to the Special Issue on Improving Software Quality through Formal Methods

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Abstract This special issue is dedicated to the presentation of novel results in the scope of formal methods for the verification and testing of software. The papers included in the special issue showcase approaches that successfully combine a rigorous and formal background with the capability to address real software applications in realistic settings.

1 Introducing the Special Issue

The special issue is devoted to the use of formal methods for improving the quality of software systems. The included articles focus on the use of runtime techniques, such as runtime verification and trace analysis; see Falcone et al. [2013]; Bartocci and Falcone [2018]; Bartocci et al. [2019]. Following an open call, this issue results in 3 papers that were selected out of 8 papers after a careful reviewing process. The papers are briefly discussed as follows.

In "Verifying Temporal Specifications of Java Programs", Spagni et al. combine satisfiability modulo theory and static analysis to approximate the behavior of Java threads. The behavior approximation is represented as a network of timed automata and the authors show that their abstraction preserves the truthfulness of two real-time temporal logics.

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In "Runtime Verification of Real-Time Event Streams Under Non-synchronized Arrival", Scheffel et al. present an approach to monitor concurrent systems with a shared clock abstracted as real-time event streams. The authors present a specification language and an online monitoring algorithm for the evaluation of the specifications.

In "Model Generation of Component-based Systems", Salva and Blot present an approach to learn the behavior of component-based systems from their execution traces using machine learning techniques. The resulting tool has been successfully experimented with network protocol traces.

The special issue was announced during the track on Software Verification and Testing (SVT) at the 33rd ACM/SIGAPP Symposium On Applied Computing. One of the papers accepted in this special issue is an extended version of a paper that originally appeared at the SVT track. Collectively, these papers tackle cutting-edge topics thereby appealing to the experts in the field.

Finally, the guest editors would like to thank the authors and the reviewers who contributed to the special issue.

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