

Industrial IoT with Crystal-Free Mote-on-Chip

Tengfei Chang*, Timothy Claeys*, Mališa Vučinić*, Xavi Vilajosana[†], Titan Yuan[‡], Brad Wheeler[‡], Filip Maksimovic[‡], David Burnett[‡], Brian Kilberg[‡], Kris Pister[‡], Thomas Watteyne*

* Inria, Paris, France.

[†] Universitat Oberta de Catalunya, Catalonia, Spain.

[‡] UC Berkeley, California, USA.

Abstract

SC μ M is a $2\times 3\times 0.3$ mm³ system-on-chip that contains an ARM Cortex-M0 and a 2.4 GHz IEEE802.15.4 radio. This paper describes the two-step calibration routine needed to run a full 6TiSCH stack on SC μ M. It is, to the best of our knowledge, the first time a fully standards-compliant protocol stack runs on a crystal-free radio, such that it can participate in a network with off-the-shelf radios.

Keywords: 6TiSCH, crystal-free, IoT, IEEE802.15.4, TSCH.

Introduction

The Single-Chip Micro Mote (SC μ M, Fig. 1) is a $2\times 3\times 0.3$ mm³ system-on-chip that contains an ARM Cortex-M0 and a 2.4 GHz radio compatible with IEEE802.15.4 and BLE [1]. Unlike existing radios, SC μ M only requires an antenna and a power source, no external crystal oscillator. SC μ M realizes the “Smart Dust” vision by offering standards-compliance: it communicates with off-the-shelf radios.

Applications such as smart wearables or microrobotics require dependable networking between SC μ M chips. A reference standardized Industrial Internet of Things protocol stack is 6TiSCH [2], standardized by the Internet Engineering Task Force (IETF). We were able to run the 6TiSCH stack on a SC μ M using an external crystal oscillator [3]. The work we present here is the first to show the 6TiSCH stack running on SC μ M with no crystal oscillator.

Calibrating SC μ M’s Oscillators

Without a crystal oscillator, SC μ M relies on a free-running 2.4 GHz RF LC oscillator for tuning the communication frequency. Three separate RC oscillators are used to generate the transmitter chipping rate (2 MHz), clock the receiver (64 MHz), run the micro-controller (5 MHz), and drive the scheduling timer. These oscillators exhibit a clock drift of up to 16,000 ppm over an 80 min 25-70 C temperature ramp, compared to 10-40 ppm for typical crystal oscillators. Calibrating these oscillators is a significant challenge because of the non-linearity of the tuning, and the 40 ppm target. This paper uses two successive calibration steps.

We use optical calibration to provide an initial coarse calibration to all four oscillators. SC μ M contains a photodiode and the circuitry for an optical programming board to load firmware onto SC μ M using an LED [4]. We augment this by having the LED send a specific 32-bit sequence that triggers an interrupt on the micro-controller 100 times after loading the binary, with a 100 ms period. At each LED sequence, SC μ M stores the value of the clock counters for calibration (Fig. 2).

This is sufficient to initially calibrate the RC oscillators with enough accuracy to establish communications.

To calibrate the RF oscillator, we calibrate against a “QuickCal box”: 16 off-the-shelf IEEE802.15.4-compliant devices (OpenMote) programmed to transmit a beacon frame every 600 μ s, each on one of the 16 IEEE802.15.4 2.4 GHz frequencies. Calibrating the RF oscillator consists of adjusting three 5-bit capacitive tuning DACs. As shown in Fig. 3, SC μ M first sweeps through 1024 radio RX settings, listens on each for 800 μ s, and records the radio setting for each of the beacons received. Frequency calibration for RX and TX are independent. SC μ M therefore sweeps a second time to calibrate radio TX settings by transmitting a frame every 1.32 ms and listening for an acknowledgement frame. RF calibration takes a total of 174 s, including the time for receiving the first frame from the QuickCal box. This version of SC μ M has not been fully optimized for energy, the active power of the entire system is 1 mW.

Running Industrial IoT protocol

After calibrating against the QuickCal box, SC μ M takes an average of 3.2 s to synchronize to an in-range OpenMote running TSCH. SC μ M’s timers are designed for running the 6TiSCH stack: during a 20 ms timeslot, SC μ M’s advanced timers orchestrate the execution of a timeslot state machine without microcontroller intervention (Fig. 4). This removes the need to wait for the microcontroller to be active, resulting in a potentially shorter timeslot and reducing energy consumption.

After optical and RF calibration, SC μ M runs the full 6TiSCH protocol stack and communicates with an OpenMote, forming a 6TiSCH network (Fig. 5). After joining the network, SC μ M relies only on receiving periodic beacon frames from the OpenMote for re-calibration. Fig. 6 indicates SC μ M stays synchronized to the 6TiSCH network as long as an OpenMote in range sends a beacon at least once every 440 ms.

Future work involves the extension of the temperature compensation used in [1] to apply frequency corrections derived from network traffic to all the free-running oscillators.

References

- [1] Maksimovic, “A Crystal-Free Single-Chip Micro Mote with Integrated 802.15.4 Compatible Transceiver, sub-mW BLE Compatible Beacon Transmitter, and Cortex M0,” in *VLSI*. IEEE, 2019.
- [2] Vilajosana, “6TiSCH: Industrial Performance for IPv6 Internet-of-Things Networks,” *Proceedings of the IEEE*, pp. 1–13, 11 April 2019.
- [3] Chang, “Demo: 6TiSCH on SC μ M, Running a Synchronized Protocol Stack without Crystals,” in *EWSN*, 2020.
- [4] Wheeler, “A Low-Power Optical Receiver for Contact-free Programming and 3D Localization of Autonomous Microsystems,” in *UEMCON*, 2019.

