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————— THÈME 1 —————



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Thierry Lafage, André Seznec, Erven Rohou, François Bodin

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Abstract: Trace-driven simulation is a widely used technique to evaluate microprocessor architecture and/or memory hierarchy performance. However, software trace collection induces significant execution slowdown. As on-line simulation induces an even more disastrous slowdown, simulating applications incurring trillions of instructions can not be considered without trace sampling. Therefore continuous software trace collection of the complete workload of a desktop computer cannot be envisaged.

The Code Cloning Tracing method is aimed at allowing trace collection on time consuming applications. Sampling the execution is performed through a static duplication and instrumentation of the source code. A first copy of the code is nearly identical to the original code and will execute most of the time. The second code copy is further instrumented by the trace user and will be parsimoniously executed in order to collect traces without slowing down too much the overall application. At execution time, some *events* make the execution switch to the instrumented code copy. Other *events* make the execution return to the original-like code copy. The choice of these *events* determines the execution overhead, therefore making the approach very flexible.

This paper presents in details the Code Cloning Tracing method. We also present **calvin**, the first prototype implemented to validate the approach. Experiments show that Code Cloning Tracing could be applied on all applications running on desktop computer without significantly affecting user response time when traces are not collected. This opens the way to punctual (let us say one second of CPU) collection of the whole activity of a desktop computer.

Key-words: software tracing, static code annotation method, micro-architecture simulation, trace-collection platform.

(Résumé : *tsvp*)

Code Cloning Tracing: une nouvelle approche pour la collecte de traces de programmes

Résumé : La méthode *Code Cloning Tracing* exposée dans cet article permet la collecte de traces sur de grosses applications (coûteuses en temps). Un échantillonnage est réalisé par l'exécution alternative de deux copies du code. Une des copies est presque identique au code original et s'exécute la plupart du temps (à une vitesse proche de celle de l'application originale). La seconde copie du code est instrumentée plus lourdement pour permettre la collecte effective de la trace. Celle-ci est exécutée avec parcimonie pour ne pas trop ralentir l'application testée. À l'exécution, certains événements permettent le saut d'une copie à l'autre.

L'article présente aussi un premier prototype, appelé **calvin**, pour implémenter la méthode *Code Cloning Tracing*. Pour le moment, **calvin** est utilisé avec des applications mono-tâches sur architecture SPARC. Les résultats obtenus avec des applications instrumentées par **calvin**, s'exécutant sans générer de trace, ont fait apparaître de faibles ralentissements de l'exécution (20 à 30 %). Ceci ouvre la voie de la collecte de trace de l'activité complète d'une station de travail. Pour cela, **calvin** sera bientôt étendu pour permettre de tracer des applications multi-tâches ainsi que le système d'exploitation (Linux).

Mots-clé : collecte de traces de programme, méthode d'annotation statique de code, simulation de micro-architecture, plate-forme de collecte de traces.

1 Introduction and Motivations

Trace-driven simulation “involves driving a simulation model of a system with a trace of external stimuli” [12]. It has been used by researchers for decades to evaluate microprocessor architectures and/or memory system performances. Such simulations might be as accurate as possible and require collecting realistic program traces. These traces may consist of the addresses of instructions executed and/or data referenced by a program (for cache simulations), but may also be registers or memory values (to simulate value prediction for instance). Such traces are needed for architecture simulations and validations in a processor design, or for new micro-architecture ideas validations.

Ideally, one would need access to traces representing the whole punctual activity of a workstation or a desktop computer for a significant time interval (maybe a few seconds). This includes activity of all alive processes and the kernel. Moreover, one would like these traces to be representative of the real workload on the station, i.e. not immediately after booting the operating system, but for instance an hour or a week later. Nowadays, no platform allows such an ideal trace collection.

Most current trace-collection platforms are limited to collect traces from a single application, generally only user instructions in this application [15], while it was shown that the usage of such uncompleted traces leads to significant misleading conclusions [1, 16, 17].

Another concern for micro-architecture studies is the performance of trace collection. Among the numerous trace collection platforms available [15], none of them allows a slowdown lower than 10 for simply collecting addresses for memory hierarchy simulations. For studies like value prediction [7], more information must be collected, thus leading to very high slowdowns (over 100).

While trace collection is quite time consuming, micro-architecture (or memory system) simulation is often orders of magnitude more complex and may induce slowdowns in the 1,000–10,000 range. Such a simulation rate does not allow to consider realistic simulation of the entire workload of a desktop computer or even a large application executing trillions of instructions. Simulating such applications requires sampling [6]. Therefore, apart very specific studies like cache behavior analysis of the whole SPEC92 benchmark suite [3] which used months of CPU time, most studies on cache or micro-architecture are performed using the first 100,000,000 or billion instructions of an application (maybe the first 50,000,000 are skipped to avoid the initialization phase) or are performed using a small or restricted data set.

The purpose of this paper is to introduce Code Cloning Tracing, a new software annotation method for collecting traces. Sampling the execution is performed through a static duplication and instrumentation of the code. A first copy of the code is nearly identical to the original code and will execute most of the time. The second code copy is further instrumented by the trace user¹ and parsimoniously executed in order to get trace without slowing down too much the overall application. At execution time, some *events* make the execution be switched to the instrumented copy of the code. Other *events* make the execution return to the original-like code copy. The choice of the *events* determines the execution overhead therefore making the approach very flexible.

The first advantage of Code Cloning Tracing is to allow trace collection in realistic time on large applications as presented in this paper. Applying Code Cloning Tracing to an application allows to collect trace samples without impairing significantly the overall execution time. Our ultimate goal is to build a “close-to-ideal” trace-collection platform. We plan to apply Code Cloning Tracing to a complete operating system (Linux) and all applications running on a desktop computer. Then, we should be able to collect trace samples of the complete workload: for instance, every hour a few seconds activity can be traced. However, this trace-collection platform is beyond the scope of this paper.

The remainder of this paper is organized as follows: in the next section, we first present some related works, then we detail the principles of the sampling method. Section 3 presents two implementations of the tool which collect traces for single user applications. Results on the SPEC95 integer benchmark suite are presented in Section 4. Section 5 summarizes this study and presents our future development directions.

2 Sampling to Collect Traces

In this section, we first overview some related works about trace collection tools including both static code annotators and instruction-set emulators. Then, we present the Code Cloning Tracing approach.

¹For convenience, throughout this paper (unless explicitly written), a *trace user* will be someone who collects program traces. He/she will use the framework we provide, but will also need to instrument the tested applications to get the traces he/she wants.

2.1 Related Works

Pixie [13], ATOM [14], and EEL [5] are among the most popular static code annotation methods. Pixie operates on MIPS executables and adds code to count the execution of each basic block. ATOM is more flexible and enables a user to specify how to annotate each individual instruction, basic block or procedure of an executable file for DEC Alpha architecture. There are annotation points where the user can specify the machine state to extract. EEL (Executable Editing Library) is a library, flexible as well, that allows the user to operate on executable files. The abstraction provided by EEL avoids to deal with machine-dependent features and support SPARC architecture at the moment.

All these static code annotation methods are to be used in a unique way: the resulting executable is only made of instrumented code and will be executed entirely. Thus, today’s static code annotated programs execute slowly when they are used to feed complex cache or micro-architecture simulators. Such slowness is due to:

1. the overhead induced by the execution of numerous added instrumentation instructions;
2. the large size of the instrumented code which affects significantly the behavior of the memory hierarchy (particularly instruction cache and TLB).

An alternative to static code annotation is instruction emulation. Some tools directly emulate each instruction of a given workload in order to simulate architectural components, and may be surprisingly efficient.

Shade [2] is an instruction-set simulator based on dynamic translation of simulated instructions. On the first occurrence of a basic block in an application, Shade translates it in a sequence of native instructions in which instrumentation code for trace recording is embedded. On further occurrences of the same basic block, the emulation sequence is directly invoked by the simulator. Slowdowns from 9 to 14 were reported using Shade for address traces instrumentation. Note that this approach allows to trace the activity in dynamically linked codes.

SimOS is a complete simulation environment capable of modeling an entire computer system, including full operating system and all application programs that run on top of it [11]. SimOS relies on the same principle of dynamic translation as Shade. However, SimOS allows a less detailed-execution mode called direct-execution mode which can provide more effective performance. This mode is used on sections the user is not interested in tracing (e.g. the operating system boot or the beginning of a user application). Switching from direct-execution mode to other (time consuming) emulation modes (to collect traces) on event occurring is allowed by the simulator [10].

2.2 Principles of Code Cloning Tracing

For large applications consuming several CPU hours, the trace cannot be realistically stored or consumed at execution time (trillions of trace elements!). Techniques to reduce this large data set include *trace sampling* as suggested by [4, 6, 8]. However, such techniques also slow the overall trace collection process.

Instead of sampling the resulting trace after (or during) execution to reduce the trace volume², we propose to sample the execution of the program by implementing two execution modes: a “no-trace collection” mode and a “trace collection” mode. This might allow a low execution slowdown on the application part where the trace will not be collected (for instance, on a large application, more than 99% of the original executed instructions may not be traced).

The principle of Code Cloning Tracing is reported in Figure 1. The original code is duplicated. The first code copy, called P_exec , is instrumented in a “light” way, in order to get an execution time close to the original one. The second code copy, called P_inst , is annotated by the trace user to gather information for simulations or statistics on the execution. At execution time, P_exec is executed most of the time, but some *events* make the execution be switched to the trace collection mode: P_inst is executed and a trace sample (could be several thousands or millions of references) is collected. Other *events* related to P_inst allow the execution to resume in “no-trace collection” mode: P_exec is executed. The choice of these events is further discussed in Section 3.2.1.

Typically, P_exec executes most of the time. This code copy is lightly instrumented and induces only a small execution overhead (compared to the execution of the original program). The size of P_exec code is also

²Trace volume is used here for convenience; by trace volume, we mean the volume of information that is passed to an on-line simulator or stored in a trace file.

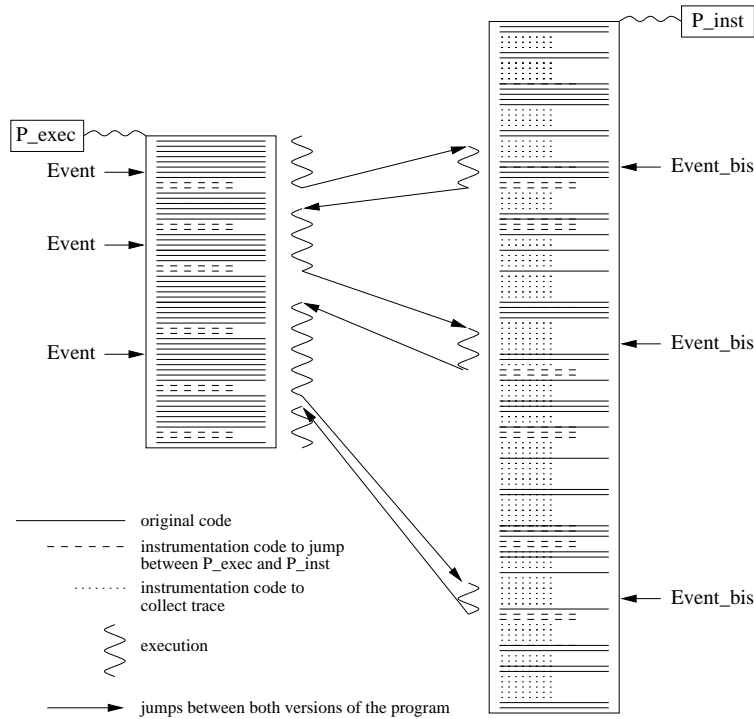


Figure 1: Execution of cloned code to collect trace.

close to the size of the original code. As a result, the behavior of the memory hierarchy (including TLBs) only suffers from a tiny change provided that P_exec and P_inst are allocated in different memory pages.

As SimOS, Code Cloning Tracing will provide a fast execution mode for sections that are not to be simulated, and a time consuming trace collection mode. *A contrario* from SimOS, Code Cloning Tracing relies on a static code annotation. Very high performance is obtained on “no-trace collection” mode as reported in Section 4.

2.3 Finely Tuning the Framework

The basic technique presented above is quite general. At implementation level, some tradeoffs about the overall trace collection process must be considered.

First, a tradeoff between the trace volume and the execution time: the larger the trace collected, the higher the execution slowdown. The trace collection will be tuned depending on the maximum allowed execution slowdown and on the trace usage.

Second, the events determining switching between both copies of code must be determined at implementation level and judiciously chosen in an empirical way. Events may be internally generated by the application or external. When the events are internal, they have to be distributed as evenly as possible all along the execution code. In this case, one can count them at run time. The mode switching may be performed when the counter reaches a threshold value. This value can easily be further tuned as a parameter of the execution setup.

Third, the trace user can choose the type of information to collect. The **calvin** prototype presented in the next section is only a framework allowing an easy and flexible further code instrumentation. Notice that, *a contrario* of other static code annotation methods, there is no special requirement to optimize the instrumented code. If P_exec is executed on 99.9% of the instructions (with a slowdown of 1.2 for instance), and if the P_inst slowdown is 1,000 (some quite complex on-line simulation for instance), the overall execution slowdown will be: $0.999 \times 1.2 + 0.001 \times 1,000 \simeq 2.2$.

3 Implementations for Mono-process Applications

Code Cloning Tracing is a general technique. Our prototype, called **calvin**³ works only on stand-alone applications. However, most of the technical issues associated with switching from “no-trace collection” mode to “trace collection” mode and vice-versa will be the same for instrumenting multi-process workloads and even a complete operating system. **calvin** also allows to validate the approach from the performance point of view: on “no-trace collection” mode, the slowdown induced by our approach is around 20–30 % and therefore can be supported by most *real* users.

In order to favor flexibility, portability, ease of use, and ease of implementation, **calvin** was made on top of the SALTO system [9].

calvin allows to build two types of application clones according to its execution mode: the first type of clones performs switches upon internal events (*inner* mode), while the second type deals with an added external event materialized by a shared variable (*shm* mode).

We first briefly overview the SALTO system [9] which was used by **calvin** to actually clone the code at assembly language level. Then we present **calvin** itself.

3.1 Concept and Features of SALTO

SALTO is a retargetable framework for developing the whole spectrum of tools that manipulate programs expressed in assembly language. This meta-tool is composed of three parts: a kernel, a target description file and the tool provided by the user. Figure 2 shows how these three components interact.

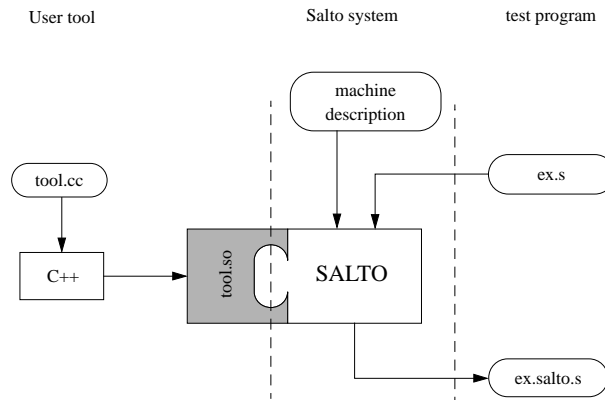


Figure 2: SALTO system overview.

The **kernel** performs common house-keeping tasks the developer does not want to worry about, like parsing the assembly code and the target description file. The kernel provides also an internal representation of the tested program which is available via the user interface.

The **target description file** contains the hardware configuration and the complete description of the instruction-set architecture, including per-instruction resource reservation table. Changing target architecture is just providing a new description file.

The **tool implemented by the user** uses the internal representation made by the kernel in a flexible way, thanks to the object-oriented user interface (C++ classes and methods).

SALTO hides most of the boring implementation details, and thus allowed us to focus on **calvin**.

3.2 calvin

Duplicating the code and making sure it executes and switches correctly between both copies leads to several difficulties. First, the events which will determine the switches between both execution modes have to be judiciously chosen. At second, a correspondence between both code copies must be maintained, and unwanted mode switching should be avoided by dealing with control-transfer instructions (CTIs). Finally, a further instrumentation of the *P_inst* copy must be possible. Each of these points is addressed below.

³cloning assembler and looking into veritable instrumentation needs.

3.2.1 Choosing the Events

We chose to switch the execution mode (in a symmetrical way) upon procedure calls. The PC (Program Counter) switches from one copy to the other when a certain number of these events are executed. For instance, to switch execution from P_exec to P_inst 10,000 procedure calls must have been executed; to switch execution from P_inst to P_exec 10 procedure calls must have been executed. So, if procedure calls are *ideally* (i.e. evenly) distributed among the instructions, 0.1% of the initial instructions will be executed in the trace-collection mode.

We chose such events to facilitate the design of a first implementation, for three main reasons:

1. Globally, procedure calls are relatively rare in C-programs, but are not so uncommon (for instance, in the programs from the SPEC95 integer benchmark suite, their frequency varies between 0.08% and 1.6%). Thus, the code added for mode switching is not executed frequently and the induced overhead is not very high. On the other hand, as procedure calls are not so uncommon, they allow a reasonable control on switches between the two execution modes.
2. In a program, a procedure call represents an important change in the execution control flow, often followed by changes in the environment. The PC gets a new value and its old value is saved, and some of the general registers are also often saved, in order to get new ones for the callee procedure. Thus, performing a jump to the other copy of the program near a procedure call will be easier (the inserted code can be shorter).
3. For stand-alone application tracing, it allows reproducible tracing/on-line simulation chains often needed to compare several micro-architecture ideas.

3.2.2 Correspondence between P_exec and P_inst

Maintaining a strict correspondence between every instruction in P_exec and P_inst would require a very large table. Such a full correspondence is not needed in practice. On the appearance of a switch event, the switch must occur to the right place. However, we will see in the next section that inopportune come-backs (i.e. unwanted switches from P_inst to P_exec or vice-versa) have to be avoided.

In the P_exec copy, every switch point must have its associated entry point in P_inst . **calvin** inserts assembly-language labels at entry points. The switch itself consists of a jump to this label. Any inserted code must not alter the normal execution of the program and, in particular, must ensure that the registers keep the same values, from the original code point of view. So the registers used by the inserted code must first be saved and then restored. In order to reduce the added overhead, the inserted code uses *dead registers*⁴ whenever possible.

3.2.3 Dealing with Control-transfer Instructions

A switch from or to trace-collection mode on the tested program is a control flow change. The original program itself also performs control flow changes with control-transfer instructions (CTIs) such as jump, conditional branch, procedure call, ... At this level, we must ensure that during the execution, the PC will not jump from P_exec to P_inst (or vice-versa) except when the correct event is encountered.

While working at assembly language level, this is relatively straightforward when the CTI branches to a named label. However, indirect jumps are also allowed: the target address is stored in a register, and is a computed value. With such indirect jumps, the target address is only known at execution time and unwanted mode switches may arise. Moreover, when a switch from P_inst to P_exec (or vice-versa) occurs during the computation of an indirect jump target address, the result may be undetermined. For instance, as shown in Figure 3, the target address may depend on a P_exec or P_inst basic block address.

Common use of indirect jumps includes:

- C-switch statements (the branch to the right “case” is sometimes performed using an indirect jump);
- returns from procedure;
- some procedure calls like function-pointer calls.

⁴At a given place in the code, dead registers are ones which will be written before they are read. So, at this place, they can be used by the inserted code.

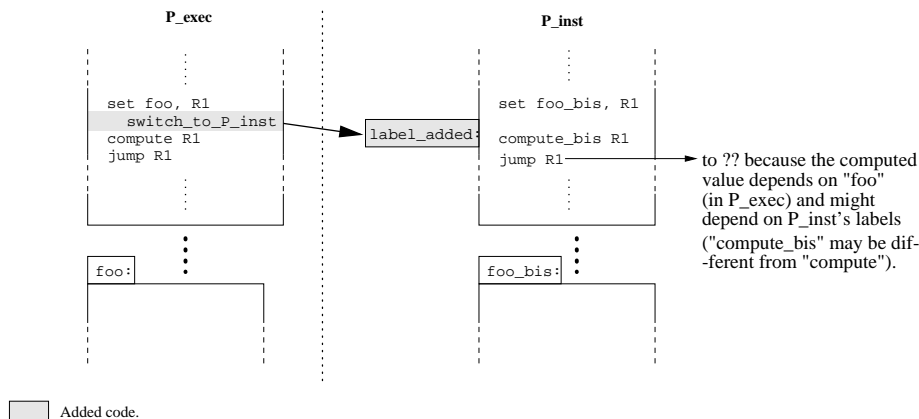


Figure 3: Problem with indirect jumps.

More generally, from our experiments on compiler-generated codes, indirect jumps appear either inside a procedure like C-switch statements (the target address points out to an instruction *into* the procedure and is locally processed), or as a procedure call or return.

Another type of indirect jump exists: `setjmp/longjmp` C-statements. We ignored, here, these functions because they typically execute when abnormal execution happens. But, as shown in Figure 4, some code can be added just after “`setjmp`” statement (“`call setjmp`” in assembler) to make a switch to the other code copy if required.

Consequently, **calvin** implements the mode switch round a procedure call (just before or just after). This guarantees that indirect jumps inside a procedure (assembly code of C-switch statements) will execute correctly.

Returns from procedures are managed by inserting code at the return point to ensure the PC is right. This code makes sure the execution resumes at the right place, otherwise makes a jump to the homologous code. Figure 4 illustrates how **calvin** handles this difficulty⁵.

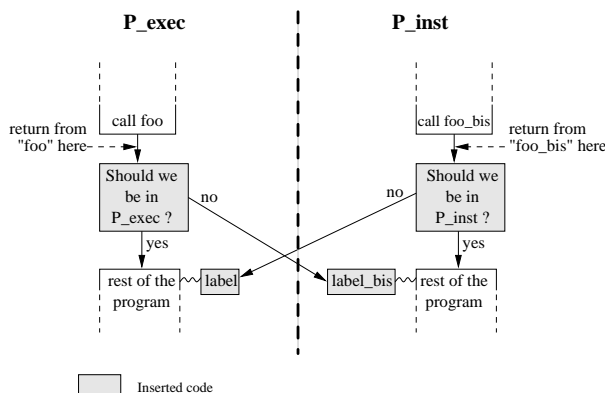


Figure 4: The management of the returns from procedure.

3.2.4 calvin’s modes

There are numerous solutions to execute the switch between both copies of code, upon a “call” event. Figure 5 describes the code section we inserted to perform the switches upon procedure calls on an internal event (*inner* mode of **calvin**). This code section will perform the switch from “no-trace collection” mode to “trace collection” mode after $P_exec_threshold$ procedure calls (and vice-versa). First, at the beginning of the procedure, a

⁵The answer to “Should we be here?” is stored in a boolean variable updated each time the execution switches to another copy of the code.

mode check is done because the procedure might have been called with an indirect call. Then a counter is incremented and tested. If required, the switch is performed. This mode allows reproducible tracing/on-line simulation chains.

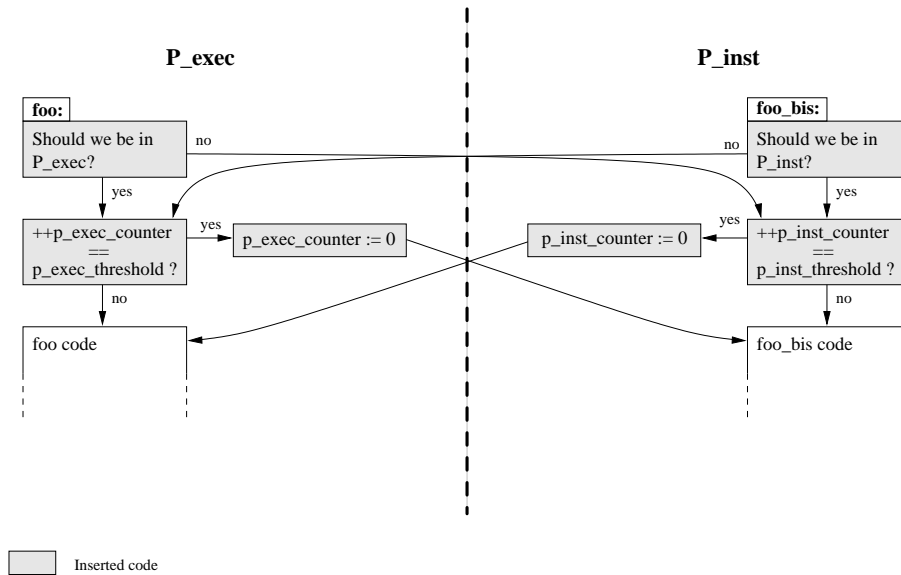


Figure 5: Switching from one copy of code to the other.

For collecting information, we may also rely on external events. Such a mode switch is implemented as follows (*shm* mode of **calvin**): a shared variable, called *Is_exec*, is periodically read by the two clones of the tested application. According to its value, a given copy of the code is executed (i.e. the switch is performed or not). An external process controls the execution mode by writing *Is_exec*. In this case, *Is_exec* is read in the traced process when each internal event appears (procedure call). The inserted code in the cloned application is slightly shorter than for the *inner* mode of **calvin**, because there is no need for internal counters.

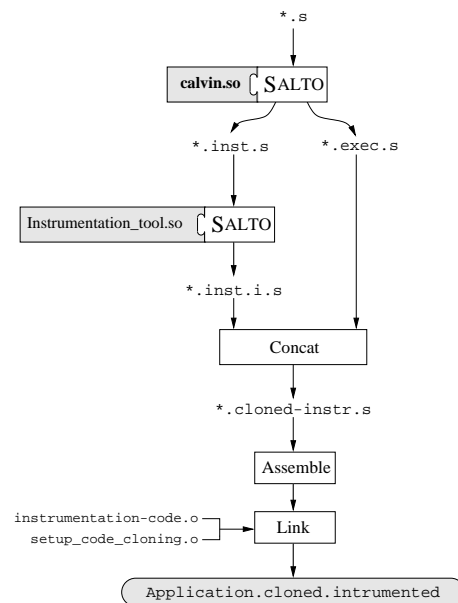


Figure 6: Generating a cloned-instrumented application.

3.2.5 Making the Trace User’s Instrumentation Possible

calvin serves as a framework for a trace user who needs to collect a trace. He/she will only have to instrument P_inst .

Figure 6 illustrates the approach. **calvin** builds two code copies, then the trace user provides its own instrumentation which is applied to P_inst . Finally both copies of the application are assembled, and then linked with specific instrumentation code.

4 Experimental Results

We provide here some experimental results on the distribution of the events in the tested applications, and on execution times using cloned applications with each of **calvin**’s execution mode (*inner* and *shm*) and their induced overhead.

calvin has been implemented on a SPARC architecture, and the experimental results were collected from a Sun Ultra 30 Model 295 with a 295 MHz UltraSPARC-II processor and 128 MB of memory, running Solaris 2.5.1.

In order to validate our approach, we instrumented the popular SPEC95 integer benchmark suite (CINT95). These programs were compiled with GCC (version 2.7.2.1) without any optimization option. The option ‘-Wa,-xarch=v8plus’ allowed the SPARC-V8plus instructions to save/restore the integer condition codes. Each run on this benchmark suite was tested on the *ref* input data set.

4.1 Distribution of the Events in the Tested Code

Table 1 first presents some statistics about the events we chose and their distribution among the executed instructions. These results were collected using an instrumentation on the full code. We define a *distance* between two internal events (here procedure calls) as: the number of executed instructions between the events.

	Millions instr. executed	Millions events executed (ratio [†])	Distances between events			
			min.	mean	max.	std. de- viation
099.go	69,562	353 (0.51 %)	3	197	349,200	393
124.m88ksim	159,286	1,059 (0.67 %)	3	150	456,799	788
126.gcc *	2,103	18 (0.89 %)	3	113	1,747,793	776
129.compress	89,592	1,148 (1.28 %)	13	78	1,622,898	168
130.li	106,685	1,710 (1.60 %)	3	62	646,719	18,487
132.jpeg	57,932	46 (0.08 %)	3	1,248	3,309,149	8,665
134.perl	29,564	404 (1.37 %)	4	73	3,766,198	226
147.vortex	124,057	1,407 (1.13 %)	7	88	431,542	1,648

[†]. $100 \times (\text{number of events}/\text{number of instructions})$.

*. The information supplied here concerns only one of the 56 executions performed by the benchmark with the *ref* input data set. Other executions provide quite similar results.

Table 1: Statistics about application procedure calls within executed instructions in SPEC95 integer benchmarks.

We can notice that the results of Table 1 depend a lot on the tested program. For instance, even if *132.jpeg* and *130.li* event ratio are very close (the difference is a small absolute value), the average distance between two events in these two programs are very different (1,248 vs. 62). This means that the control on the switches between P_exec and P_inst will be much finer in *130.li* than in *132.jpeg*. On the other hand, the standard deviation of the distances between the events give an idea of the regularity of the distribution of the events among the code. *130.li* has got the worse standard deviation, so the control of the switches between the two

cloned copies might be very good (events close to each other) in a given portion of code, and very bad in other portions of code (events far from each other).

Globally, the disparity of the results presented in Table 1 points out the necessity to have a per-application type of events in order to obtain:

1. a better balance of the events/instructions ratio executed between the tested applications;
2. a better regularity in the distribution of the events between instructions.

This will be one of our future works. This is particularly important if we also want to be able to provide meaningful sample traces on scientific programs where millions or even billions of instructions may be executed on a single procedure call.

However, as we expected, the events do not have a too high frequency in the executed code, but they are sufficiently present, in the range of about 1%⁶. So we may expect that the slowdown induced by **calvin**'s inserted code will not be prohibitive.

4.2 Execution Times on the SPEC95 Integer Benchmarks

In order to quantify the overhead induced by **calvin**, we present execution times on the SPEC95 integer benchmark suite with the *ref* input data set.

Here, we also acted as trace users and we instrumented in a quite straightforward way the *P_inst* copy of the cloned applications to get data references (code was inserted near each load or store instruction). These (quite expensive) sections of code save registers and condition codes, and call a routine which processes the references. For the cloned-instrumented applications presented here, this routine simulates some data cache configurations which are set up before execution. This routine is very expensive and may induce a slowdown in the range of 100 if executed within the whole application.

The different program versions we ran were:

- the stand-alone program which gives the base execution time;
- two cloned-instrumented versions simulating six different 32 KB data cache configurations (from direct mapped to 4-way set-associative with various placement and replacement algorithms). In these two versions, the user-instrumentation provided is the same, while the cloning mode is different: the first (*inner* mode) makes switches upon internally counted events (procedure calls), and the second (*shm* mode) makes switches according to a shared variable's value.

Table 2 presents the execution times of each SPEC95 integer benchmark for different executions of the programs cited above. Each execution time presented is the longest one of two executions (except for the base time which is the shortest of two executions), and the sum of user and system execution time in seconds. The number in parenthesis represents the slowdown of the concerned workload which is computed as follows:

$$\text{slowdown} = \frac{\text{tested_user_time} + \text{tested_system_time}}{\text{base_user_time} + \text{base_system_time}}$$

The execution times are displayed for several executions of the same workload, with different switch patterns.

"*P_exec* & *P_inst*" switch pattern corresponds to a normal execution of the instrumented programs. The switches from *P_exec* to *P_inst* are performed every 1000 events, and the switches from *P_inst* to *P_exec* are done every 10 events. Therefore, the number of the data references simulated is order of magnitude of 1% of the total data references. The results given here are only to show an upper bound of the cost of a sampling simulation: the switches are voluntary very frequent and may induce frequent instruction and data caches flushes, therefore increasing the slowdown. Moreover, the trace samples simulated here (a few hundreds or thousands references for each sample) are not large enough to give a realistic simulation result (this was not our goal).

We do not display the corresponding result coming from **calvin**'s *shm* execution mode. It is not possible to simulate exactly the same reference sequence since the process which manages the external variable (*Is_exec*) is synchronized with the system clock.

⁶The exact mean is 0.94%.

	Base	Shm mode		
		<i>P_exec</i> only	<i>P_exec</i> only	<i>P_exec</i> & <i>P_inst</i>
099.go	321 (1.00)	347 (1.08)	396 (1.23)	805 (2.51) [190 M]*
124.m88ksim	604 (1.00)	682 (1.13)	691 (1.14)	1,411 (2.33) [453 M]*
126.gcc	318 (1.00)	366 (1.15)	378 (1.19)	722 (2.27) [155 M]*
129.compress	321 (1.00)	388 (1.21)	382 (1.19)	773 (2.41) [188 M]*
130.li	544 (1.00)	619 (1.14)	630 (1.16)	1,348 (2.48) [369 M]*
132.jpeg	230 (1.00)	233 (1.01)	246 (1.07)	580 (2.52) [193 M]*
134.perl	183 (1.00)	240 (1.31)	227 (1.24)	401 (2.19) [77 M]*
147.vortex	617 (1.00)	681 (1.10)	722 (1.17)	1,765 (2.86) [498 M]*

*. Millions references simulated.

Table 2: Execution times of the cloned-instrumented SPEC95 integer benchmarks.

“*P_exec* only” estimates Code Cloning Tracing’s overhead in no-trace collection mode (no mode switch is executed). For internal event switches (**calvin**’s *inner* mode), the threshold allowing a switch from *P_exec* to *P_inst* is set to a value higher than the number of executed events with the *ref* input data set. For the external event switching version (**calvin**’s *shm* mode), the shared variable keeps the adequate value during the whole execution of the concerned workloads. As expected, the execution overheads for the two versions of the cloned applications are globally in the same range: generally around 10 to 20%. Then this makes possible to consider memory hierarchy or micro-architecture studies on large application incurring hours of CPU.

5 Summary and Future Work

Most of the popular trace collection software methods only allow to collect user instructions on single processes. They also induce a slowdown incompatible with tracing large applications incurring billions or trillions of instructions. On the other hand, to avoid misleading conclusions, micro-architecture and memory systems studies require more and more accurate traces including kernel activities and multiprocess workloads.

Ideally, one would like to collect traces representing the whole punctual activity of a workstation for a few second. Such a collection cannot be realistically considered if trace collection induces a slowdown of 10 or higher on the whole activity of the computer, particularly before the trace collection: no *real* user may tolerate such a slowdown.

The Code Cloning Tracing method we have presented in the paper is a first step towards the resolution of this difficulty. Code Cloning Tracing is based on sampling the execution and on a static code duplication and instrumentation of the original code. A no-trace collection code copy is nearly identical to the original code, except that *events* allow mode switching. This copy will be executed most of the time. The second copy is instrumented by the trace user and will produce traces to feed simulators. This copy may have a very significant slowdown (let us say 100), but as it is parsimoniously executed, it allows to collect trace samples on very long applications. Code Cloning Tracing can be implemented working either on assembly codes or executables. We preferred the first method as this favors portability on different platforms (software and hardware).

In this paper, we have presented in details the basic principles of Code Cloning Tracing and some technical issues related to its implementation. The **calvin** prototype we built has been currently tested on single process applications. It has shown to incur a very acceptable slowdown in no-trace collection mode (around 1.2). Such a low slowdown seems to be acceptable for most users.

Future developments will first include collecting traces on a multiprocess workload (using a shared variable like **calvin**’s *shm* mode). Finally, we plan to use Code Cloning Tracing to completely instrument a desktop computer including all user applications, operating system and libraries. The Linux operating system running on a SPARC machine will be our main target.

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