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► **To cite this version:**

Olfa Mosbahi, Leila Jemni Ben Ayed, Jacques Jaray. Formal Development Method of Automated Systems using the Temporal Logic of Actions TLA. MOSIM'06 6ième Conférence Francophone de Modélisation et Simulation des Systèmes, Apr 2006, Rabat, Morocco. inria-00102229

HAL Id: inria-00102229

<https://hal.inria.fr/inria-00102229>

Submitted on 29 Sep 2006

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Formal development method of automated systems using the temporal logic of Actions TLA+

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Abstract : *The paper presents a method for control systems formal development. We focus on the refinement process used for the development of a control part controlling an operative part of an automated (controlled) system satisfying requirements. We first build an abstract model of both operative and control parts and complete this model to get a model of the automated system. The next steps consists in refining the control part and the operative one to get a model of the automated system capturing every important feature. The method is developed through a case study : a parcel sorting system. We use the temporal logic of actions TLA+ which deals with refinement and proved usfull for the specification and the verification of safety and liveness properties.*

Key-Words : *Modelling, Model checking, Refinement, Automated systems, TLA+, Validation.*

1 INTRODUCTION

Reactive systems are systems that react continuously to their environment and require description of allowed patterns of behaviour for their specification, rather than a simple input/output relation. Reactive systems often involve concurrent execution of processes (in order to achieve responsiveness requirements, or because of inherent distribution in the application) and requirements on system states that must not arise (safety constraints), the reachability of some states (liveness constraints) and timing constraints between system responses to events. Among reactive systems are most of the industrial real-time and embedded systems, such as control and supervision systems. They are critical systems and require a high level of safety and reliability. To reach a necessary degree of reliability and safety, it would be quite interesting to lay out a specification approach which simplifies the requirement description, deals with mathematical notations inducing verification and validation.

In this paper, we propose a development method of automated (controlled) systems. In such systems, we distinguish a software part : the "controller" and an operative part formed by a physical device and its environment which the behavior can be observed through variables and on which it is possible to re-

act with effectors. The controller and the operative part form what is called the controlled (or automated) system. The goal is that the controller acts on the operative part such as the controlled system satisfies required properties. So, we are concerned with the correctness of the controller. The usual way to deal with the correctness of a program is to prove that the interaction with its operative part satisfies some required properties.

We take a different point of view and notice that the user is satisfied by the behaviour of the controller as soon as the automated system behaves in the expected way. Our development method consists in building an abstract discrete model of both operative and control parts. The model of the operative part is completed to obtain a controlled or automated system. Correctness concerns the controlled system, provided the model of the operative part is accurate. Successive refinements are used to produce final model for each of the control and the operative parts. At each refinement level new required properties are added to the model and have to be verified by the automated system. The last refinement step shall verify all requirements. These insures that the final model will be correct since all required properties have been verify (Figure 2). The quality of the last refinement supposed to model the controlled system depends on the first abstract models of both of

the control and operative parts.

Several formal methods have been proposed for the development of control systems, most of them relay on set theory such as B, VDM and Z other ones relay on temporal logic such as PLTL and TLA. To build our models we use the temporal logic of actions TLA+ which has been proved useful for the specification and the verification of safety, liveness and fairness properties (Lamport 2002, Lamport 1998, Lamport 1993), deals with refinement process, which we need in our approach, and is provided with a powerful model checker TLC (Lamport 2002) which can be used to simulate the automated system and to prove the correctness of the development. The expected behaviour of the controlled system is expressed by means of invariants and temporal properties. The model is then refined such that its operative part gets closer to the real operative one.

The paper is organized as follows : section 1 presents an overview of the temporal logic of actions, section 2 a description of the development method that we propose. In section 3 the method is illustrated by a parcel sorting case study and the model checker TLC is used.

2 TLA+ : THE TEMPORAL LOGIC OF ACTIONS

TLA+ is a language intended for the high level specification of reactive, distributed, and in particular asynchronous systems. It combines the linear-time temporal logic of actions TLA (Lamport 1991, Lamport 1994), and mathematical set theory. The language has a mechanism for structuring in the form of modules, either by extension, or by instance.

The semantics of TLA is based on state behaviors of variables. It can be viewed as a logic built in an incremental manner in three stages :

1. predicates having as free variables rigid and flexible variables and whose semantics is based on states. A state s satisfies the predicate P if and only if $s[[P]]$ is true. $s[[P]]$ is the value obtained by substituting in P variables by its values in the state s (for instance $s[[x = 0]] \equiv s[[x]]^1 = 0$),
2. actions which are logical formulas having primed flexible variables as well as free variables and whose semantics is based on pairs of states. A pair of state $\langle s, t \rangle$ satisfies the action A if and only if $s[[A]]t$ is true. $s[[A]]t$ is the value obtained by substituting in A unprimed variables

by its value in the state s and primed variables by its values in the state t . (for instance $s[[x' = x + 1]]t \equiv t[[x]] = s[[x]] + 1$). As predicates are actions we thus have $s[[P]]t \equiv s[[P]]$ and $s[[P']]t \equiv t[[P]]$,

3. temporal formulas of actions (addition of the operator \Box) whose semantics is based on state behaviors of variables. $\langle s_0, s_1, \dots \rangle [[\Box T]]$ (a behavior $\langle s_0, s_1, \dots \rangle$ satisfies $\Box T$) is true if and only if $\forall n \in \text{Nat} : \langle s_n, s_{n+1}, \dots \rangle [[T]]$. As an action is a temporal formula we have $\langle s_0, s_1, \dots \rangle [[A]] \equiv s_0[[A]]s_1$.

A TLA specification looks like : $\text{Init} \wedge \Box[\text{Next}]_x \wedge L$
Where :

1. Init is the predicate which specifies initial states ($s_0[[\text{Init}]]$),
2. $\Box[\text{Next}]_x$ means that either two consecutive states are equal on x , $x' = x$ (stuttering), or Next is an action (a relation) which binds two consecutive states by using the variable (not primed) for the first state and the primed variable for the second state ($\forall n \in \text{Nat} : s_n[[\text{Next}]]s_{n+1}$),
3. L is a fairness assumption (strong or weak) on actions $A : A \Rightarrow \text{Next}$. $WF_{\text{unprimed variables}[[S]]}(s)$ defines the condition of weak fairness over the system S and $SF_{\text{unprimed variables}[[S]]}(s)$ defines the condition of strong fairness over the system S .

We use TLA to prove invariance properties ($\text{Spec} \Rightarrow \Box I$), eventuality properties ($\text{Spec} \Rightarrow \Diamond F$) or $\text{Spec} \Rightarrow (P \rightsquigarrow Q)$ and refinement properties ($\text{Spec_ref} \Rightarrow \text{Spec_abs}$). Unlike most other temporal logics, TLA is intended to support stepwise system development by refinement of specifications. The basic idea of refinement consists in successively adding implementation detail while preserving the properties required at an abstract level. In a refinement-based approach to system development, one proceeds by writing successive models, each of which introduces some additional detail while preserving the properties of the preceding model. Fundamental properties of a system can thus be established at high levels of abstraction, errors can be detected in early phases, and the complexity of formal assurance is spread over the entire development process. A system S_1 is refined by a system S_2 , when : $\text{Specification}(S_2) \Rightarrow \text{Specification}(S_1)$. A refinement S_2 preserves all TLA properties of an abstract specification S_1 if and only if for every formula F , if $S_1 \Rightarrow F$ is valid, then so is $S_2 \Rightarrow F$. This condition is in turn equivalent to requiring the validity of $S_2 \Rightarrow S_1$.

Because S_2 will contain extra variables to represent the lower-level detail, and because these variables will change in transitions that have no counter-part at the abstract level, stuttering invariance of TLA formulas is essential to make validity of implication a reasonable definition of refinement.

Proofs in TLA (can be found in (Lamport 1991)) are carried out using proof rules and rules of the classical logic. It is clear that the main advantage is that every thing is stated as a logical object and relations as probability or refinement are stated using the logical implication. The refinement states a property between logical formulae interpreted over external behaviour.

TLA+ (Lamport 2002, Lamport 1998) is an extension of TLA including predicate calculus and ZF set theory and mechanisms for structuring a specification in a module. A module is a text containing a name, a list of definitions (constants, variables, operators, functions, predicates, assumptions, theorems, proofs). A specification is made up of several modules that are combined using clauses EXTENDS and INSTANCE. The clause EXTENDS imports the definitions of specified modules by a macro expansion mechanism; the clause INSTANCE provides the mechanism of parametrization and a bloc-like structure. A module TLA+ is represented as follows :

```

MODULE <Name>
CONSTANTS <List of constants>
VARIABLES <List of variables>
ASSUME <properties of constants>
TYPE INVARIANT
INIT <initialization of variables>
SPEC <SpecInit  $\wedge$   $\square$ [Next]x  $\wedge$  L>
INVARIANT <Safety properties>
LIVENESS <Liveness properties>
THEOREM
END

```

3 THE DEVELOPMENT METHOD OF AUTOMATED SYSTEMS

In this section we present a development method of automated systems. The problem can be stated as follows: being given an operative part (physical device and an environment) which behavior is observed through a number of variables and on which it is possible to act by means of effectors. Some of the variables are controllable, they correspond to the results of some actuators, other are not controllable and their

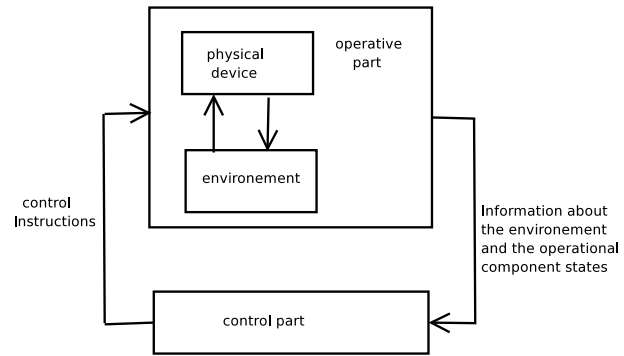


Figure 1. Automated system in closed loop

changes correspond to the effect of some physical process we do not control. The behavior of the automated system can be represented by a closed loop expressing relation between operative and control parts (Figure 1). We have to develop a control program which observes the operative part and change controllable variables such that the automated system meets some requirements . The modeling method that we propose uses the temporal logic of actions (TLA+) and consists on the following steps:

1. First of all, we build an abstract model of the operative part, an abstract model of minimal controller in defining its actions with guards as weak as possible, just taking into account safety requirements. The resulting system is a “*permissive*” controlled system, it is likely not the case that all behaviors meet the automated system requirements but some should.
2. We have to prove the last part of this assertion, if not we can deduce that it is not possible to achieve the expected system. This point is similar to the notion of controllability in control theory (Ramadge & Wonham 1989). Then, we constrain the *permissive* controlled system as a module TLA+ and we use the model checker TLC to verify some safety requirements.
3. Successive refinements can be used to produce the concrete model of the automated system formed by physical device and its environment and the concrete control system through the addition of expected behaviour with the addition of new actions, variables and new properties for each of the operative and the control parts (Figure 2). Related verifications are discharged. At each refinement step, new module modeling operative and control parts is shown to satisfy new automated system requirements. The refinement process stops when the automated system model meets the desired properties. The quality of the last refinement supposed to model the controlled

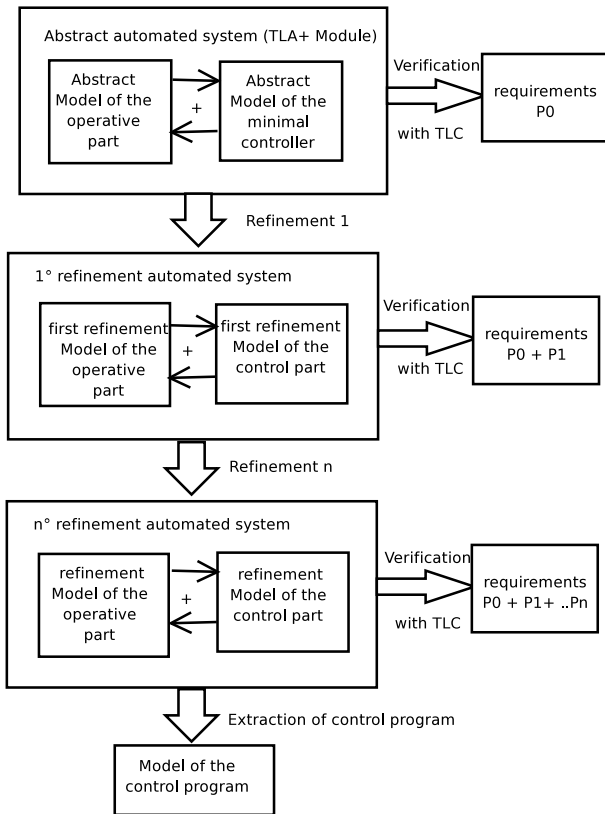


Figure 2. Development method of automated systems

system depends on the first operative part model.

4. The last step consists in separating what concerns the operative component from what concerns the controller in order to build a program of the controller,

4 CASE STUDY : A PARCEL SORTING DEVICE

Our method is illustrated by a case study : the development of a parcel sorting system (JARAY & A.Mahjoub 1994). We start with an informal description of the problem and then apply our method.

4.1 Informal description of the problem

The problem is to sort parcels into sorting baskets according to an address written on the parcel. In order to achieve such a sorting function we are provided with a device made of a feeder connected to the root of a binary tree made of switches and pipes as shown Figure 3. The switches are the nodes of the tree, pipes

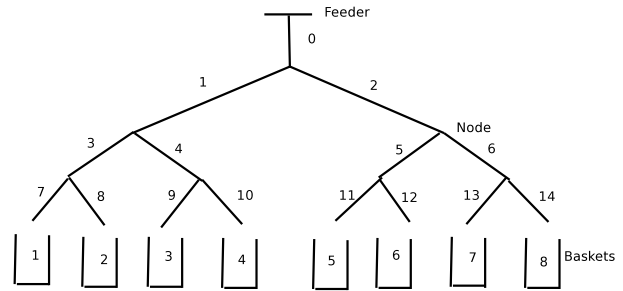


Figure 3. Sorting plant

are the edges and leaves are the baskets. A parcel, thanks to gravity, can slide down through switches and pipes to reach a basket. A switch is connected to an entry pipe and two exit pipes, a parcel crossing the switch is directed to an exit pipe depending on the switch position. The feeder releases a parcel at once in the router, the feeder contains a device to read the address of the parcel to be released. When released, a parcel enters a first switch (the root of the binary tree) and slides down the router to reach a basket. The controller can activate the feeder and change the switches position. For security reasons, it is required that switch change should not occur when a parcel is crossing it. In order to check this condition, sensors are placed at the entry and the exits of each switch.

4.2 Abstract model of the system

The controller extends the operative part in order to produce the controlled system. The first step of the method is to build an abstract model of the operative part and an abstract model of the control part using inputs from the informal presentation. The operative part is formed by the sorting device and the parcel behaviour.

The sorting device. The sorting device consists of a feeder and a sorting layout. The feeder has two functions : selection of the next parcel to introduce into the sorting layout and opening the gate (releasing a parcel in the sorting layout). We introduce the actions *select* and *release* to capture the two functions. In order to produce the abstract model of the sorting layout, we have to notice that a given state of the switches forms a *channel* linking the entrance to only one sorting basket. A basket is an element of a set named *Basket*. In future refinements, the tree structure of the sorting device will be introduced and the sorting baskets will be classed as nodes of the tree (leaves). Therefore, we introduce the set *Nodes* having *Basket* as a proper subset. Channels and sorting baskets are in a one to one correspondence. Therefore, the abstract model of the sorting device can be

reduced to a single variable *channel* taking the value of the sorting basket it leads to, namely a value in the set *Basket*. The *channel* value is changed by the action *set_channel*, defined in the control part. The full description of the actions will be given in the controller section. It is worth noticing that the abstraction forces a "sequential functioning" of the sorting device, i.e. the value of the channel remains unchanged as long as the parcel released in the sorting device has not reached a sorting basket. Up to a point, it is indeed possible to sort more than one parcel simultaneously. The chosen abstraction does not allow such a concurrency.

Parcels. Parcels, as part of the environment, are components of the operative part and are represented as elements of a set we name *PARCELS*. We use a total function (*adr*) from *PARCELS* to the interval *Baskets* to refer to the parcels address. The action concerning a selected parcel to sort is the crossing of the sorting device up to the basket the channel leads to. We give the status "arrived" to the parcel which has reached a sorting basket. The variable (*arrived*) is a total function from *PARCELS* to *Baskets*. The goal of the sorting system is to decrease the set of the parcels to sort. The variable *sorted* represents the set of sorted parcels. The remaining parcels are defined by the expression *PARCELS* - *sorted* named *UNSORTED*. *pe* describes an entry parcel. As *pe* is undefined when the sorting device is empty, we have introduced a set *PPARCELS* of which *PARCELS* is a proper subset; *pe* is an element of *PPARCELS* and assignment of any value in *PPARCELS* - *PARCELS* stands for "undefined". The expression *PPARCELS* - *PARCELS* will be referred as *NOPARCELS*. The action *select_parcel* is introduced and is activated once the device is free and the variable *pe* is undefined, which means it does not exist a parcel being sorted.

$$\begin{aligned} \text{select_parcel} &\triangleq \wedge \text{sorting} = \text{free} \\ &\wedge \text{pe} \in \text{NOPARCELS} \\ &\wedge \exists p \in \text{UNSORTED} : \wedge \text{pe}' = p \\ &\wedge \text{UNCHANGED} \langle \text{channel}, \text{arrived}, \\ &\quad \text{sorted}, \text{sorting}, \text{ready_to_sort} \rangle \end{aligned}$$

Moving parcels. In our abstraction, a parcel takes no time to travel from the feeder to a basket. A parcel arrives in the basket to which the channel leads up. When the action *cross_parcel* occurs, the current parcel sorting is finished and then, of course, the current parcel becomes undefined.

$$\begin{aligned} \text{cross_parcel} &\triangleq \wedge \text{sorting} = \text{busy} \\ &\wedge \text{arrived}' = [\text{arrived} \text{ EXCEPT } ![\text{pe}] = \text{channel}] \\ &\wedge \text{sorted}' = \text{sorted} \cup \{\text{pe}\} \\ &\wedge \text{pe}' \in \text{NOPARCELS} \wedge \text{sorting}' = \text{free} \\ &\wedge \text{UNCHANGED} \langle \text{channel}, \text{ready_to_sort} \rangle \end{aligned}$$

The safe operating physical layout. From the existing (physical) layout, it is possible to build different controlled systems having different behaviors. It is natural to dismiss systems having unsafe behaviors. The controlled system which respects only these safety constraints, is called the *permissive* controlled system. In this automated system, *permissive* controller system actions are the *set* and *release*.

$$\begin{aligned} \text{set_channel} &\triangleq \wedge \text{sorting} = \text{free} \wedge \text{pe} \notin \text{NOPARCELS} \\ &\wedge \text{ready_to_sort} = \text{FALSE} \wedge \text{channel}' \in \text{Baskets} \\ &\wedge \text{ready_to_sort}' = \text{TRUE} \\ &\wedge \text{UNCHANGED} \langle \text{arrived}, \text{sorted}, \text{pe}, \text{sorting} \rangle \end{aligned}$$

Here, the value assigned to the variable *channel* is randomly determined. This justifies the "permissive" qualification. The guard fulfills a safety constraint in preventing a change of the channel when a parcel crosses the sorting device, preventing the parcel to be damaged.

$$\begin{aligned} \text{release} &\triangleq \wedge \text{ready_to_sort} = \text{TRUE} \\ &\wedge \text{sorting} = \text{free} \wedge \text{pe} \in \text{PARCELS} \\ &\wedge \text{sorting}' = \text{busy} \wedge \text{ready_to_sort}' = \text{FALSE} \\ &\wedge \text{UNCHANGED} \langle \text{channel}, \text{arrived}, \text{sorted}, \text{pe} \rangle \end{aligned}$$

The first part of the guard ensures that a single parcel, at once, is being sorted, the second that a parcel may be selected before being released.

4.3 The permissive controlled system

The actions section of the TLA system modelling the permissive controlled system is obtained by merging the controller actions. The action *select_parcel* assigns the (next) current parcel to the variable *pe*. Once selected this parcel remains the current one as long as it has not reached a sorting basket. This captures a non explicit property of the feeder in which the parcels are stored in a first out order.

4.3.1 Controllability

The permissive controller behavior will likely not satisfy user's needs, in the sense that we can not certify that every parcel arrives in the right basket. Conversely, if we can verify that it is never the case that a parcel arrives in the expected basket, we assert that the system is not controllable with respect of the following requirement **P** and the development process stops here.

$$\forall p \in \text{PARCELS} : p \in \text{DOMAIN } \text{arrived} \rightsquigarrow \text{arrived}[p] = \text{adr}[p]$$

4.3.2 The first abstract controlled system

As far as we did not take into account the destination address of the parcel we can not expect a parcel to reach the right basket. The expected behavior of the automated system is defined in the requirement P. We add it to the invariant part of the TLA model. When attempting to verify the model, we expect a failure and use the report to find out what should be changed to satisfy the requirements. We deduce that it is necessary to change $channel' \in Baskets$ into $channel' = adr[pe]$ in the action $set_channel$ in order to transform the permissive controlled system into an abstract model of the expected controlled system. The technique may seem less convincing as the case study at the level of abstraction is rather simple, but we claim its interest when we are faced to combinatorial situations. The parcel routing is a particular routing, obtained while making the control action more deterministic. At this abstract level, we have to prove the property asserting that any parcel which is presented in entry will arrive at the destination bin expressed by the following property:

$$\forall p.(p \in PARCELS \wedge p \in dom(arrived(p)) \Rightarrow arrived(p) = adr(p))$$

The abstract model of the automated system will be presented in the annexe A.

4.4 First refinement

In the proposed method, we refine in parallel each of the operative and the control parts adding new variables, properties, actions or redefining some of them.

Refinement of the operative part. In the abstract model, the physical device of the system is reduced to a single variable $channel$. A channel value corresponds to a setting of node switches. In order to represent a more concrete sorting device, we introduce the nodes $Node$ as elements in an interval of integers, the level of the nodes in the sorting device tree structure (the root node corresponding to the level 0) $Level$, a function adr_level which gives at a given level the node to be visited to reach a given basket, a variable function $path_level$ which associates to each level the node visited. The left successor of the node i is labelled $2 * i + 1$ and the right one is $2 * i + 2$. The root node is labelled 0. A pipe has the label of the node it "feeds". The leaves (terminal nodes) of the tree correspond to the baskets introduced in the abstract model.

The action $cross_parcel1$ was extended by strengthen-

ing its guard with the condition ($l = n$), meaning that the variable l has reached the baskets level and the action has been modified. $arrived(pe)$ is assigned the value $adr_level[adr[pe], n]$, the (basket) node where the current parcel ends its run. The actions $cross_parcel$ and $select_parcel$ are redefined as follows :

$$\begin{aligned} cross_parcel1 &\triangleq \wedge sorting = busy \wedge l = n \\ &\wedge arrived' = [arrived \text{ EXCEPT } ![pe] = adr_level[adr[pe], n]] \\ &\wedge sorted' = sorted \cup \{pe\} \wedge pe' \in NOPARCELS \\ &\wedge sorting' = free \\ &\wedge \text{UNCHANGED } \langle channel, ready_to_sort, \\ &\quad path_level, i, l, ctrl_cross \rangle \\ select_parcel1 &\triangleq \wedge select_parcel \wedge i' = 0 \\ &\wedge l' = 0 \wedge path_level' = [path_level \text{ EXCEPT } ![0] = 0] \\ &\wedge \text{UNCHANGED } \langle ctrl_cross \rangle \end{aligned}$$

In the abstract model, the crossing of the current parcel concerned a single action $cross_parcel$, here we are concerned by the passing of the parcel through different nodes. Two next actions $cross_right$ and $cross_left$ are introduced to determine the next node to be visited which mean updating the variable i representing the current node. The variable l representing the current level is updated as well. Updating the current node variable depends on the value of $path_level(l+1)$. If the value is odd the next node to reach is the left son otherwise it is the right son. The coding of the nodes makes the computation of the left or right son easy.

$$\begin{aligned} cross_left &\triangleq \wedge ll < = n \wedge i \in Internal_N \\ &\wedge sorting = busy \wedge ctrl_cross = TRUE \\ &\wedge path_level[l + 1] = 2 * i + 1 \wedge l' = l + 1 \\ &\wedge i' = 2 * i + 1 \wedge ctrl_cross' = FALSE \\ &\wedge \text{UNCHANGED } \langle channel, sorting, pe, arrived, \\ &\quad sorted, ready_to_sort, path_level \rangle \\ cross_right &\triangleq \wedge ll < = n - 1 \wedge i \in Internal_N \\ &\wedge sorting = busy \wedge ctrl_cross = TRUE \\ &\wedge path_level[l + 1] = 2 * i + 2 \wedge l' = l + 1 \\ &\wedge i' = 2 * i + 2 \wedge ctrl_cross' = FALSE \\ &\wedge \text{UNCHANGED } \langle channel, sorting, ready_to_sort, \\ &\quad pe, arrived, sorted, path_level \rangle \end{aligned}$$

The actions $select_parcel1$, $cross_left$, $cross_right$ and $cross_parcel1$ form the operative part of the automated system.

Refinement of the control part. In this refinement, we have added two new actions $path_right$ and $path_left$. The actions $path_right$, $path_left$, $set_channel1$ and $release1$ form the control part of the automated system. The function $path_level$ concerning the current parcel is updated "on the fly" by two actions : $path_right$, $path_left$. The $select_parcel$ gives the opportunity to initialize $path_level(0)$ with 0.

$$\begin{aligned}
& \text{path_left} \triangleq \wedge ii \in \text{Internal_N} \wedge ll < = n \\
& \wedge pe \in \text{PARCELS} \wedge \text{sorting} = \text{busy} \wedge \text{ctrl_cross} = \text{FALSE} \\
& \wedge \text{adr_level}[\text{adr}[pe], l + 1] = 2 * i + 1 \\
& \wedge \text{path_level}' = [\text{path_level} \text{ EXCEPT } ![l + 1] = 2 * i + 1] \\
& \wedge \text{ctrl_cross}' = \text{TRUE} \\
& \wedge \text{UNCHANGED} \langle \text{channel}, \text{ready_to_sort}, \\
& \quad \text{sorting}, pe, \text{arrived}, \text{sorted}, i, l \rangle \\
& \text{path_right} \triangleq \wedge ii \in \text{Internal_N} \wedge ll < = n \\
& \wedge pe \in \text{PARCELS} \wedge \text{sorting} = \text{busy} \\
& \wedge \text{ctrl_cross} = \text{FALSE} \\
& \wedge \text{adr_level}[\text{adr}[pe], l + 1] = 2 * i + 2 \\
& \wedge \text{path_level}' = [\text{path_level} \text{ EXCEPT } ![l + 1] = 2 * i + 2] \\
& \wedge \text{ctrl_cross}' = \text{TRUE} \\
& \wedge \text{UNCHANGED} \langle \text{channel}, \text{ready_to_sort}, \\
& \quad \text{sorting}, pe, \text{arrived}, \text{sorted}, i, l \rangle
\end{aligned}$$

The automated system has to verify the following property :

$$\begin{aligned}
\forall p \in \text{PARCELS} : p \in \text{DOMAIN arrived} \\
\rightsquigarrow \text{arrived}[p] = \text{adr_level}[\text{adr}[p], n]
\end{aligned}$$

The automated system has been processed by the model checker TLC and all properties were verified by the model. The first refinement model of the automated system will be presented in the annexe B.

4.5 Second refinement

This refinement consists in adding details to the nodes. A node is provided with the following sensors : an input sensor, two outputs sensors and a gate which can be set to open the exit to the left pipe or to the right pipe.

Refinement of the operative part : an even more concrete model of the operative part.

The position of the gates is represented by a function *gate* returning for each node a value in : {L, R}. The input sensor of a node is represented by a boolean variable *in* which is initially set to *false*. The output sensors are represented by the Boolean variables *out_l* and *out_r*. These variables become true when a parcel is detected and as we cannot have two outputs at the same time, the output sensors must verify the property $\neg (\text{out_l} \wedge \text{out_r})$.

Two new actions were added to the operative part : *crossing_right* and *crossing_left* modelling the node crossing to go towards the left pipe, respectively right pipe, resulting from this node. The action *cross_parcell*, concerning the operative part is refined by adding to the level of each node the sensor which detects the entry of parcels.

$$\begin{aligned}
& \text{crossing_left} \triangleq \wedge i \in \text{Internal_N} \\
& \wedge \text{in}[i] = \text{TRUE} \wedge \text{gate}[i] = L \\
& \wedge \text{out_l}' = [\text{out_l} \text{ EXCEPT } ![i] = \text{TRUE}] \\
& \wedge \text{out_r}' = [\text{out_r} \text{ EXCEPT } ![i] = \text{FALSE}] \\
& \wedge \text{in}' = [\text{in} \text{ EXCEPT } ![i] = \text{FALSE}] \\
& \wedge \text{UNCHANGED} \langle \text{channel}, pe, \text{arrived}, \text{sorted}, \text{sorting}, \\
& \quad \text{ready_to_sort}, i, l, \text{path_level}, \text{ctrl_cross}, \text{gate}, \text{gate_set} \rangle \\
& \text{cross_left2} \triangleq \wedge \text{cross_left} \\
& \wedge (\text{gate_set} = \text{TRUE} \vee l \geq n - 2) \wedge \text{out_l}[i] = \text{TRUE} \\
& \wedge \text{in}' = [\text{in} \text{ EXCEPT } ![2 * i + 1] = \text{TRUE}] \\
& \wedge \text{gate_set}' = \text{FALSE} \wedge \text{UNCHANGED} \langle \text{out_l}, \text{out_r}, \text{gate} \rangle \\
& \text{crossing_right} \triangleq \wedge i \in \text{Internal_N} \\
& \wedge \text{in}[i] = \text{TRUE} \wedge \text{gate}[i] = R \\
& \wedge \text{out_l}' = [\text{out_l} \text{ EXCEPT } ![i] = \text{FALSE}] \\
& \wedge \text{out_r}' = [\text{out_r} \text{ EXCEPT } ![i] = \text{TRUE}] \\
& \wedge \text{in}' = [\text{in} \text{ EXCEPT } ![i] = \text{FALSE}] \\
& \wedge \text{UNCHANGED} \langle \text{channel}, pe, \text{arrived}, \text{sorted}, \text{sorting}, \\
& \quad \text{ready_to_sort}, i, l, \text{gate}, \text{path_level}, \text{ctrl_cross}, \text{gate_set} \rangle \\
& \text{cross_right2} \triangleq \wedge \text{cross_right} \\
& \wedge (\text{gate_set} = \text{TRUE} \vee ll \geq n - 2) \wedge \text{out_r}[i] = \text{TRUE} \\
& \wedge \text{in}' = [\text{in} \text{ EXCEPT } ![2 * i + 2] = \text{TRUE}] \\
& \wedge \text{gate_set}' = \text{FALSE} \wedge \text{UNCHANGED} \langle \text{out_l}, \text{out_r}, \text{gate} \rangle
\end{aligned}$$

The actions *cross_right2*, respectively *cross_left2* model the pipe crossing, indicating that the parcel passed towards the next left node, respectively towards the right following node. The variable *gate_set*, is used to guarantee that the parcel crossing is possible only if the node gate is positioned in advance. It is assigned the value TRUE by the actions *cross_left2* and FALSE by the action *cross_right2*.

Refinement of the control part. Two new actions were added to refine control part, *set_gate_right* and *set_gate_left*. The action *set_gate_left* model gate opening on the left, by putting $\text{gate}(\text{adr_level}(\text{adr}(pe), ll+2)) := L$, where $\text{adr_level}(\text{adr}(pe), ll+2)$ indicates the current node. The same behavior but towards right side is modelled by the action *set_gate_right*.

$$\begin{aligned}
& \text{set_gate_left} \triangleq \wedge ll < = n - 1 \wedge pe \in \text{PARCELS} \\
& \wedge \text{adr_level}[\text{adr}[pe], l + 1] = 2 * \text{adr_level}[\text{adr}[pe], l] + 1 \\
& \wedge \text{gate_set}' = \text{TRUE} \\
& \wedge \text{gate}' = [\text{gate} \text{ EXCEPT } ![\text{adr_level}[\text{adr}[pe], l]] = L] \\
& \wedge \text{UNCHANGED} \langle \text{channel}, pe, \text{arrived}, \text{sorted}, \text{sorting}, \\
& \quad \text{ready_to_sort}, i, l, \text{path_level}, \text{ctrl_cross}, \text{in}, \\
& \quad \text{out_l}, \text{out_r} \rangle \\
& \text{set_gate_right} \triangleq \wedge ll < = n - 1 \wedge pe \in \text{PARCELS} \\
& \wedge \text{adr_level}[\text{adr}[pe], l + 1] = 2 * \text{adr_level}[\text{adr}[pe], l] + 2 \\
& \wedge \text{gate_set}' = \text{TRUE} \\
& \wedge \text{gate}' = [\text{gate} \text{ EXCEPT } ![\text{adr_level}[\text{adr}[pe], l]] = R] \\
& \wedge \text{UNCHANGED} \langle \text{channel}, pe, \text{arrived}, \text{sorted}, \text{sorting}, \\
& \quad \text{ready_to_sort}, i, l, \text{path_level}, \text{ctrl_cross}, \text{in}, \\
& \quad \text{out_l}, \text{out_r} \rangle
\end{aligned}$$

The actions *path_left2*, respectively *path_right2* having for effect to calculate, the next node to be visited by checking that the gate is opened in the good orientation ($\text{gate}(i) = L$ (left), $\text{gate}(i) = R$ (right)). The

action *select_parcel1*, is also refined en *select_parcel2* by taking into account the sensor which models the parcel presence on the level of the first entry basket.

$$\begin{aligned} path_left2 &\triangleq \wedge path_left \\ &\wedge in[i] = TRUE \wedge gate[i] = L \\ &\wedge UNCHANGED \langle in, out_l, out_r, gate, gate_set \rangle \\ path_right2 &\triangleq \wedge path_right \\ &\wedge in[i] = TRUE \wedge gate[i] = R \\ &\wedge UNCHANGED \langle in, out_l, out_r, gate, gate_set \rangle \end{aligned}$$

Once the parcel is in a node, three actions can be started consecutively, the first is *set_gate_left*, which models gate opening on the left. The same behavior but towards right side is modelled by the action *set_gate_right*. The second executed action is *crossing_left*, respectively *crossing_right*, modelling the node crossing to go towards the left pipe, respectively right pipe, resulting from this node. The third action concerns the pipe crossing and it is modelled by *cross_right2*, respectively *cross_left2*.

The automated system has been processed by the model checker TLC and all properties were verified by the model. These properties are fairness asserting that each action able to be executed is activated at least one time and safety represented as invariant and asserting that we cannot have two outputs at the same time ($\neg(out_l \wedge out_r)$). The second refinement model of the automated system will be presented in the annexe C. As our goal in the proposed method is to develop a control system, we separate what concerns the operative component from what concerns the controller in order to build a program of the controller,

5 Conclusion

In this paper, we have proposed the first steps of a formal method for the development of automated systems using the temporal logic of actions TLA+. The main contribution of the paper consists in building a model of the expected automated system starting from an abstract model of the operative part. In the stepwise approach, the first model consists of an abstract model of the operative part with the guards of the action kept as weak as possible to prevent unsafe behaviors. The only property one can expect from a system, we have called permissive, is that it does not prevent the expected behaviour of the final automated system to occur. If it is the case, we deduce that the system is controllable and that is possible to strengthen the guards in order to obtain an abstract model of the required controlled system. The controlled system is then refined in the usual way.

The refinement, indeed, concerns the operative part and the control part. At the end of the process, we have a detailed model of the automated system. The validation of the operative part, using the animator has been experienced in another work. The proposed method has been illustrated by the example of a parcel sorting system and our approach has simplified the specification of an automated system which meets desired properties.

We have specified also this example with the event based B approach (Abrial 1996b, Abrial 1996a, Abrial & Mussat 1998) and we have verified all proof obligations with the powerful tool AtelierB. In the event based B approach, only invariance and safety properties are considered but fairness and eventuality properties are not considered. The temporal Logic of Actions TLA+, in the other hand, expresses well eventuality and fairness and supports refinement and this is why we have used it in our method. In future works we will develop a method using in the same approach the B method and TLA+ to take benefits of the powerful tool of B and to formulate in TLA+ more natural properties that are not straightforward to express with B and to verify them with the model checker TLC.

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$$\begin{aligned} & \wedge \text{UNCHANGED } \langle \text{channel}, \text{ready_to_sort} \rangle \\ \text{Next} & \triangleq \vee \text{select_parcel} \vee \text{set_channel} \vee \text{release} \vee \text{cross_parcel} \\ \hline \text{trvars} & \triangleq \langle \text{channel}, \text{sorting}, \text{pe}, \text{arrived}, \text{sorted}, \text{ready_to_sort} \rangle \\ \text{TrFairness} & \triangleq \text{WF}_{\text{trvars}}(\text{Next}) \\ \text{Spec} & \triangleq \text{Init} \wedge \Box[\text{Next}]_{\text{trvars}} \wedge \text{TrFairness} \\ \text{Invariant} & \triangleq \wedge \text{sorting} = \text{busy} \Rightarrow \text{channel} = \text{adr}[pe] \\ & \wedge \text{sorting} \neq \text{free} \Rightarrow \text{pe} \in \text{PARCELS} \wedge \text{sorting} = \text{busy} \Rightarrow \text{ready_to_sort} = \text{FALSE} \\ & \wedge \text{ready_to_sort} = \text{TRUE} \Rightarrow \text{channel} = \text{adr}[pe] \\ & \wedge \text{ready_to_sort} = \text{TRUE} \Rightarrow \text{pe} \in \text{PARCELS} \\ \text{Liveness} & \triangleq \wedge \forall p \in \text{PARCELS} : p \in \text{DOMAIN arrived} \leadsto \text{arrived}[p] = \text{adr}[p] \\ & \wedge \forall p \in \text{PARCELS} : p \in \text{DOMAIN arrived} \leadsto \Diamond \text{arrived}[p] \in \text{Baskets} \\ \hline \text{THEOREM } \text{Spec} & \Rightarrow \Box \text{Init} \quad \text{THEOREM } \text{Spec} \Rightarrow \Box \text{TypeInvariant} \\ \text{THEOREM } \text{Spec} & \Rightarrow \Box \text{Invariant} \quad \text{THEOREM } \text{Spec} \Rightarrow \text{Liveness} \\ \hline \end{aligned}$$

Annexe A

$$\begin{aligned} & \text{MODULE } \text{Parcel_Sorting} \\ \text{EXTENDS } & \text{Naturals} \\ \text{CONSTANTS } & \text{PPARCELS}, \text{PARCELS}, \text{Baskets}, \text{noBaskets}, \text{adr}, \text{free}, \text{busy} \\ \text{VARIABLES } & \text{channel}, \text{sorting}, \text{pe}, \text{sorted}, \text{arrived}, \text{ready_to_sort} \\ \text{ASSUME } & \text{adr} \in [\text{PARCELS} \rightarrow \text{Baskets}] \wedge \text{PARCELS} \neq \{\} \wedge \text{Baskets} \neq \{\} \\ & \wedge \text{PARCELS} \subseteq \text{PPARCELS} \wedge \text{PARCELS} \neq \text{PPARCELS} \\ & \wedge \text{noBaskets} \notin \text{Baskets} \\ \text{SortingState} & \triangleq \{\text{free}, \text{busy}\} \\ \hline \text{TypeInvariant} & \triangleq \wedge \text{channel} \in \text{Baskets} \cup \{\text{noBaskets}\} \\ & \wedge \text{sorting} \in \text{SortingState} \wedge \text{pe} \in \text{PPARCELS} \wedge \text{ready_to_sort} \in \text{BOOLEAN} \\ & \wedge \text{arrived} \in [\text{PARCELS} \rightarrow \text{Baskets}] \wedge \text{sorted} \subseteq \text{PARCELS} \\ \hline \text{UNSORTED} & \triangleq \text{PARCELS} \setminus \text{sorted} \\ \text{NOPARCELS} & \triangleq \text{PPARCELS} \setminus \text{PARCELS} \\ \text{Init} & \triangleq \wedge \text{channel} = \text{noBaskets} \wedge \text{sorting} = \text{free} \wedge \text{pe} \in \text{NOPARCELS} \\ & \wedge \text{arrived} = [p \in \text{PARCELS} \mapsto \text{noBaskets}] \wedge \text{sorted} = \{\} \wedge \text{ready_to_sort} = \text{FALSE} \\ \text{select_parcel} & \triangleq \wedge \text{sorting} = \text{free} \\ & \wedge \text{pe} \in \text{NOPARCELS} \wedge \exists p \in \text{UNSORTED} : \wedge \text{pe}' = p \\ & \wedge \text{UNCHANGED } \langle \text{channel}, \text{arrived}, \text{sorted}, \text{sorting}, \text{ready_to_sort} \rangle \\ \text{set_channel} & \triangleq \wedge \text{sorting} = \text{free} \\ & \wedge \text{pe} \notin \text{NOPARCELS} \wedge \text{ready_to_sort} = \text{FALSE} \wedge \text{channel}' = \text{adr}[pe] \\ & \wedge \text{ready_to_sort}' = \text{TRUE} \wedge \text{UNCHANGED } \langle \text{arrived}, \text{sorted}, \text{pe}, \text{sorting} \rangle \\ \text{release} & \triangleq \wedge \text{ready_to_sort} = \text{TRUE} \\ & \wedge \text{sorting} = \text{free} \wedge \text{pe} \in \text{PARCELS} \wedge \text{sorting}' = \text{busy} \\ & \wedge \text{ready_to_sort}' = \text{FALSE} \wedge \text{UNCHANGED } \langle \text{channel}, \text{arrived}, \text{sorted}, \text{pe} \rangle \\ \text{cross_parcel} & \triangleq \wedge \text{sorting} = \text{busy} \\ & \wedge \text{arrived}' = [\text{arrived} \text{ EXCEPT } ![pe] = \text{channel}] \\ & \wedge \text{sorted}' = \text{sorted} \cup \{pe\} \wedge \text{pe}' \in \text{NOPARCELS} \wedge \text{sorting}' = \text{free} \end{aligned}$$

Annexe B

$$\begin{aligned} & \text{MODULE } \text{Trieuse1} \\ \text{EXTENDS } & \text{Trieuse}, \text{TLC} \\ \text{CONSTANTS } & \text{adr_level}, n, \text{Level}, \text{Node}, \text{noNode}, \text{Internal_N} \\ \text{VARIABLES } & \text{path_level}, i, l, \text{ctrl_cross} \\ \text{ASSUME } & \text{Level} \subseteq \text{Nat} \wedge \text{Level} \neq \text{Nat} \wedge \text{adr_level} \in [(\text{Baskets} * \text{Level}) \rightarrow \text{Node}] \\ & \wedge \text{Baskets} \subseteq \text{Node} \wedge \text{Baskets} \neq \text{Node} \wedge \text{Node} \neq \{\} \wedge \text{Internal_N} \subseteq \text{Node} \\ & \wedge \text{Internal_N} \neq \text{Node} \wedge \text{noNode} \notin \text{Node} \\ & \wedge \forall p \in \text{PARCELS} : \text{adr_level}[\text{adr}[p], n] = \text{adr}[p] \\ \hline \text{TypeInvariant1} & \triangleq \wedge \text{TypeInvariant} \wedge \text{path_level} \in [(\text{Level} \rightarrow \text{Node}) \cup \{\text{noNode}\}] \\ & \wedge ii \in \text{Nat} \wedge ii \in \text{Node} \wedge ll \in \text{Nat} \wedge ll \in \text{Level} \wedge \text{ctrl_cross} \in \text{BOOLEAN} \\ \hline \text{Init1} & \triangleq \text{Init} \wedge i = 0 \wedge ll = 0 \\ & \wedge \text{path_level} = [i \in \text{Level} \mapsto \text{noNode}] \wedge \text{ctrl_cross} = \text{FALSE} \\ \text{select_parcel1} & \triangleq \wedge \text{select_parcel} \wedge i' = 0 \wedge l' = 0 \\ & \wedge \text{path_level}' = [\text{path_level} \text{ EXCEPT } ![0] = 0] \wedge \text{UNCHANGED } \langle \text{ctrl_cross} \rangle \\ \text{set_channel1} & \triangleq \wedge \text{set_channel} \wedge \text{UNCHANGED } \langle \text{path_level}, i, l, \text{ctrl_cross} \rangle \\ \text{release1} & \triangleq \wedge \text{release} \wedge \text{UNCHANGED } \langle i, l, \text{path_level}, \text{ctrl_cross} \rangle \\ \text{path_left} & \triangleq \wedge \text{pe} \in \text{PARCELS} \wedge \text{sorting} = \text{busy} \wedge \text{ctrl_cross} = \text{FALSE} \\ & \wedge \text{adr_level}[\text{adr}[pe], l + 1] = 2 * i + 1 \wedge ii \in \text{Internal_N} \\ & \wedge \text{path_level}' = [\text{path_level} \text{ EXCEPT } ![l + 1] = 2 * i + 1] \wedge \text{ctrl_cross}' = \text{TRUE} \wedge ll < = n \\ & \wedge \text{UNCHANGED } \langle \text{channel}, \text{ready_to_sort}, \text{sorting}, \text{pe}, \text{arrived}, \text{sorted}, i, l \rangle \\ \text{cross_left} & \triangleq \wedge ll < = n \wedge i \in \text{Internal_N} \\ & \wedge \text{sorting} = \text{busy} \wedge \text{ctrl_cross} = \text{TRUE} \wedge \text{path_level}[l + 1] = 2 * i + 1 \\ & \wedge l' = l + 1 \wedge i' = 2 * i + 1 \wedge \text{ctrl_cross}' = \text{FALSE} \\ & \wedge \text{UNCHANGED } \langle \text{channel}, \text{sorting}, \text{pe}, \text{arrived}, \text{sorted}, \text{ready_to_sort}, \text{path_level} \rangle \\ \text{path_right} & \triangleq \wedge ii \in \text{Internal_N} \wedge ll < = n \end{aligned}$$

$$\begin{aligned} & \wedge pe \in PARCELS \wedge sorting = busy \wedge ctrl_cross = FALSE \\ & \wedge adr_level[adr[pe], l + 1] = 2 * i + 2 \\ & \wedge path_level' = [path_level \text{ EXCEPT } ![l + 1] = 2 * i + 2] \wedge ctrl_cross' = TRUE \\ & \wedge \text{UNCHANGED } \langle channel, ready_to_sort, sorting, pe, arrived, sorted, i, l \rangle \\ & cross_right \stackrel{\Delta}{=} \wedge ll < = n - 1 \wedge i \in Internal_N \wedge sorting = busy \\ & \wedge ctrl_cross = TRUE \wedge path_level[l + 1] = 2 * i + 2 \wedge l' = l + 1 \\ & \wedge i' = 2 * i + 2 \wedge ctrl_cross' = FALSE \\ & \wedge \text{UNCHANGED } \langle channel, sorting, ready_to_sort, pe, arrived, sorted, path_level \rangle \\ & cross_parcel1 \stackrel{\Delta}{=} \wedge sorting = busy \wedge l = n \\ & \wedge arrived' = [arrived \text{ EXCEPT } ![pe] = adr_level[adr[pe], n]] \\ & \wedge sorted' = sorted \cup \{pe\} \wedge pe' \in NOPARCELS \wedge sorting' = free \\ & \wedge \text{UNCHANGED } \langle channel, ready_to_sort, path_level, i, l, ctrl_cross \rangle \\ & Next1 \stackrel{\Delta}{=} \vee select_parcel1 \vee set_channel1 \vee release1 \vee path_left \\ & \quad \vee cross_left \vee path_right \vee cross_right \vee cross_parcel1 \end{aligned}$$

$$\begin{aligned} tr1vars \stackrel{\Delta}{=} & \langle channel, sorting, pe, arrived, sorted, ready_to_sort, path_level \\ & , i, l, ctrl_cross \rangle \\ Tr1Fairness \stackrel{\Delta}{=} & WF_{tr1vars}(Next1) \\ Spec1 \stackrel{\Delta}{=} & Init1 \wedge \square [Next1]_{tr1vars} \wedge Tr1Fairness \\ Liveness1 \stackrel{\Delta}{=} & \wedge \forall p \in PARCELS : p \in \text{DOMAIN } arrived \\ & \leadsto arrived[p] = adr_level[adr[p], n] \end{aligned}$$

THEOREM $Spec1 \Rightarrow Spec$ THEOREM $Spec1 \Rightarrow \square Init1$

THEOREM $Spec1 \Rightarrow \square TypeInvariant1$ THEOREM $Spec1 \Rightarrow \square Liveness1$

Annexe C

MODULE *Trieuse2*

EXTENDS *Trieuse1, TLC*

CONSTANTS *L, R, indef*

VARIABLES *in, out_l, out_r, gate, gate_set* Result $\stackrel{\Delta}{=} \{L, R, indef\}$

$$TypeInvariant2 \stackrel{\Delta}{=} \wedge TypeInvariant1 \wedge gate_set \in \text{BOOLEAN}$$

$$\wedge in \in [Node \rightarrow \text{BOOLEAN}] \wedge out_l \in [Internal_N \rightarrow \text{BOOLEAN}]$$

$$\wedge out_r \in [Internal_N \rightarrow \text{BOOLEAN}] \wedge gate \in [Node \rightarrow \text{Result}]$$

$$Init2 \stackrel{\Delta}{=} Init1 \wedge in = [i \in Node \mapsto \text{FALSE}] \wedge out_l = [i \in Internal_N \mapsto \text{FALSE}]$$

$$\wedge out_r = [i \in Internal_N \mapsto \text{FALSE}] \wedge gate = [i \in Node \mapsto indef] \wedge gate_set = \text{FALSE}$$

$$select_parcel2 \stackrel{\Delta}{=} \wedge select_parcel1$$

$$\wedge in' = [in \text{ EXCEPT } ![0] = \text{TRUE}] \wedge \text{UNCHANGED } \langle out_l, out_r, gate, gate_set \rangle$$

$$set_channel2 \stackrel{\Delta}{=} \wedge set_channel1$$

$$\wedge \text{UNCHANGED } \langle out_l, out_r, gate, gate_set, in \rangle$$

$$release2 \stackrel{\Delta}{=} \wedge release1 \wedge \text{UNCHANGED } \langle out_l, out_r, gate, in, gate_set \rangle$$

$$set_gate_left \stackrel{\Delta}{=} \wedge ll < = n - 1 \wedge pe \in PARCELS$$

$$\wedge adr_level[adr[pe], l + 1] = 2 * adr_level[adr[pe], l] + 1$$

$$\wedge gate_set' = \text{TRUE} \wedge gate' = [gate \text{ EXCEPT } ![adr_level[adr[pe], l]] = L]$$

$$\wedge \text{UNCHANGED } \langle channel, pe, arrived, sorted, sorting, i, l,$$

$$ready_to_sort, path_level, ctrl_cross, in, out_l, out_r \rangle$$

$$path_left2 \stackrel{\Delta}{=} \wedge path_left \wedge in[i] = \text{TRUE} \wedge gate[i] = L$$

$$\wedge \text{UNCHANGED } \langle in, out_l, out_r, gate, gate_set \rangle$$

$$crossing_left \stackrel{\Delta}{=} \wedge i \in Internal_N \wedge in[i] = \text{TRUE} \wedge gate[i] = L$$

$$\wedge out_l' = [out_l \text{ EXCEPT } ![i] = \text{TRUE}] \wedge out_r' = [out_r \text{ EXCEPT } ![i] = \text{FALSE}]$$

$$\wedge in' = [in \text{ EXCEPT } ![i] = \text{FALSE}] \wedge \text{UNCHANGED } \langle channel, pe, arrived, sorted, sorting, i, l,$$

$$ready_to_sort, path_level, ctrl_cross, gate, gate_set \rangle$$

$$cross_left2 \stackrel{\Delta}{=} \wedge cross_left \wedge (gate_set = \text{TRUE} \vee l \geq n - 2)$$

$$\wedge out_l[i] = \text{TRUE} \wedge in' = [in \text{ EXCEPT } ![2 * i + 1] = \text{TRUE}]$$

$$\wedge gate_set' = \text{FALSE} \wedge \text{UNCHANGED } \langle out_l, out_r, gate \rangle$$

$$set_gate_right \stackrel{\Delta}{=} \wedge ll < = n - 1 \wedge pe \in PARCELS$$

$$\wedge adr_level[adr[pe], l + 1] = 2 * adr_level[adr[pe], l] + 2 \wedge gate_set' = \text{TRUE}$$

$$\wedge gate' = [gate \text{ EXCEPT } ![adr_level[adr[pe], l]] = R]$$

$$\wedge \text{UNCHANGED } \langle channel, pe, arrived, sorted, sorting, ready_to_sort, i, l, path_level,$$

$$ctrl_cross, in, out_l, out_r \rangle$$

$$path_right2 \stackrel{\Delta}{=} \wedge path_right \wedge in[i] = \text{TRUE} \wedge gate[i] = R$$

$$\wedge \text{UNCHANGED } \langle in, out_l, out_r, gate, gate_set \rangle$$

$$crossing_right \stackrel{\Delta}{=} \wedge i \in Internal_N \wedge in[i] = \text{TRUE} \wedge gate[i] = R$$

$$\wedge out_l' = [out_l \text{ EXCEPT } ![i] = \text{FALSE}] \wedge out_r' = [out_r \text{ EXCEPT } ![i] = \text{TRUE}]$$

$$\wedge in' = [in \text{ EXCEPT } ![i] = \text{FALSE}] \wedge \text{UNCHANGED } \langle channel, pe, arrived, sorted, sorting$$

$$, ready_to_sort, i, l, gate, path_level, ctrl_cross, gate_set \rangle$$

$$cross_right2 \stackrel{\Delta}{=} \wedge cross_right \wedge (gate_set = \text{TRUE} \vee ll \geq n - 2)$$

$$\wedge out_r[i] = \text{TRUE} \wedge in' = [in \text{ EXCEPT } ![2 * i + 2] = \text{TRUE}]$$

$$\wedge gate_set' = \text{FALSE} \wedge \text{UNCHANGED } \langle out_l, out_r, gate \rangle$$

$$cross_parcel2 \stackrel{\Delta}{=} \wedge cross_parcel1$$

$$\wedge in' = [in \text{ EXCEPT } ![channel] = \text{TRUE}] \wedge \text{UNCHANGED } \langle out_l, out_r, gate, gate_set \rangle$$

$$Next2 \stackrel{\Delta}{=} \vee select_parcel2 \vee set_channel2 \vee release2 \vee cross_parcel2$$

$$\vee path_left2 \vee path_right2 \vee cross_left2 \vee cross_right2 \vee crossing_left$$

$$\vee crossing_right \vee set_gate_left \vee set_gate_right$$

$$tr2vars \stackrel{\Delta}{=} \langle channel, sorting, pe, arrived, sorted, ready_to_sort,$$

$$path_level, i, l, ctrl_cross, in, out_l, out_r, gate, gate_set \rangle$$

$$Tr2Fairness \stackrel{\Delta}{=} WF_{tr2vars}(Next2)$$

$$Spec2 \stackrel{\Delta}{=} Init2 \wedge \square [Next2]_{tr2vars} \wedge Tr2Fairness$$

$$Invariant2 \stackrel{\Delta}{=} \forall jj \in Internal_N : \neg(out_r[jj] \wedge out_l[jj])$$

THEOREM $Spec2 \Rightarrow Spec1$ THEOREM $Spec2 \Rightarrow \square TypeInvariant2$

THEOREM $Spec2 \Rightarrow \square Init2$ THEOREM $Spec2 \Rightarrow Invariant2$