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*Improved On-the-Fly Equivalence Checking
using Boolean Equation Systems*

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Improved On-the-Fly Equivalence Checking using Boolean Equation Systems

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Abstract: Equivalence checking is a classical verification method for ensuring the compatibility of a finite-state concurrent system (*protocol*) with its desired external behaviour (*service*) by comparing their underlying labeled transition systems (LTSS) modulo an appropriate equivalence relation. The local (or *on-the-fly*) approach for equivalence checking combats state explosion by exploring the synchronous product of the LTSS incrementally, thus allowing an efficient detection of errors in complex systems. However, when the two LTSS being compared are equivalent, the on-the-fly approach is outperformed by the global one, which completely builds the LTSS and computes the equivalence classes between states using partition refinement. In this report, we consider the technique based on translating the on-the-fly equivalence checking problem in terms of the local resolution of a boolean equation system (BES). We propose two enhancements of this technique in the case of equivalent LTSS: a new, faster encoding of equivalence relations in terms of BESS, and a new local BES resolution algorithm with a better average complexity. These enhancements were incorporated into the BISIMULATOR 2.0 equivalence checker of the CADP toolbox, and they led to significant performance improvements w.r.t. existing on-the-fly equivalence checking algorithms.

Key-words: bisimulation, boolean equation system, equivalence checking, labeled transition system, on-the-fly verification

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Vérification améliorée à la volée par équivalences au moyen de systèmes d'équations booléennes

Résumé : La vérification par équivalences (*equivalence checking*) est une méthode classique pour assurer la compatibilité entre un système concurrent à nombre fini d'états (*protocole*) et son comportement externe désiré (*service*) en comparant leurs systèmes de transitions étiquetées (STES) sous-jacents selon une relation d'équivalence appropriée. L'approche locale (ou à la volée) pour la vérification par équivalences lutte contre l'explosion d'états en explorant le produit synchrone des STES de manière incrémentale, permettant ainsi une détection efficace des erreurs dans des systèmes complexes. Cependant, quand les deux STES à comparer sont équivalents, l'approche à la volée est dépassée par l'approche globale, qui construit complètement les STES et calcule les classes d'équivalence entre états par raffinement de partitions. Dans ce rapport, nous considérons la technique basée sur une traduction du problème de la vérification par équivalences vers la résolution locale d'un système d'équations booléennes (SEB). Nous proposons deux améliorations de cette technique dans le cas des STES équivalents : un nouveau codage, plus rapide, des relations d'équivalence en termes de SEBs et un nouvel algorithme de résolution locale de SEBs avec une meilleure complexité moyenne. Ces améliorations ont été incorporées dans le vérificateur par équivalences BISIMULATOR 2.0 de la boîte à outils CADP et ont conduit à des améliorations significatives des performances par rapport aux algorithmes existants de vérification à la volée par équivalences.

Mots-clés : bisimulation, système d'équations booléennes, système de transitions étiquetées, vérification par équivalences, vérification à la volée

1 Introduction

Equivalence checking is a classical verification method for finite-state concurrent systems that consists in comparing the behaviour of the system under design (typically, a *protocol* or a low-level hardware description) with its desired external behaviour (typically, a *service* or a high-level hardware description) modulo a suitable equivalence relation. Protocol and service behaviours are usually represented as labeled transition systems (LTSS), and the relations most used for comparing them are the *bisimulations* defined in the framework of process algebras, such as CCS [37], CSP [9], or ACP [6] and of the formal specification languages inspired from them, such as LOTOS [26] or CHP [31]. In practice, LTSS are often represented in two complementary ways, which also determine the nature of equivalence checking algorithms: either *explicitly*, by their list of states and transitions, or *implicitly*, by their “successor function” returning the set of transitions going out of a given state. The implicit and explicit LTS representations are suitable for protocols (which are usually large) and services (which are usually small), respectively.

There are basically two approaches for equivalence checking: the *global* one [15], which operates on explicit LTSS, computes the equivalence classes of states by using partition refinement and then checks whether the initial states of the two LTSS fall into the same equivalence class; and the *local* one [12], which operates on implicit LTSS, explores the synchronous product between the two LTSS and searches for mismatches indicating the non equivalence of their initial states. Global algorithms are more effective when the two LTSS are equivalent, but require their complete construction, which is limited for large systems by the amount of memory available. Local (or *on-the-fly*) algorithms are more effective when the LTSS are not equivalent, allowing the detection of errors in complex systems even when the global approach would fail. Therefore, on-the-fly algorithms are useful at the beginning of the verification process, when errors occur frequently and must be detected quickly, whereas global algorithms are more suitable at a later stage, once the formal descriptions of the protocol and the service are stable and their underlying LTSS become equivalent.

Our objective is to improve the performance of on-the-fly equivalence checking algorithms when the LTSS to be compared are equivalent, which is the worst case for this class of algorithms because it forces them to explore the synchronous product of the two LTSS entirely. This would combine the advantages of global and local verification, making the on-the-fly approach suitable throughout the verification process. We consider here the technique relying on the translation of on-the-fly equivalence checking to the local resolution of a boolean equation system (BES) [2, 35]. This technique involves two clearly separated aspects, namely the BES encodings of bisimulation relations and the local BES resolution algorithms, which can be developed and optimized independently. To improve performance, we seek to enhance both these aspects.

First, we devise new BES encodings of the branching [44] and weak [37] bisimulations, obtained by migrating a part of the computation of transitive reflexive closures over internal steps (τ -closures) into the boolean equations. This simplifies the structure of BES equations considerably and reveals to be faster than computing τ -closures separately by using spe-

cialized algorithms [34]. Second, we propose a new local BES resolution algorithm, which exhibits a smaller average complexity than previously published algorithms [1, 47, 17, 35]. Our algorithm is based on a suspend/resume depth first search (sr-DFS) of the dependencies between boolean variables, and stops as soon as the BES portion explored contains a single example or counterexample for the boolean variable to be solved, therefore being optimal from this point of view.

These two enhancements led to version 2.0 of the BISIMULATOR [35] equivalence checker of the CADP [22] verification toolbox. The tool was developed using the generic OPEN/CÆSAR [21] environment for on-the-fly manipulation of LTSS, and uses as verification engine the CÆSAR_SOLVE [35] library for on-the-fly resolution of BESS. The enhancements led to a significant performance increase w.r.t. BISIMULATOR 1.0, as we observed on LTSS generated from protocol and hardware descriptions or taken from the VLTS benchmark suite [46].

Related work. On-the-fly equivalence checking algorithms [12] received relatively little attention from the verification community, the research being mainly focused on optimizing global algorithms based on partition refinement [15, 20]. Among the first on-the-fly equivalence checking algorithms were those proposed in [18] and subsequently implemented in the ALDÉBARAN tool [19]. Two different algorithms were elaborated: the first one compares deterministic LTSS by searching their synchronous product for a pair of non equivalent states, and the second one handles nondeterministic LTSS by assuming that certain couples of states are equivalent and by backtracking in the synchronous product whenever such an assumption turns out to be wrong. The verification technique based on BES resolution allows one to reproduce the first algorithm by observing that the BESS corresponding to bisimulations between deterministic LTSS are conjunctive, and by devising a specialized local resolution algorithm for this case [35]. The algorithm for the nondeterministic case is outperformed in practice by local BES resolution algorithms, as it was observed experimentally [5].

Another approach of checking the equivalence of two LTSS is to rephrase the problem as the model checking on one LTS of a *characteristic formula* [25] in modal μ -calculus derived from the other LTS. This approach was elegantly implemented in the Concurrency Workbench [13, 10], but was hampered in practice for large LTSS by the prohibitive size of characteristic formulas, which is at least of the same order as the LTS size. The quest for performance was pursued by considering other intermediate formalisms suitable for representing equivalence checking, such as the BESS, which are lower-level than the modal μ -calculus and therefore are likely to require less computation effort.

Encodings of branching and weak bisimulation using BESS of alternation depth two were proposed in [2]. These BESS contain two mutually recursive equation blocks, a maximal fixed point one encoding the bisimulation relation, and a minimal fixed point one encoding the τ -closures to be computed in the input LTSS. The local resolution algorithms underlying this class of BESS have a quadratic complexity w.r.t. the BES size [47], which makes them impractical for large LTSS; no implementation of this approach was reported as far as we know. Although a subquadratic algorithm for solving BESS with disjunctive/conjunctive equation blocks of arbitrary alternation depth was proposed in [24], it does not seem to

capture the BESs for branching and weak bisimulations, which consist of a disjunctive block encoding τ -closures and a general block encoding the equivalence relation. Simpler encodings of weak equivalences using alternation-free BESs, obtained by leaving the computation of τ -closures (possibly enhanced with on-the-fly τ -confluence reduction [38]) outside the BES, proved to be practically effective [35]. The resulting BESs can be solved using the many local resolution algorithms available [28, 1, 47, 29, 17, 35].

An alternative approach consists in formulating equivalence checking by means of Horn clauses [40], which can be solved using classical HORNSAT resolution algorithms [16, 4]. We believe that BES encodings provide a more direct way of connecting on-the-fly equivalence checking to graph exploration algorithms. In fact, local BES resolution algorithms, such as the one presented in this report, can also be used for solving HORNSAT efficiently, by applying the translation from Horn clauses to BESs proposed in [29].

Report outline. Section 2 defines the class of BESs we use and illustrates the functioning of local resolution algorithms. Section 3 proposes new, faster BES encodings for branching and weak bisimulations. Section 4 describes our new local resolution algorithm, and Section 5 shows experimentally its performance when applied to equivalence checking. Section 6 gives some concluding remarks and directions for future work. Annex A contains the proofs of our BES encodings for strong and branching bisimulation.

2 Background

A boolean equation system (BES) is a set of possibly recursive equations $B = \{X_i \stackrel{\sigma}{=} X_{i_1} op_i \cdots op_i X_{i_{m_i}}\}_{1 \leq i \leq n}$, where $X_i \in \mathcal{X}$ are boolean variables, $op_i \in \{\vee, \wedge\}$ are disjunctive or conjunctive connectors, and $\sigma \in \{\mu, \nu\}$ is a minimal or maximal fixed point sign. An empty disjunction (resp. conjunction) is equivalent to the **false** (resp. **true**) constant. Each boolean variable occurring in the right-hand side of an equation must be defined by some equation of the BES. A variable X_i is said to be disjunctive (resp. conjunctive) iff $op_i = \vee$ (resp. \wedge). BESs of this kind are called *simple*, because each of their equations contains a single type of boolean connector (either \vee , or \wedge) in its right-hand side. Any BES containing arbitrary combinations of boolean connectors in the right-hand sides of its equations can be brought to the simple form with at most a linear blow-up in size, by introducing new equations to factor subformulas [3]. We focus our attention on BESs with a single equation block (i.e., set of equations having the same fixed point sign), since they are suitable for encoding equivalence checking problems [35]; more general BESs with multiple blocks are used for encoding model checking problems [14, 36]. In-depth presentations of the theory and applications of BESs can be found in [1, 30].

For each equation i of a BES, the evaluation of the formula in its right-hand side yields a boolean value defined as follows:

$$\llbracket X_{i_1} op_i \cdots op_i X_{i_{m_i}} \rrbracket \delta = \delta(X_{i_1}) op_i \cdots op_i \delta(X_{i_{m_i}}).$$

where the context $\delta : \mathcal{X} \rightarrow \text{Bool}$ is a partial function assigning boolean values to all variables occurring in the formula. The solution of a BES is a vector $\langle v_1, \dots, v_n \rangle \in \text{Bool}^n$ equal to the fixed point $\sigma\Phi$ of the functional $\Phi : \text{Bool}^n \rightarrow \text{Bool}^n$ associated to the BES:

$$\Phi(b_1, \dots, b_n) = \langle \llbracket X_{i_1} \text{ op}_i \cdots \text{op}_i X_{i_{m_i}} \rrbracket [b_1/X_1, \dots, b_n/X_n] \rangle_{1 \leq i \leq n}$$

where $[b_1/X_1, \dots, b_n/X_n]$ is the context assigning the boolean value b_i to variable X_i for $1 \leq i \leq n$. Since the boolean formulas in a BES do not contain negation operators, the functional Φ is monotonic, which ensures the existence of its minimal and maximal fixed points on $\langle \text{Bool}^n, \text{false}^n, \text{true}^n, \vee^n, \wedge^n \rangle$, the pointwise extension of the boolean lattice [27]. In the sequel, we consider only maximal fixed point BESS (i.e., with $\sigma = \nu$), which allow to encode equivalence checking.

The local resolution of a BES B , which underlies on-the-fly verification (based on a forward exploration of LTSS), amounts to computing the solution v_i of a particular variable X_i by solving as few equations of B as possible. Local resolution algorithms are easier to devise and understand by representing BESS as *boolean graphs* [1]. Given a BES $B = \{X_i \stackrel{\sigma}{=} X_{i_1} \text{ op}_i \cdots \text{op}_i X_{i_{m_i}}\}_{1 \leq i \leq n}$, its associated boolean graph $G = (V, E, L)$ is defined as follows: $V = \{X_1, \dots, X_n\}$ is the set of vertices (boolean variables), $E = \{(X_i, X_j) \mid 1 \leq i \leq n \wedge j \in \{i_1, \dots, i_{m_i}\}\}$ is the set of edges (dependencies between variables), and $L : V \rightarrow \{\vee, \wedge\}$, $L(X_i) = \text{op}_i$ for $1 \leq i \leq n$ is the labeling of vertices as disjunctive or conjunctive. The constant false (resp. true) is represented as a sink \vee -vertex (resp. \wedge -vertex). The local resolution of a vertex X_i consists in two activities performed simultaneously [1, 47, 35]: a forward exploration of the boolean graph along its edges, starting at X_i ; and a backward propagation of the *stable* variables found, i.e., whose boolean value has been computed. An example of local BES resolution is shown on Figure 1. The local resolution algorithm used is based on a depth-first search (DFS) of the boolean graph, starting at the variable of interest X_1 . The light grey area delimits the boolean subgraph explored during resolution. Black (resp. white) vertices correspond to variables whose solution is true (resp. false).

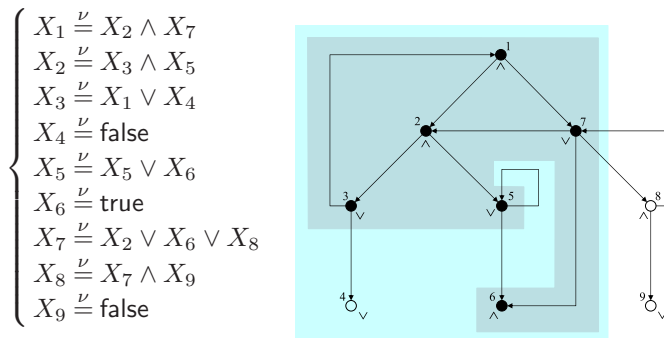


Figure 1: Boolean graph-based local resolution of variable X_1

The solution of a BES can also be characterized by interpreting on its boolean graph the following *example formula* [32] written in modal μ -calculus:

$$\phi_{ex} = \nu X. (P_{\vee} \wedge \langle - \rangle X) \vee (P_{\wedge} \wedge [-] X)$$

where the atomic propositions P_{\vee} and P_{\wedge} denote \vee -vertices and \wedge -vertices, respectively. Formula ϕ_{ex} expresses that every \vee -vertex (resp. \wedge -vertex) satisfying ϕ_{ex} must have one (resp. all) of its successors satisfying ϕ_{ex} . The solution v_i of a variable X_i is true iff the corresponding vertex X_i satisfies ϕ_{ex} in the boolean graph. A positive diagnostic (or *example*) for vertex X_i is a boolean subgraph that contains X_i and is a model for ϕ_{ex} . Dually, a negative diagnostic (or *counterexample*) for X_i is a boolean subgraph containing X_i and being a model for the counterexample formula $\phi_{cx} = \neg\phi_{ex}$. The dark grey area shown on Figure 1 delimits an example for X_1 , generated by traversing again the boolean subgraph explored during resolution [32].

3 Encoding Bisimulation Relations as BESs

Labeled transition systems (LTSS) are the semantic model underlying process algebras [7] and the related languages, such as LOTOS [26] and CHP [31]. An LTS is a quadruple $M = \langle Q, A, T, q_0 \rangle$, where Q is the set of states, A is the set of actions (including the internal action τ), $T \subseteq Q \times A \times Q$ is the transition relation, and $q_0 \in Q$ is the initial state. A transition $\langle p, a, q \rangle \in T$ (also written $p \xrightarrow{a} q$) indicates that the system can move from state p to state q by performing action a . The notation is extended to transition sequences: $p \xrightarrow{l} q$ denotes the existence of a sequence going from p to q and whose concatenated labels form a word of the language $l \subseteq A^*$.

To compare the LTSS modeling the behaviour of concurrent systems, various equivalence relations were proposed (see [45] for a survey), among which *bisimulations* are most useful in practice due to their congruence properties w.r.t. the parallel composition operators of process algebras. We consider here three widely-used bisimulations: strong [39], branching [44], and weak [37], the last two being originally proposed as native equivalence relations for ACP [6] and CCS [37], respectively. Given two LTSS $M_i = \langle Q_i, A_i, T_i, q_{0_i} \rangle$ with $i \in \{1, 2\}$, a bisimulation $\approx \subseteq Q_1 \times Q_2$ is a relation such that $p \approx q$ if $\forall p \xrightarrow{a} p'. \exists q \xrightarrow{a} q'. p' \approx q'$ and $\forall q \xrightarrow{a} q'. \exists p \xrightarrow{a} p'. p' \approx q'$, where $p, p' \in Q_1$, $a \in A_1 \cup A_2$, and $q, q' \in Q_2$. Bisimulations are closed under union, and the strong bisimulation \approx_s is defined as the greatest one, i.e., the union of all bisimulations. M_1 is strongly equivalent to M_2 (notation $M_1 \approx_s M_2$) iff $q_{0_1} \approx_s q_{0_2}$.

A basic encoding of this mathematical definition as a maximal fixed point BES is shown in Table 1 (upper part, first row). The fact that $p \approx_s q$ is encoded as a boolean variable X_{pq} defined by an equation whose right-hand side boolean formula is directly derived from the two bisimulation conditions. The correctness of this encoding scheme (see the proof in Annex A.1), which reformulates the definition of strong bisimulation in propositional logic instead of first-order logic, relies on a bijection between the set of bisimulations and the set of fixed point solutions of the functional associated to the BES. To obtain a simple BES compliant with the definition given in Section 2, we introduce the new variables $Y_{p'qa}$ and $Z_{pq'a}$ such that each right-hand side formula contains a single type of boolean connector (second row). The BES for the strong preorder relation \preceq_s is obtained by keeping only the coloured parts of the equations. Checking the strong bisimilarity of M_1 and M_2 amounts to solving

the variable $X_{q_0_1 q_0_2}$ of this BES, which can be carried out using a local resolution algorithm. The evaluation of the boolean formulas in the right-hand sides of the equations defining X_{pq} , $Y_{p'qa}$, and $Z_{pq'a}$ triggers a forward exploration of transitions in M_1 and M_2 , which enables an incremental construction of both LTSS, and therefore an on-the-fly verification.

Table 1: Basic and full BES encodings of three widely-used bisimulations

Strong bisimulation	
$X_{pq} \stackrel{\nu}{=} \bigwedge_{p \rightarrow p'} \bigvee_{q \rightarrow q'} X_{p'q'} \wedge \bigwedge_{q \rightarrow q'} \bigvee_{p \rightarrow p'} X_{p'q'}$	
$X_{pq} \stackrel{\nu}{=} \bigwedge_{p \rightarrow p'} Y_{p'qa} \wedge \bigwedge_{q \rightarrow q'} Z_{pq'a} \quad Y_{p'qa} \stackrel{\nu}{=} \bigvee_{q \rightarrow q'} X_{p'q'} \quad Z_{pq'a} \stackrel{\nu}{=} \bigvee_{p \rightarrow p'} X_{p'q'}$	
Branching bisimulation	
$X_{pq} \stackrel{\nu}{=} \bigwedge_{p \rightarrow p'} ((a = \tau \wedge X_{p'q}) \vee \bigvee_{q \xrightarrow{\tau^*} q'} (X_{p'q'} \wedge X_{p''q''})) \wedge \bigwedge_{q \rightarrow q'} ((a = \tau \wedge X_{p'q'}) \vee \bigvee_{p \xrightarrow{\tau^*} p'} (X_{p'q} \wedge X_{p''q''}))$	
$X_{pq} \stackrel{\nu}{=} \bigwedge_{p \rightarrow p'} Y_{pp'qa} \wedge \bigwedge_{q \rightarrow q'} Z_{ppq'a} \quad Y_{pp'qa} \stackrel{\nu}{=} (a = \tau \wedge X_{p'q}) \vee U_{pp'qa}$	
$Z_{ppq'a} \stackrel{\nu}{=} (a = \tau \wedge X_{p'q'}) \vee V_{ppq'a} \quad U_{pp'qa} \stackrel{\nu}{=} \bigvee_{q \rightarrow q'} W_{pp'qq'} \vee \bigvee_{q \xrightarrow{\tau} q'} U_{pp'q'a}$	
$V_{ppq'a} \stackrel{\nu}{=} \bigvee_{p \rightarrow p'} W_{pp'qq'} \vee \bigvee_{p \xrightarrow{\tau} p'} V_{p'qq'a} \quad W_{pp'qq'} \stackrel{\nu}{=} X_{pq} \wedge X_{p'q'}$	
Weak bisimulation	
$X_{pq} \stackrel{\nu}{=} \bigwedge_{p \rightarrow p'} ((a = \tau \wedge \bigvee_{q \xrightarrow{\tau^*} q'} X_{p'q'}) \vee \bigvee_{q \xrightarrow{\tau^*} q'} X_{p'q'}) \wedge \bigwedge_{q \rightarrow q'} ((a = \tau \wedge \bigvee_{p \xrightarrow{\tau^*} p'} X_{p'q'}) \vee \bigvee_{p \xrightarrow{\tau^*} p'} X_{p'q'})$	
$X_{pq} \stackrel{\nu}{=} \bigwedge_{p \rightarrow p'} Y_{p'qa} \wedge \bigwedge_{q \rightarrow q'} Z_{pq'a}$	
$Y_{p'qa} \stackrel{\nu}{=} (a = \tau \wedge U_{p'q}) \vee V_{p'qa} \quad U_{p'q} \stackrel{\nu}{=} X_{p'q} \vee \bigvee_{q \xrightarrow{\tau} q'} U_{p'q'}$	
$V_{p'qa} \stackrel{\nu}{=} \bigvee_{q \rightarrow q'} U_{p'q'} \vee \bigvee_{q \xrightarrow{\tau} q'} V_{p'q'a} \quad Z_{pq'a} \stackrel{\nu}{=} (a = \tau \wedge W_{pq'}) \vee T_{pq'a}$	
$W_{pq'} \stackrel{\nu}{=} X_{pq'} \vee \bigvee_{p \xrightarrow{\tau} p'} W_{p'q'} \quad T_{pq'a} \stackrel{\nu}{=} \bigvee_{p \rightarrow p'} W_{p'q'} \vee \bigvee_{p \xrightarrow{\tau} p'} T_{p'q'a}$	

Similar encoding schemes hold for the branching (\approx_b) and weak (\approx_w) bisimulations, as shown in Table 1 (middle and lower parts, first rows). The important difference w.r.t. strong bisimulation is the presence of transitive reflexive closures over τ -transitions, which correspond to the abstraction of internal activity done by these two bisimulations. The simple BESs derived from these encodings by introducing new variables (similarly to strong bisimulation as shown above) were successfully used as basis for on-the-fly equivalence checking in conjunction with linear-time local BES resolution algorithms [35]. For LTSS with a high percentage of τ -transitions, the encodings of weak bisimulations yield relatively small BESs, but shift the computation effort to the evaluation of right-hand side boolean formulas, which involve various forms of τ -closures ($p \xrightarrow{\tau^*} p' \xrightarrow{a} p''$, $p \xrightarrow{\tau^*} p' \xrightarrow{a, \tau^*} p''$, and $p \xrightarrow{\tau^*} p'$) having a quadratic worst-case complexity. Practical usage confirmed that computation of τ -closures, even using optimized algorithms [34], is the most time-consuming part of the verification process.

An alternative solution for computing τ -closures would be to encode them directly using boolean equations, yielding the BESs shown on Table 1 (middle and lower part, second rows); however, this works only for τ -convergent LTSS (i.e., without τ -cycles). To see this, consider the two LTSS $M_1 = \langle \{p_0, p_1\}, \{a, \tau\}, \{p_0 \xrightarrow{\tau} p_0, p_0 \xrightarrow{a} p_1\}, p_0 \rangle$ and $M_2 = \langle \{q_0, q_1\}, \{b, \tau\}, \{q_0 \xrightarrow{\tau} q_0, q_0 \xrightarrow{b} q_1\}, q_0 \rangle$, which are obviously not equivalent modulo any of the three bisimulations considered since they have different action sets. The comparison of M_1 and M_2 modulo weak bisimulation yields the BES below:

$$\begin{array}{lll}
X_{p_0q_0} \stackrel{\nu}{=} Y_{p_0q_0\tau} \wedge Y_{p_1q_0a} \wedge Z_{p_0q_0\tau} \wedge Z_{p_0q_1b} & & \\
Y_{p_0q_0\tau} \stackrel{\nu}{=} U_{p_0q_0} \vee V_{p_0q_0\tau} & U_{p_0q_0} \stackrel{\nu}{=} X_{p_0q_0} \vee U_{p_0q_0} & V_{p_0q_0\tau} \stackrel{\nu}{=} V_{p_0q_0\tau} \\
Z_{p_0q_0\tau} \stackrel{\nu}{=} W_{p_0q_0} \vee T_{p_0q_0\tau} & W_{p_0q_0} \stackrel{\nu}{=} X_{p_0q_0} \vee W_{p_0q_0} & T_{p_0q_0\tau} \stackrel{\nu}{=} T_{p_0q_0\tau} \\
Y_{p_1q_0a} \stackrel{\nu}{=} V_{p_1q_0a} & V_{p_1q_0a} \stackrel{\nu}{=} V_{p_1q_0a} & Z_{p_0q_1b} \stackrel{\nu}{=} T_{p_0q_1b} \quad T_{p_0q_1b} \stackrel{\nu}{=} T_{p_0q_1b}
\end{array}$$

We can easily compute the maximal fixed point solution of this BES by using Kleene's iterative characterization [27], which consists in initializing all variables to true and repeatedly evaluating the right-hand sides of equations until the values of all variables become stable; the process converges in one iteration and all variables remain true, erroneously indicating that $M_1 \approx_w M_2$. The problem here is that τ -closures express the existence of *finite* τ -sequences in the LTSS, and hence they correspond to *minimal* fixed point computations, which cannot be done accurately by solving the equations of a *maximal* fixed point BES. On the other hand, if we eliminate the two τ -loops in M_1 and M_2 , the BES becomes:

$$X_{p_0q_0} \stackrel{\nu}{=} Y_{p_1q_0a} \wedge Z_{p_0q_1b} \quad Y_{p_1q_0a} \stackrel{\nu}{=} \text{false} \quad Z_{p_0q_1b} \stackrel{\nu}{=} \text{false}$$

and yields the correct solution false for the variable $X_{p_0q_0}$. This is a consequence of the fact that minimal and maximal fixed points have the same interpretation on acyclic models, as shown in [33] for modal μ -calculus formulas. Thus, if the LTSS being compared are τ -convergent, the encoding of τ -closures using maximal fixed point equations is correct. A proof of this fact for branching bisimulation is available in Annex A.2. The elimination of τ -cycles by collapsing their states (also called τ -compression), which preserves both branching and weak bisimulation, can be performed in linear-time during an on-the-fly LTS exploration [34], using an adaptation of Tarjan's algorithm [41] for detecting strongly connected components (SCCs). To make the BES encodings in Table 1 correct, it is therefore sufficient to reduce both LTSS on-the-fly by applying τ -compression simultaneously with the local BES resolution.

The new BESs obtained in this way for branching and weak bisimulation have a size comparable (see Figure 3 (a), (b)) with the BESs resulting from the previous encodings in which τ -closures were computed separately by specialized algorithms [34]; however, we observed experimentally that their resolution (using the same algorithms) is about one order of magnitude faster. This is due to the fact that intermediate results of τ -closure computations are stored as values of the boolean variables used to encode τ -closures (e.g., variables $U_{pp'qa}$ and $V_{pp'qa}$ of the BES for branching bisimulation), which are retrieved immediately if needed again during resolution; the only risk with this scheme was a too important quantity of such variables, which was not observed in practice. We also encoded as BESs, using similar schemes, the $\tau^*.a$ [18] and safety [8] equivalences, which are weaker than branching bisimulation and slightly less used in practice.

4 Local BES Resolution based on Suspend/Resume DFS

Several local BES resolution algorithms with a linear-time complexity are available [1, 47, 17, 35], typically based on DFS or breadth-first search (BFS) strategies for exploring the dependencies between boolean variables, i.e., the edges of the boolean graph. Here we aim to satisfy the following optimality criterion for local BES resolution algorithms, based on the notion of diagnostic [32]: the resolution must stop as soon as the boolean subgraph already explored contains exactly one diagnostic (example or counterexample) for the variable of interest. To our knowledge, all existing algorithms satisfy only a half of this criterion, i.e., they detect optimally either the presence of examples, or of counterexamples, but not of both of them. The LMC algorithm proposed in [17], based on a DFS traversal of the boolean graph with computation of SCCs, detects counterexamples optimally and speeds up the search of examples (in maximal fixed point BESS) without attempting their optimal detection.

In the BESS produced from equivalence checking problems, false constants (sink \vee -vertices) denote couples of non equivalent states; if their backward propagation along edges in the boolean graph is done as soon as these vertices are encountered, it leads to an optimal detection of counterexamples, as in the A0 algorithm proposed in [35]. However, when the LTSS being compared are equivalent, the variable of interest is true and the associated diagnostic is an *example*, which must be detected as soon as possible during resolution. Using the characterization of examples induced by the μ -calculus formula ϕ_{ex} given in Section 2, we can draw an alternative graph-based characterization: an example for vertex X is a subgraph containing X in which every \vee -vertex (resp. \wedge -vertex) must have exactly one successor (resp. all its successors) contained in the example. Each example can be split into maximal SCCs, which are connected acyclically; in the sequel, we denote them as *pseudo-SCCs*, since they are special cases of SCCs in the boolean graph (for instance, a trivial SCC containing a single sink \vee -vertex denotes a false constant, which is neither an example, nor a pseudo-SCC). These pseudo-SCCs are the smallest “building blocks” of the examples, and therefore their presence in the boolean subgraph already explored must be determined as soon as possible in order to achieve an optimal detection of examples.

To detect pseudo-SCCs, one can adapt Tarjan’s algorithm [41], which relies on a DFS traversal. The problem is that a classical DFS of the boolean graph does not allow the detection of pseudo-SCCs as soon as they occur, because Tarjan’s algorithm identifies SCCs only when their root vertex is popped from the DFS stack, meaning that the subgraph reachable from the root has been entirely explored; this subgraph may very well contain other pseudo-SCCs, which could make several examples to be contained in the boolean subgraph explored at the end of the resolution, i.e., when the variable of interest will be popped in turn from the DFS stack (if it evaluates to true, this variable is the root of the last pseudo-SCC identified). In order to detect the first pseudo-SCC encountered, it is necessary to *suspend* the DFS for each \vee -vertex when one of its successors was already visited; if this successor turns out to be false at some later stage (and thus not part of a pseudo-SCC), and the \vee -vertex is encountered again, it is necessary to *resume* the DFS by considering some other successor of the \vee -vertex that may belong to a pseudo-SCC. The exploration of \wedge -vertices is done as in the classical

DFS, since for each \wedge -vertex, all its successors must be visited before attempting to detect a pseudo-SCC containing it.

The local resolution algorithm sr-DFS that we propose, based on this suspend/resume DFS, is illustrated below. Taking as input a boolean graph $G = (V, E, L)$ represented implicitly (i.e., by its successor function) and a variable of interest x , the algorithm performs iteratively a forward search of G starting at vertex x . Visited vertices are stored in a set $A \subseteq V$. The DFS stack is stored in a variable dfs and the stack used for detecting pseudo-SCCs is stored in a variable scc . The variable $count$ keeps a global counter allowing the assignment of unique DFS numbers to visited vertices. To each vertex v are associated the following fields:

- a counter $c(v)$ which counts the number of remaining successors of v to visit in order to stabilize v ;
- a number $p(v)$ recording the index of the next successor of v to be visited (the successors in $E(v)$ are supposed to be indexed from 0 to $|E(v)| - 1$);
- a number $n(v)$ representing the DFS number of v ;
- a number $l(v)$ representing the “lowlink” number [41] of v , used to detect if a vertex is the root of a pseudo-SCC;
- a set $d(v)$ containing the vertices that currently depend upon v ;
- a boolean $on_scc(v)$ which is true if v is on the scc stack and false otherwise;
- a boolean $stop(v)$ which is true if the DFS must be suspended for v (i.e., v is a \vee -vertex and one of its successors has been visited);
- a boolean $stable(v)$, which is true if v is stable;
- a boolean $value(v)$, which represents the value of v (this field is of interest only if v is stable).

At each iteration of the main while-loop (lines 20–122), the vertex y at the top of the dfs stack is explored. If y is stable, or the DFS must be suspended for y (that is, y is a \vee -vertex and one of its successors has already been visited), the value of y is back-propagated along its predecessors d (lines 30–62). For each vertex w which is not stabilized by the back-propagation, the algorithm must keep on visiting its successors, if w will be visited again during the DFS (the variable $stop(w)$ becomes false). Due to the suspend/resume principle, this propagation phase may influence the contents of the pseudo-SCC currently stored on the scc stack. Indeed, each \vee -vertex which is visited during the propagation phase is stored on the scc stack. The definition of pseudo-SCCs requires that each \vee -vertex must have exactly one successor contained in its pseudo-SCC. But, as the vertex was not stabilized by the value of the successor which was propagated, it does not meet any more the definition of the pseudo-SCC. Since the scc does not contain a pseudo-SCC anymore, it must be cleared. A variable called $purge$ is used for this purpose and becomes true when the scc stack must

Algorithm 1 Local BES resolution based on suspend/resume DFS

```

1: function sr-DFS ( $x, (V, E, L)$ ) : Bool is
2: var  $A, B : 2^V; d : V \rightarrow 2^V;$ 
3:    $u, w, y, z : V; dfs, scc : V^*;$ 
4:    $c, p, n, l : V \rightarrow \mathbf{Nat};$ 
5:    $stop, stable : V \rightarrow \mathbf{Bool};$ 
6:    $value, on\_scc : V \rightarrow \mathbf{Bool};$ 
7:    $count, max, min : \mathbf{Nat};$ 
8:    $purge : \mathbf{Bool};$ 
9: if  $L(x) = \wedge$  then
10:   $c(x) := |E(x)|$ 
11: else
12:   $c(x) := 1$ 
13: end if
14:  $p(x) := 0; stable(x) := \text{false};$ 
15:  $d(x) := \emptyset; value(x) := \text{false};$ 
16:  $A := \{x\}; count := 0;$ 
17:  $dfs := \text{push}(x, nil);$ 
18:  $scc := \text{push}(x, nil);$ 
19:  $on\_scc(x) := \text{true}; stop(x) := \text{false};$ 
20: while  $dfs \neq nil$  do
21:   $y := top(dfs);$ 
22:   $n(y) := count;$ 
23:   $l(y) := n(y);$ 
24:   $count := count + 1;$ 
25:   $max := 0;$ 
26:   $min := n(y);$ 
27:  if  $stable(y) \vee stop(y)$  then
28:   if  $d(y) \neq \emptyset$  then
29:     $B := \{y\};$ 
30:    while  $B \neq \emptyset$  do
31:     let  $u \in B;$ 
32:      $B := B \setminus \{u\};$ 
33:     for all  $w \in d(u)$  do
34:      if  $\neg stable(w)$  then
35:       if  $((L(w) = \vee) \wedge value(u)) \vee ((L(w) =$ 
36:         $\wedge) \wedge \neg value(u))$  then
37:         $c(w) := 0$ 
38:       else
39:         $c(w) := c(w) - 1$ 
40:       end if
41:       if  $c(w) = 0$  then
42:         $stable(w) := \text{true};$ 
43:         $value(w) := value(u);$ 
44:         $B := B \cup \{w\};$ 
45:        if  $n(w) < min$  then
46:          $min := n(w)$ 
47:        end if
48:       else
49:         $stop(w) := \text{false};$ 
50:        if  $L(w) = \wedge$  then
51:          $c(w) := 1;$ 
52:          $p(w) := 0$ 
53:        else
54:          $E(w) := E(w) \setminus \{u\};$ 
55:         if  $n(w) > max$  then
56:           $max := n(w)$ 
57:         end if
58:        end if
59:       end if
60:     end for
61:      $d(u) := \emptyset$ 
62:   end while;
63:   if  $max > min$  then
64:     $purge := \text{true}$ 
65:   end if
66:  else
67:    $dfs := pop(dfs);$ 
68:   if  $dfs \neq nil$  then
69:     $l(top(dfs)) :=$ 
70:      $\min(l(top(dfs)), l(y))$ 
71:   end if
72:  end if
73: else
74:  if  $purge$  then
75:   while  $top(scc) \neq top(dfs)$  do
76:     $scc := pop(scc)$ 
77:   end while;
78:    $purge := \text{false}$ 
79:  end if
80:  if  $p(y) < |E(y)|$  then
81:   if  $L(y) = \vee$  then
82:     $stop(y) := \text{true}$ 
83:   end if
84:    $z := (E(y))_{p(y)};$ 
85:    $p(y) := p(y) + 1;$ 
86:    $d(z) := d(z) \cup \{y\};$ 
87:   if  $z \in A$  then
88:    if  $on\_scc(z)$  then
89:     if  $n(z) < n(y)$  then
90:       $l(y) := \min(n(z), n(y))$ 
91:     end if
92:    else
93:      $dfs := \text{push}(z, dfs);$ 
94:      $scc := \text{push}(z, scc);$ 
95:      $on\_scc(z) := \text{true}$ 
96:    end if
97:   else
98:    if  $L(z) = \wedge$  then
99:      $c(z) := |E(z)|$ 
100:    else
101:      $c(z) := 1$ 
102:    end if;
103:     $p(z) := 0;$ 
104:     $A := A \cup \{z\};$ 
105:     $dfs := \text{push}(z, dfs);$ 
106:     $scc := \text{push}(z, scc);$ 
107:     $on\_scc(z) := \text{true}$ 
108:   end if
109:  end if
110:  if  $(l(y) = n(y)) \wedge (top(scc) \neq y)$  then
111:   repeat
112:     $z := top(scc);$ 
113:     $c(z) := 0;$ 
114:     $scc := pop(scc)$ 
115:   until  $top(scc) = y$ 
116:  end if
117:   $dfs := pop(dfs);$ 
118:  if  $dfs \neq nil$  then
119:    $l(top(dfs)) :=$ 
120:     $\min(l(top(dfs)), l(y))$ 
121:  end if
122: end while;
123: return  $value(x)$ 

```

be cleared (two variables min and max are used to determine if scc must be cleared: min represents the least DFS number among all the DFS numbers of vertices stabilized during the propagation phase and max represents the greatest DFS number among all \vee -vertices towards which a false value has been propagated).

If the vertex y at the top of the dfs stack is unstable or that the exploration must continue for this vertex, its next unexplored successor $z = E(y)_{p(y)}$ is visited. Before that, the scc stack is cleared if needed (i.e., if a back-propagation of a false value took place at some previous iteration of the main while-loop). If z is a new vertex (lines 96-107), it is pushed on the dfs stack. If z is an already explored vertex, two cases may appear. Either z is on the scc stack (lines 87-90) and therefore its lowlink number must be updated, or it is not on the stack (lines 91-95), and therefore it must be explored as if it was a new vertex (i.e., z was popped from the scc stack after a propagation phase which induced a clearing of this stack). Finally, if y is unstable and all its successors have been visited, the algorithm watches if y is the root of a pseudo-SCC (lines 108–120). If this is the case, the scc stack is cleared from its top until y and each vertex of the pseudo-SCC is stabilized. Then, y is popped from the dfs stack. Finally, after termination of the main while-loop, the value computed for x is returned.

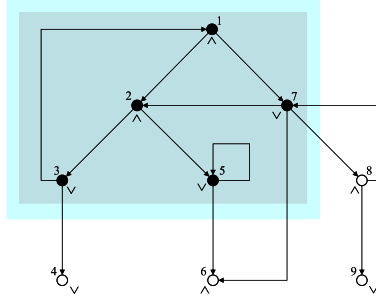


Figure 2: Local resolution of variable X_1 using the sr-DFS algorithm

The execution of algorithm sr-DFS on the boolean graph considered in Section 2 is illustrated on Figure 2. We observe on this example an optimal behaviour of sr-DFS: due to the suspension of the DFS for the \vee -vertices X_3 , X_5 , and X_7 when one of their successors was visited, the subgraph explored by the algorithm (light grey area) coincides with the example found for vertex X_1 (dark grey area), made of the pseudo-SCCs $\{X_1, X_2, X_3, X_7\}$ and $\{X_5\}$. The resolution previously shown on Figure 1 was done using the algorithm A0 [35], which is based on a classical DFS without computation of SCCs, and therefore explores a larger subgraph than sr-DFS in order to find another, larger example for X_1 .

Complexity. For boolean graphs $G = (V, E, L)$ without sink \vee -vertices (i.e., for maximal fixed point BESs without false constants in the right-hand sides of their equations), the sr-DFS algorithm has a linear-time complexity $O(|V| + |E|)$. The presence of false constants could trigger the reexploration of some vertices (those present on the portions of the scc stack that were cleared after back-propagation of false constants), increasing the complexity

of the algorithm towards quadratic-time $O((|V| + |E|)^2)$, which is the theoretical worst-case. This is the price to pay for achieving an optimal detection of examples and counterexamples in the boolean graph. However, the behaviour of the sr-DFS algorithm that we observed in practice for equivalence checking (by measuring the number of variables explored and reexplored) shows that its complexity is close to linear-time.

5 Implementation and Experiments

The `CÆSAR_SOLVE` [35] library of CADP [22] provides a generic implementation of several local BES resolution algorithms. The library was developed using the `OPEN/CÆSAR` [21] generic environment for LTS manipulation, which offers many graph exploration primitives (stacks, hash tables, edge lists, etc.). BESs are handled by `CÆSAR_SOLVE` by means of their corresponding boolean graphs, represented implicitly in a way similar to LTSS in `OPEN/CÆSAR`. This representation is application-independent, allowing to employ the resolution algorithms as computing engines for several on-the-fly verification tools of CADP: the model checker `EVALUATOR 3.x` [36, 35], the equivalence checker `BISIMULATOR 1.0` [5, 35], and the `REDUCTOR 5.0` tool for LTS generation equipped with partial order reductions.

Table 2: Algorithms of `CÆSAR_SOLVE` and their application to equivalence checking

Alg.	BES type	Strategy	Time	Memory	Condition
A0	general	DFS	$O(V + E)$	$O(V + E)$	nondeterm. LTSS
A1		BFS			
A2	acyclic	DFS		$O(V)$	one LTS acyclic
A3	disjunctive			—	
A4	conjunctive			one LTS determ., τ -free	
A5	general			$O(V + E)$	nondeterm. LTSS
A6	disjunctive	BFS		$O(V)$	—
A7	conjunctive		one LTS determ., τ -free		

Table 2 summarizes the local resolution algorithms currently available in the `CÆSAR_SOLVE` library and their application for equivalence checking within `BISIMULATOR`. All algorithms have a linear complexity w.r.t. the size of boolean graphs (number of vertices and edges). Algorithms A0, A1, and A5 can solve general BESs (without constraints on the structure of equations), A1 being BFS-based and thus able to produce small-depth diagnostics. When one LTS is deterministic (for strong equivalence) and τ -free (for weak equivalences), the resulting BES is conjunctive and can be solved using the memory-efficient algorithm A4 [35], which stores only the vertices of the boolean graph (and not its edges), i.e., only the states of the LTSS (and not their transitions). Also, when one LTS is acyclic, the resulting BES is also acyclic (i.e., it has an acyclic boolean graph) and can be solved using the memory-efficient algorithm A2. The BFS-based algorithm A7, recently added to the library, can be applied to conjunctive BESs and combines the advantages of algorithms A1 (small-depth diagnostics) and A4 (low memory consumption) when one LTS is deterministic and τ -free.

The version BISIMULATOR 2.0 includes the new BES encodings of weak equivalences defined in Section 3 and the new resolution algorithm sr-DFS given in Section 4, which was recently added to CESAR_SOLVE with the number A8. In the sequel, we present various performance measures showing the effect of these two enhancements. The LTSS considered were generated from the demo examples of CADP (specifications of communication protocols and asynchronous circuits) or taken from the VLTS benchmark suite [46].

New encodings of weak equivalences. The new BES encodings of weak equivalences that we proposed in Section 3 compute τ -closures by means of BES equations instead of relying on external, dedicated graph algorithms as the previous encodings used in BISIMULATOR 1.0. Figure 3(a)–(b) compares the performance of the two encodings for branching bisimulation as regards the size of the underlying BESS and their resolution time using algorithm A0. As expected, the BESS produced by the new encoding are larger (more variables but less operators) because intermediate results of τ -closure computations are stored as boolean variables, but they are solved faster due to the simpler structure of boolean equations. Of course, what matters from the end-user point of view is the overall performance of using sr-DFS in conjunction with the new BES encoding; this is illustrated below.

Resolution using the sr-DFS algorithm. The series of experiments shown in Figure 3(c)–(f) compare the behaviour of BISIMULATOR 1.0 (algorithm A0 and previous BES encoding) w.r.t. version 2.0 (algorithm sr-DFS and new BES encoding) for branching bisimulation. To improve readability, we separated the LTSS in two groups according to their number of transitions. When applying version 2.0, we observed reductions of both the number of vertices visited and edges explored, which determine the memory consumption and the execution time, respectively. These reductions become more important as the LTS size increases, as indicated by curves (e) and (f); in particular, the number of transitions traversed can decrease by a factor 8. It is worth noticing that some of the LTSS compared were not equivalent (e.g., certain erroneous variants of a leader election protocol examined in [23]), showing that version 2.0 of the tool exhibits a good behaviour also for counterexample detection. These experimental results indicate that the increase in BES size induced by the new encoding of weak equivalences is compensated by the reduction achieved using sr-DFS, leading to an overall improvement of the on-the-fly verification procedure. As regards strong bisimulation, sr-DFS reduces the number of variables explored by up to 25%, as shown in Figure 3(g).

Complexity w.r.t. theoretical worst-case. As pointed out in [18], the on-the-fly comparison of two nondeterministic LTSS $M_1 = \langle Q_1, A_1, T_1, q_{01} \rangle$ and $M_2 = \langle Q_2, A_2, T_2, q_{02} \rangle$ has a worst-case complexity $O((|Q_1| \cdot |T_2|) + (|Q_2| \cdot |T_1|))$. Considering the BES formulation of the problem, this complexity can be estimated in terms of BES size: the BESS given in Table 1 have a number of boolean variables proportional to the size of the synchronous product between the two LTSS. However in practice, the BESS produced from equivalence checking have a much smaller size (several orders of magnitude) than the theoretical worst-case, as it is illustrated in Figure 3(h) for strong bisimulation. This also holds for weak equivalences, in particular for branching bisimulation.

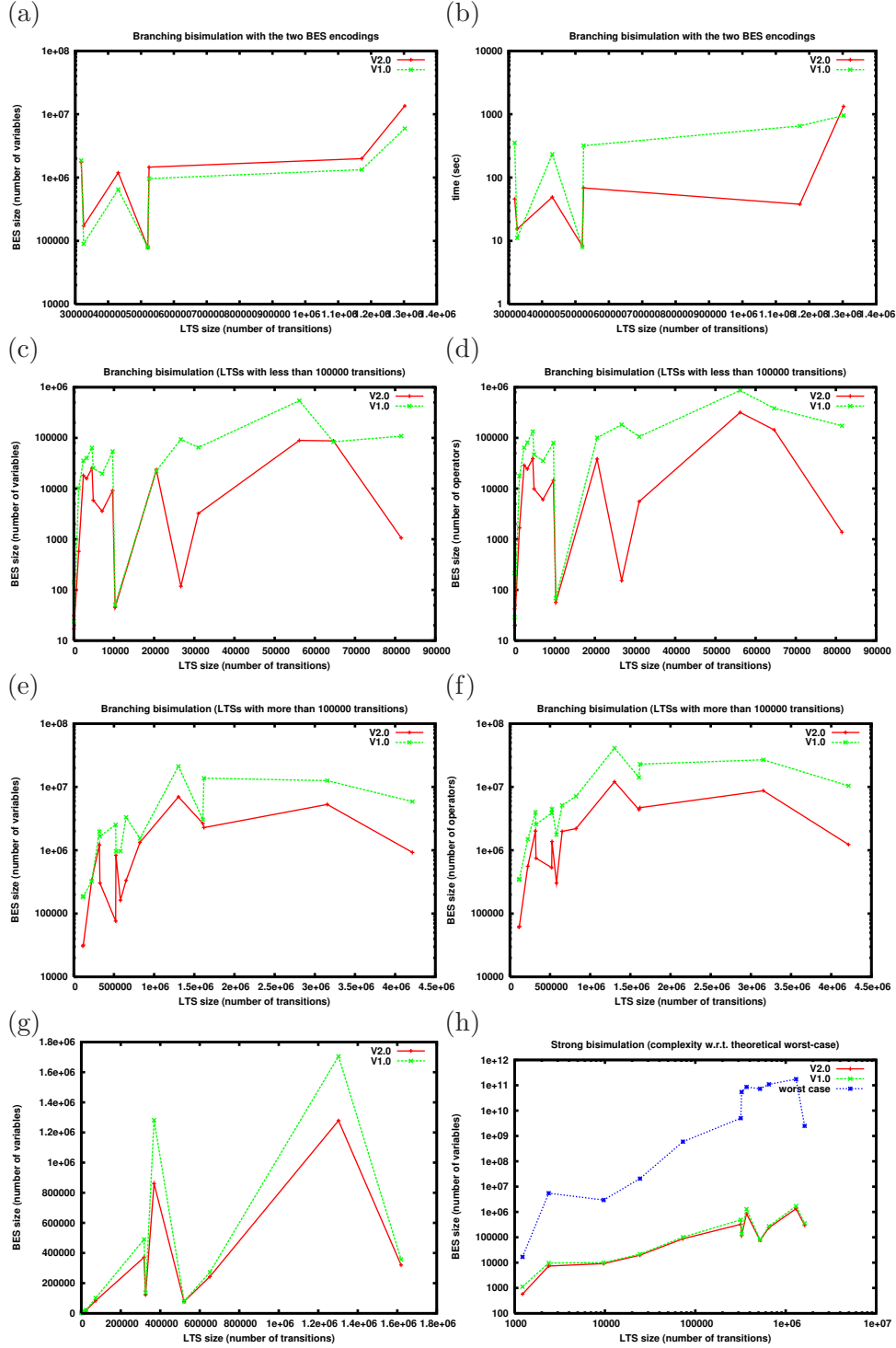


Figure 3: Performance of equivalence checking using BISIMULATOR 1.0 and 2.0

6 Conclusion and Future Work

Building efficient software tools for on-the-fly equivalence checking between LTSS is a difficult and time-consuming task. The usage of intermediate formalisms, such as BESs, allows one to separate the concerns of phrasing the verification problem and of solving it, leading to highly modular verification tools [36, 5]. The two optimizations we proposed, namely the new encodings of weak equivalences by applying τ -compression on the input LTSS and computing τ -closures using boolean equations (Section 3) and the new sr-DFS local BES resolution algorithm (Section 4) significantly increased the performance of on-the-fly equivalence checking w.r.t. existing approaches.

These optimizations underlie the new version 2.0 of the BISIMULATOR equivalence checker [35] of the CADP toolbox [22]. The sr-DFS algorithm was integrated to the generic CÆSAR_SOLVE library [35] for on-the-fly BES resolution, which is part of the generic OPEN/CÆSAR environment [21] for LTS manipulation. Local BES resolution proved to be a suitable alternative way for computing τ -closures on LTSS produced from protocols and distributed systems, competing favourably with general transitive closure algorithms. The sr-DFS algorithm is able to detect optimally the presence of both examples and counterexamples in the boolean graph, and appears to be quite effective for comparing LTSS modulo weak equivalences.

We plan to continue our work along two directions. First, the range of equivalences and preorders already available in BISIMULATOR 2.0 (strong, branching, weak, $\tau^*.a$, safety, trace, and weak trace) could be extended by devising BES encodings for other weak equivalences, such as CFFD [43] and testing equivalence [11], following the scheme in Section 3. Next, we will pursue experimenting the sr-DFS algorithm and study its applicability for solving BESs coming from other verification problems, such as the model checking of alternation-free modal μ -calculus and the on-the-fly LTS reduction modulo partial order relations (e.g., τ -confluence, τ -inertness, etc.) as formulated in [38].

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References

- [1] Henrik R. Andersen. Model Checking and Boolean Graphs. *Theoretical Computer Science*, 126(1):3–30, April 1994.
- [2] Henrik R. Andersen and Bart Vergauwen. Efficient Checking of Behavioural Relations and Modal Assertions using Fixed-Point Inversion. In Pierre Wolper, editor, *Proceedings of the 7th International Conference on Computer Aided Verification CAV'95 (Liege,*

- Belgium), volume 939 of *Lecture Notes in Computer Science*, pages 142–154. Springer Verlag, July 1995.
- [3] André Arnold and Paul Crubillé. A Linear Algorithm to Solve Fixed-Point Equations on Transition Systems. *Information Processing Letters*, 29(1):57–66, September 1988.
 - [4] G. Ausiello and G. F. Italiano. On-Line Algorithms for Polynomially Solvable Satisfiability Problems. *Journal of Logic Programming*, 10(1/2/3&4):69–90, 1991.
 - [5] Damien Bergamini, Nicolas Descoubes, Christophe Joubert, and Radu Mateescu. BISIMULATOR: A Modular Tool for On-the-Fly Equivalence Checking. In Nicolas Halbwachs and Lenore Zuck, editors, *Proceedings of the 11th International Conference on Tools and Algorithms for the Construction and Analysis of Systems TACAS'2005 (Edinburgh, Scotland, UK)*, volume 3440 of *Lecture Notes in Computer Science*, pages 581–585. Springer Verlag, April 2005.
 - [6] J. A. Bergstra and J. W. Klop. Process Algebra for Synchronous Communication. *Information and Computation*, 60:109–137, 1984.
 - [7] Jan A. Bergstra, Alban Ponse, and Scott A. Smolka. *Handbook of Process Algebra*. Elsevier, 2001.
 - [8] Ahmed Bouajjani, Jean-Claude Fernandez, Susanne Graf, Carlos Rodríguez, and Joseph Sifakis. Safety for Branching Time Semantics. In *Proceedings of 18th ICALP*. Springer Verlag, July 1991.
 - [9] S. D. Brookes, C. A. R. Hoare, and A. W. Roscoe. A Theory of Communicating Sequential Processes. *Journal of the ACM*, 31(3):560–599, July 1984.
 - [10] R. Cleaveland, J. Parrow, and B. Steffen. The Concurrency Workbench: A Semantics-Based Verification Tool for Finite State Systems. *ACM Transactions on Programming Languages and Systems*, 15(1):36–72, January 1993.
 - [11] Rance Cleaveland and Matthew Hennessy. Testing Equivalence as a Bisimulation Equivalence. *Formal Aspects of Computing*, 5(1):1–20, 1993.
 - [12] Rance Cleaveland and Oleg Sokolsky. *Equivalence and Preorder Checking for Finite-State Systems*. In Jan A. Bergstra, Alban Ponse, and Scott A. Smolka, editors, *Handbook of Process Algebra*, chapter 6, pages 391–424. North-Holland, 2001.
 - [13] Rance Cleaveland and Bernhard Steffen. Computing Behavioural Relations, Logically. In Javier Leach Albert, Burkhard Monien, and Mario Rodríguez-Artalejo, editors, *Proceedings of the 18th International Colloquium on Automata, Languages, and Programming ICALP'91 (Madrid, Spain)*, volume 510 of *Lecture Notes in Computer Science*, pages 127–138. Springer Verlag, July 1991.
 - [14] Rance Cleaveland and Bernhard Steffen. A Linear-Time Model-Checking Algorithm for the Alternation-Free Modal Mu-Calculus. *Formal Methods in System Design*, 2(2):121–147, April 1993.

-
- [15] Agostino Dovier, Carla Piazza, and Alberto Policriti. An Efficient Algorithm for Computing Bisimulation Equivalence. *Theoretical Computer Science*, 311(1–3):221–256, 2004.
- [16] W.F. Dowling and J. H. Gallier. Linear-time Algorithms for Testing the Satisfiability of Propositional Horn Formulae. *Journal of Logic Programming*, 3:267–284, 1984.
- [17] Xiaoqun Du, Scott A. Smolka, and Rance Cleaveland. Local Model Checking and Protocol Analysis. *Springer International Journal on Software Tools for Technology Transfer (STTT)*, 2(3):219–241, 1999.
- [18] Jean-Claude Fernandez and Laurent Mounier. Verifying Bisimulations “On the Fly”. In Juan Quemada, José Manas, and Enrique Vázquez, editors, *Proceedings of the 3rd International Conference on Formal Description Techniques FORTE’90 (Madrid, Spain)*. North-Holland, November 1990.
- [19] Jean-Claude Fernandez and Laurent Mounier. A Tool Set for Deciding Behavioral Equivalences. In *Proceedings of CONCUR’91 (Amsterdam, The Netherlands)*, August 1991.
- [20] Kathi Fisler and Moshe Y. Vardi. Bisimulation Minimization and Symbolic Model Checking. 21(1):39–78, 2002.
- [21] Hubert Garavel. OPEN/CÆSAR: An Open Software Architecture for Verification, Simulation, and Testing. In Bernhard Steffen, editor, *Proceedings of the First International Conference on Tools and Algorithms for the Construction and Analysis of Systems TACAS’98 (Lisbon, Portugal)*, volume 1384 of *Lecture Notes in Computer Science*, pages 68–84, Berlin, March 1998. Springer Verlag. Full version available as INRIA Research Report RR-3352.
- [22] Hubert Garavel, Frédéric Lang, Radu Mateescu, and Wendelin Serwe. CADP 2006: A Toolbox for the Construction and Analysis of Distributed Processes. In Werner Damm and Holger Hermanns, editors, *Proceedings of the 19th International Conference on Computer Aided Verification CAV’2007 (Berlin, Germany)*, volume 4590 of *Lecture Notes in Computer Science*, pages 158–163. Springer Verlag, July 2007.
- [23] Hubert Garavel and Laurent Mounier. Specification and Verification of Various Distributed Leader Election Algorithms for Unidirectional Ring Networks. *Science of Computer Programming*, 29(1–2):171–197, July 1997. Special issue on Industrially Relevant Applications of Formal Analysis Techniques. Full version available as INRIA Research Report RR-2986.
- [24] Jan Friso Groote and Misa Keinänen. Solving Disjunctive/Conjunctive Boolean Equation Systems with Alternating Fixed Points. In Kurt Jensen and Andreas Podelski, editors, *Proceedings of the 10th International Conference on Tools and Algorithms for the Construction and Analysis of Systems TACAS’2004 (Barcelona, Spain)*, volume 2988 of *Lecture Notes in Computer Science*, pages 436–450. Springer Verlag, March 2004.

- [25] A. Ingólfssdóttir and B. Steffen. Characteristic Formulae for Processes with Divergence. *Information and Computation*, 110(1):149–163, June 1994.
- [26] ISO/IEC. LOTOS — A Formal Description Technique Based on the Temporal Ordering of Observational Behaviour. International Standard 8807, International Organization for Standardization — Information Processing Systems — Open Systems Interconnection, Genève, September 1989.
- [27] S. C. Kleene. *Introduction to Metamathematics*. North-Holland, 1952.
- [28] K. G. Larsen. Efficient Local Correctness Checking. In G. v. Bochmann and D. K. Probst, editors, *Proceedings of 4th International Workshop in Computer Aided Verification CAV'92 (Montréal, Canada)*, volume 663 of *Lecture Notes in Computer Science*, pages 30–43, Berlin, June-July 1992. Springer Verlag.
- [29] X. Liu and S. A. Smolka. Simple Linear-Time Algorithms for Minimal Fixed Points. In Kim G. Larsen, Sven Skyum, and Glynn Winskel, editors, *Proceedings of the 25th International Colloquium on Automata, Languages, and Programming ICALP'98 (Aalborg, Denmark)*, volume 1443 of *Lecture Notes in Computer Science*, pages 53–66. Springer Verlag, July 1998.
- [30] Angelika Mader. *Verification of Modal Properties Using Boolean Equation Systems*. VERSAL 8, Bertz Verlag, Berlin, 1997.
- [31] Alain J. Martin. Compiling Communicating Processes into Delay-Insensitive VLSI Circuits. *Distributed Computing*, 1(4):226–234, 1986.
- [32] Radu Mateescu. Efficient Diagnostic Generation for Boolean Equation Systems. In *Proceedings of 6th International Conference on Tools and Algorithms for the Construction and Analysis of Systems TACAS'2000 (Berlin, Germany)*, LNCS. Springer Verlag, March 2000.
- [33] Radu Mateescu. Local Model-Checking of Modal Mu-Calculus on Acyclic Labeled Transition Systems. In Joost-Pieter Katoen and Perdita Stevens, editors, *Proceedings of the 8th International Conference on Tools and Algorithms for the Construction and Analysis of Systems TACAS'2002 (Grenoble, France)*, volume 2280 of *Lecture Notes in Computer Science*, pages 281–295. Springer Verlag, April 2002. Full version available as INRIA Research Report RR-4430.
- [34] Radu Mateescu. On-the-fly State Space Reductions for Weak Equivalences. In Tiziana Margaria and Mieke Massink, editors, *Proceedings of the 10th International Workshop on Formal Methods for Industrial Critical Systems FMICS'05 (Lisbon, Portugal)*, pages 80–89. ERCIM, ACM Computer Society Press, September 2005.
- [35] Radu Mateescu. CAESAR_SOLVE: A Generic Library for On-the-Fly Resolution of Alternation-Free Boolean Equation Systems. *Springer International Journal on Software Tools for Technology Transfer (STTT)*, 8(1):37–56, February 2006. Full version available as INRIA Research Report RR-5948, July 2006.

- [36] Radu Mateescu and Mihaela Sighireanu. Efficient On-the-Fly Model-Checking for Regular Alternation-Free Mu-Calculus. *Science of Computer Programming*, 46(3):255–281, March 2003.
- [37] Robin Milner. *Communication and Concurrency*. Prentice-Hall, 1989.
- [38] Gordon Pace, Frédéric Lang, and Radu Mateescu. Calculating τ -Confluence Compositionally. In Jr Warren A. Hunt and Fabio Somenzi, editors, *Proceedings of the 15th International Conference on Computer Aided Verification CAV'2003 (Boulder, Colorado, USA)*, volume 2725 of *Lecture Notes in Computer Science*, pages 446–459. Springer Verlag, July 2003. Full version available as INRIA Research Report RR-4918.
- [39] David Park. Concurrency and Automata on Infinite Sequences. In Peter Deussen, editor, *Theoretical Computer Science*, volume 104 of *Lecture Notes in Computer Science*, pages 167–183. Springer Verlag, March 1981.
- [40] S. K. Shukla, H. B. Hunt III, and D. J. Rosenkrantz. Hornsat, Model Checking, Verification and Games. In Rajeev Alur and Thomas A. Henzinger, editors, *Proceedings of the 8th International Conference on Computer Aided Verification CAV'96 (New Brunswick, New Jersey, USA)*, volume 1102 of *Lecture Notes in Computer Science*, pages 99–110. Springer Verlag, August 2006.
- [41] Robert E. Tarjan. Depth First Search and Linear Graph Algorithms. *SIAM Journal of Computing*, 1(2):146–160, 1972.
- [42] A. Tarski. A Lattice-Theoretical Fixpoint Theorem and its Applications. *Pacific Journal of Mathematics*, 5:285–309, 1955.
- [43] Antti Valmari and Martti Tienari. Compositional Failure-Based Semantics Models for Basic LOTOS. *Formal Aspects of Computing*, 7(4):440–468, 1995.
- [44] R. J. van Glabbeek and W. P. Weijland. Branching-Time and Abstraction in Bisimulation Semantics (extended abstract). CS R8911, Centrum voor Wiskunde en Informatica, Amsterdam, 1989. Also in proc. IFIP 11th World Computer Congress, San Francisco, 1989.
- [45] Rob van Glabbeek. *The Linear Time — Branching Time Spectrum I. The Semantics of Concrete, Sequential Processes*. In Jan A. Bergstra, Alban Ponse, and Scott A. Smolka, editors, *Handbook of Process Algebra*, chapter 1, pages 3–100. Elsevier, 2001.
- [46] Vasy. The VLTS Benchmark Suite. http://www.inrialpes.fr/vasy/cadp/resources/benchmark_bcg.html, June 2003.
- [47] B. Vergauwen and J. Lewi. Efficient Local Correctness Checking for Single and Alternating Boolean Equation Systems. In S. Abiteboul and E. Shamir, editors, *Proceedings of the 21st ICALP (Vienna)*, volume 820 of *Lecture Notes in Computer Science*, pages 304–315, Berlin, July 1994. Springer Verlag.

A Proofs of the BES encodings

We prove in the sequel the correctness of our BES encodings for strong and branching bisimulation. The correctness proof for weak bisimulation is very similar to the one for branching bisimulation and is therefore left as exercise for the interested reader.

A few additional notions are needed in order to proceed. Let $M_1 = \langle Q_1, A_1, T_1, q_{01} \rangle$ and $M_2 = \langle Q_2, A_2, T_2, q_{02} \rangle$ be two LTSS. Consider the two lattices $\langle \text{Bool}^{|Q_1| \cdot |Q_2|}, \sqsubseteq, \text{false}^{|Q_1| \cdot |Q_2|}, \text{true}^{|Q_1| \cdot |Q_2|}, \sqcup, \sqcap \rangle$ and $\langle 2^{Q_1 \times Q_2}, \subseteq, \emptyset, Q_1 \times Q_2, \cup, \cap \rangle$, where the relation \sqsubseteq and the operations \sqcup, \sqcap are defined as the pointwise extensions of the boolean connectors \Rightarrow, \vee , and \wedge , respectively. These lattices are isomorphic, being related by the function $\Gamma : \text{Bool}^{|Q_1| \cdot |Q_2|} \rightarrow 2^{Q_1 \times Q_2}$ defined below:

$$\Gamma(\langle b_{pq} \rangle_{p \in Q_1, q \in Q_2}) = \{ \langle p, q \rangle \mid b_{pq} = \text{true} \}.$$

It is straightforward to show that Γ is an isomorphism, i.e., it is a bijection preserving the compatibility of operations ($\mathbf{b} \sqsubseteq \mathbf{b}' \Leftrightarrow \Gamma(\mathbf{b}) \subseteq \Gamma(\mathbf{b}')$, $\Gamma(\text{false}^{|Q_1| \cdot |Q_2|}) = \emptyset$, $\Gamma(\text{true}^{|Q_1| \cdot |Q_2|}) = Q_1 \times Q_2$, $\Gamma(\mathbf{b} \sqcup \mathbf{b}') = \Gamma(\mathbf{b}) \cup \Gamma(\mathbf{b}')$, and $\Gamma(\mathbf{b} \sqcap \mathbf{b}') = \Gamma(\mathbf{b}) \cap \Gamma(\mathbf{b}')$).

A.1 BES encoding of strong bisimulation

We recall below the definitions of strong bisimulation [39] and the corresponding BES encoding given in Section 3.

Definition 1 (Strong bisimulation) *Let $M_1 = \langle Q_1, A_1, T_1, q_{01} \rangle$, $M_2 = \langle Q_2, A_2, T_2, q_{02} \rangle$ be two LTSS. A relation $\approx \subseteq Q_1 \times Q_2$ is a bisimulation between M_1 and M_2 iff for every $p \in Q_1$ and $q \in Q_2$, $p \approx q$ if the two conditions below are satisfied:*

1. $\forall p \xrightarrow{a} p' \in T_1. \exists q \xrightarrow{a} q' \in T_2. p' \approx q'$;
2. $\forall q \xrightarrow{a} q' \in T_2. \exists p \xrightarrow{a} p' \in T_1. p' \approx q'$.

The strong bisimulation $\approx_s \subseteq Q_1 \times Q_2$ is the union of all bisimulations between M_1 and M_2 .

Definition 2 (Strong bisimulation BES) *Let $M_1 = \langle Q_1, A_1, T_1, q_{01} \rangle$, $M_2 = \langle Q_2, A_2, T_2, q_{02} \rangle$ be two LTSS. The strong bisimulation $\approx_s \subseteq Q_1 \times Q_2$ is encoded by the maximal fixed point BES below:*

$$B_s = \left\{ X_{pq} \stackrel{\nu}{=} \bigwedge_{p \xrightarrow{a} p'} \bigvee_{q \xrightarrow{a} q'} X_{p'q'} \wedge \bigwedge_{q \xrightarrow{a} q'} \bigvee_{p \xrightarrow{a} p'} X_{p'q'} \right\}_{p \in Q_1, q \in Q_2}$$

The interpretation $\llbracket B_s \rrbracket$ is defined as the maximal fixed point $\nu \Phi_s$, where $\Phi_s : \text{Bool}^{|Q_1| \cdot |Q_2|} \rightarrow \text{Bool}^{|Q_1| \cdot |Q_2|}$ is the (monotonic) functional associated to B_s :

$$\Phi_s(\langle b_{pq} \rangle_{p \in Q_1, q \in Q_2}) = \langle \llbracket \bigwedge_{p \xrightarrow{a} p'} \bigvee_{q \xrightarrow{a} q'} X_{p'q'} \wedge \bigwedge_{q \xrightarrow{a} q'} \bigvee_{p \xrightarrow{a} p'} X_{p'q'} \rrbracket [b_{pq} / X_{pq}] \rangle_{p \in Q_1, q \in Q_2}.$$

According to Tarski's theorem [42], the maximal fixed point $\nu\Phi_s$ can be computed as follows:

$$\nu\Phi_s = \bigsqcup \{ \mathbf{b} \in \text{Bool}^{|Q_1| \cdot |Q_2|} \mid \mathbf{b} \sqsubseteq \Phi_s(\mathbf{b}) \}.$$

The following lemma provides a link between bisimulations and the functional associated to the BES above.

Lemma 1 *Let $M_1 = \langle Q_1, A_1, T_1, q_{01} \rangle$, $M_2 = \langle Q_2, A_2, T_2, q_{02} \rangle$ be two LTSS, and let $\mathbf{b} \in \text{Bool}^{|Q_1| \cdot |Q_2|}$. Then:*

$$\mathbf{b} \sqsubseteq \Phi_s(\mathbf{b}) \text{ iff } \Gamma(\mathbf{b}) \text{ is a bisimulation.}$$

Proof. If. Let $\mathbf{b} = \langle b_{pq} \rangle_{p \in Q_1, q \in Q_2}$ such that $\Gamma(\mathbf{b})$ is a bisimulation. We must show that $\mathbf{b} \sqsubseteq \Phi_s(\mathbf{b})$.

Let $p \in Q_1, q \in Q_2$ such that $b_{pq} = \text{true}$. From the definition of Γ , this implies $\langle p, q \rangle \in \Gamma(\mathbf{b})$. Since $\Gamma(\mathbf{b})$ is a bisimulation, from Definition 1 this implies the two conditions below:

$$\forall p \xrightarrow{a} p' \in T_1. \exists q \xrightarrow{a} q' \in T_2. \langle p', q' \rangle \in \Gamma(\mathbf{b}) \quad \text{and} \quad \forall q \xrightarrow{a} q' \in T_2. \exists p \xrightarrow{a} p' \in T_1. \langle p', q' \rangle \in \Gamma(\mathbf{b})$$

Let $p \xrightarrow{a} p' \in T_1$. From the first condition above, there exists $q \xrightarrow{a} q' \in T_2$ such that $\langle p', q' \rangle \in \Gamma(\mathbf{b})$. From the definition of Γ , this implies $b_{p'q'} = \text{true}$, which means that the boolean formula $\bigwedge_{p \xrightarrow{a} p'} \bigvee_{q \xrightarrow{a} q'} b_{p'q'}$ is true. A symmetric reasoning applied to the second condition above shows that the formula $\bigwedge_{q \xrightarrow{a} q'} \bigvee_{p \xrightarrow{a} p'} b_{p'q'}$ is also true. From the interpretation of boolean formulas as given in Section 2 and Definition 2, this implies:

$$\llbracket \bigwedge_{p \xrightarrow{a} p'} \bigvee_{q \xrightarrow{a} q'} X_{p'q'} \wedge \bigwedge_{q \xrightarrow{a} q'} \bigvee_{p \xrightarrow{a} p'} X_{p'q'} \rrbracket [b_{pq}/X_{pq}]_{p \in Q_1, q \in Q_2} = \text{true}$$

meaning that $\Phi_s(\mathbf{b}) = \text{true}$. Therefore, $\mathbf{b} \sqsubseteq \Phi_s(\mathbf{b})$.

Only if. Let $\mathbf{b} = \langle b_{pq} \rangle_{p \in Q_1, q \in Q_2}$ such that $\mathbf{b} \sqsubseteq \Phi_s(\mathbf{b})$. We must show that $\Gamma(\mathbf{b})$ is a bisimulation, i.e., it satisfies the two conditions stated in Definition 1.

Let $\langle p, q \rangle \in \Gamma(\mathbf{b})$. From the definition of Γ , this implies $b_{pq} = \text{true}$. Since $\mathbf{b} \sqsubseteq \Phi_s(\mathbf{b})$, from Definition 2 and the interpretation of boolean formulas defined in Section 2, this implies:

$$\llbracket \bigwedge_{p \xrightarrow{a} p'} \bigvee_{q \xrightarrow{a} q'} X_{p'q'} \rrbracket [b_{pq}/X_{pq}]_{p \in Q_1, q \in Q_2} \wedge \llbracket \bigwedge_{q \xrightarrow{a} q'} \bigvee_{p \xrightarrow{a} p'} X_{p'q'} \rrbracket [b_{pq}/X_{pq}]_{p \in Q_1, q \in Q_2} = \text{true}.$$

In order this equality to hold, both conjuncts must be true, which yields, after further applying the interpretation of boolean formulas, the two conditions below:

$$\bigwedge_{p \xrightarrow{a} p'} \bigvee_{q \xrightarrow{a} q'} b_{p'q'} = \text{true} \quad \text{and} \quad \bigwedge_{q \xrightarrow{a} q'} \bigvee_{p \xrightarrow{a} p'} b_{p'q'} = \text{true}.$$

Let $p \xrightarrow{a} p' \in T_1$. From the first condition above, each conjunct associated to such a transition must be true, i.e., $\bigvee_{q \xrightarrow{a} q'} b_{p'q'} = \text{true}$. This means that some disjunct corresponding to a transition $q \xrightarrow{a} q' \in T_2$ must be true, i.e., there exists such a transition such that $b_{p'q'} = \text{true}$. From the definition of Γ , this implies $\langle p', q' \rangle \in \Gamma(\mathbf{b})$, which means that $\Gamma(\mathbf{b})$ satisfies condition 1 of Definition 1. A symmetric reasoning applied to the second condition above shows that $\Gamma(\mathbf{b})$ also satisfies condition 2 of Definition 1, and therefore $\Gamma(\mathbf{b})$ is a bisimulation. \square

We are now ready to show the correctness of the BES encoding for strong bisimulation.

Proposition 1 (Correctness of strong bisimulation BES) *Let $M_1 = \langle Q_1, A_1, T_1, q_{01} \rangle$, $M_2 = \langle Q_2, A_2, T_2, q_{02} \rangle$ be two LTSSs, and let B_s be the BES encoding the strong bisimulation between M_1 and M_2 . Then:*

$$\Gamma(\llbracket B_s \rrbracket) = \approx_s .$$

Proof.

$$\begin{aligned} \Gamma(\llbracket B_s \rrbracket) &= \Gamma(\nu \Phi_s) && \text{by Definition 2} \\ &= \Gamma(\bigsqcup \{ \mathbf{b} \mid \mathbf{b} \in \text{Bool}^{|Q_1|+|Q_2|} \wedge \mathbf{b} \sqsubseteq \Phi_s(\mathbf{b}) \}) && \text{by Tarski's theorem} \\ &= \bigcup \{ \Gamma(\mathbf{b}) \mid \mathbf{b} \in \text{Bool}^{|Q_1|+|Q_2|} \wedge \mathbf{b} \sqsubseteq \Phi_s(\mathbf{b}) \} && \text{by } \Gamma \text{ isomorphism} \\ &= \bigcup \{ \Gamma(\mathbf{b}) \mid \mathbf{b} \in \text{Bool}^{|Q_1|+|Q_2|} \wedge \Gamma(\mathbf{b}) \text{ is a bisimulation} \} && \text{by Lemma 1} \\ &= \bigcup \{ \approx \subseteq Q_1 \times Q_2 \mid \approx \text{ is a bisimulation} \} && \text{by } \Gamma \text{ bijection} \\ &= \approx_s && \text{by Definition 1.} \end{aligned}$$

□

A.2 BES encoding of branching bisimulation

We recall below the definitions of branching bisimulation [44] and the corresponding BES encoding given in Section 3.

Definition 3 (Branching bisimulation) *Let $M_1 = \langle Q_1, A_1, T_1, q_{01} \rangle$, $M_2 = \langle Q_2, A_2, T_2, q_{02} \rangle$ be two LTSSs. A relation $\approx \subseteq Q_1 \times Q_2$ is a branching-like bisimulation between M_1 and M_2 iff for every $p \in Q_1$ and $q \in Q_2$, $p \approx q$ if the two conditions below are satisfied:*

1. $\forall p \xrightarrow{a} p' \in T_1. ((a = \tau \wedge p' \approx q) \vee \exists q' \xrightarrow{\tau^*} q' \xrightarrow{a} q'' \in T_2^*. (p \approx q' \wedge p' \approx q''))$;
2. $\forall q \xrightarrow{a} q' \in T_2. ((a = \tau \wedge p \approx q') \vee \exists p' \xrightarrow{\tau^*} p' \xrightarrow{a} p'' \in T_1^*. (p' \approx q \wedge p'' \approx q'))$.

The branching bisimulation $\approx_b \subseteq Q_1 \times Q_2$ is the union of all branching-like bisimulations between M_1 and M_2 .

Definition 4 (Branching bisimulation BES) *Let $M_1 = \langle Q_1, A_1, T_1, q_{01} \rangle$, $M_2 = \langle Q_2, A_2, T_2, q_{02} \rangle$ be two τ -convergent LTSSs. The branching bisimulation $\approx_b \subseteq Q_1 \times Q_2$ is encoded by the maximal fixed point BES below:*

$$B_b = \left\{ \begin{array}{l} X_{pq} \stackrel{\nu}{=} \bigwedge_{p \xrightarrow{a} p'} Y_{pp'qa} \wedge \bigwedge_{q \xrightarrow{a} q'} Z_{pqq'a} \\ Y_{pp'qa} \stackrel{\nu}{=} (a = \tau \wedge X_{p'q}) \vee U_{pp'qa} \\ U_{pp'qa} \stackrel{\nu}{=} \bigvee_{q \xrightarrow{a} q'} W_{pp'qq'} \vee \bigvee_{q \xrightarrow{\tau} q'} U_{pp'q'a} \\ Z_{pqq'a} \stackrel{\nu}{=} (a = \tau \wedge X_{pq'}) \vee V_{pqq'a} \\ V_{pqq'a} \stackrel{\nu}{=} \bigvee_{p \xrightarrow{a} p'} W_{pp'qq'} \vee \bigvee_{p \xrightarrow{\tau} p'} V_{p'qq'a} \\ W_{pp'qq'} \stackrel{\nu}{=} X_{pq} \wedge X_{p'q'} \end{array} \right\}_{p, p' \in Q_1, q, q' \in Q_2, a \in A_1 \cup A_2}$$

We first prove several lemmas concerning BES manipulation, and then we prove the correctness of the BES encoding for branching bisimulation.

Definition 5 Let \mathcal{X} be a set of boolean variables including X_1, \dots, X_n, X_{n+1} . Let $B = \{X_i \stackrel{\nu}{=} \varphi_i\}_{1 \leq i \leq n}$ and $B' = \{X_i \stackrel{\nu}{=} \varphi_i\}_{1 \leq i \leq n+1}$ be two BESs having their first n equations identical (φ_i are boolean formulas built from disjunctions and conjunctions) and $\delta : \mathcal{X} \rightarrow \text{Bool}$ be a context. Let $\Phi_\delta : \text{Bool}^n \rightarrow \text{Bool}^n$ and $\Phi'_\delta : \text{Bool}^{n+1} \rightarrow \text{Bool}^{n+1}$ be the two functionals associated to B and B' in the context δ :

$$\begin{aligned}\Phi_\delta(\langle b_i \rangle_{1 \leq i \leq n}) &= \langle \llbracket \varphi_i \rrbracket(\delta \circ [b_j/X_j]_{1 \leq j \leq n}) \rangle_{1 \leq i \leq n} \\ \Phi'_\delta(\langle b_i \rangle_{1 \leq i \leq n+1}) &= \langle \llbracket \varphi_i \rrbracket(\delta \circ [b_j/X_j]_{1 \leq j \leq n+1}) \rangle_{1 \leq i \leq n+1}\end{aligned}$$

where $\delta \circ [b_j/X_j]_{1 \leq j \leq n}$ denotes a context identical to δ except for variables X_1, \dots, X_n , which are assigned values b_1, \dots, b_n . According to Kleene's theorem [27], the maximal fixed points of the functionals Φ_δ and Φ'_δ can be computed as follows:

$$\nu\Phi_\delta = \bigcap_{k \geq 0} \Phi_\delta^k(\text{true}^n) \quad \nu\Phi'_\delta = \bigcap_{k \geq 0} \Phi'_\delta^k(\text{true}^{n+1}).$$

The notation $\langle e_i, e \rangle_{1 \leq i \leq n}$, where e_i and e are boolean expressions, is a shorthand for $\langle e_1, \dots, e_n, e \rangle$. We define the series $U_k \in \text{Bool}^{n+1}$ associated to B , B' , and δ as follows:

$$U_0 = \text{true}^{n+1}, \quad U_{k+1} = \langle (\nu\Phi_{\delta \circ [(\Phi'_\delta(U_k))_{n+1}/X_{n+1}]})_i, (\Phi'_\delta(U_k))_{n+1} \rangle_{1 \leq i \leq n}.$$

The following lemma relates the maximal fixed points of the two functionals Φ_δ and Φ'_δ .

Lemma 2 Consider two BESs B , B' and a context δ as in Definition 5. Then:

$$(\nu\Phi_{\delta \circ [(\nu\Phi'_\delta)_{n+1}/X_{n+1}]})_i \sqsupseteq (\nu\Phi'_\delta)_i$$

for all $1 \leq i \leq n$.

Proof. According to Kleene's theorem [27], the maximal fixed point of Φ has the iterative characterization below:

$$\nu\Phi_{\delta \circ [(\nu\Phi'_\delta)_{n+1}/X_{n+1}]} = \bigcap_{k \geq 0} \Phi_{\delta \circ [(\nu\Phi'_\delta)_{n+1}/X_{n+1}]}^k(\text{true}^n).$$

Therefore, to show the desired inequality, it is sufficient to show the following statement for all $k \geq 0$ and $1 \leq i \leq n$:

$$(\Phi_{\delta \circ [(\nu\Phi'_\delta)_{n+1}/X_{n+1}]}^k(\text{true}^n))_i \sqsupseteq (\nu\Phi'_\delta)_i.$$

We proceed by induction on k .

Base step. $(\Phi_{\delta \circ [(\nu\Phi'_\delta)_{n+1}/X_{n+1}]}^0(\text{true}^n))_i = (\text{true}^n)_i \sqsupseteq (\nu\Phi'_\delta)_i$.

Inductive step.

$$\begin{aligned}
(\Phi_{\delta \circ [(\nu \Phi'_\delta)_{n+1}/X_{n+1}]}^{k+1}(\text{true}^n))_i &= (\Phi_{\delta \circ [(\nu \Phi'_\delta)_{n+1}/X_{n+1}]}^k(\Phi_{\delta \circ [(\nu \Phi'_\delta)_{n+1}/X_{n+1}]}^k(\text{true}^n)))_i \\
&\quad \text{by definition} \\
&\supseteq (\Phi_{\delta \circ [(\nu \Phi'_\delta)_{n+1}/X_{n+1}]}(\langle (\nu \Phi'_\delta)_j \rangle_{1 \leq j \leq n}))_i \\
&\quad \text{by induction hypothesis} \\
&= \llbracket \varphi_i \rrbracket ((\delta \circ [(\nu \Phi'_\delta)_{n+1}/X_{n+1}]) \circ [(\nu \Phi'_\delta)_j/X_j]_{1 \leq j \leq n}) \\
&\quad \text{by definition of } \Phi \\
&= \llbracket \varphi_i \rrbracket (\delta \circ [(\nu \Phi'_\delta)_j/X_j]_{1 \leq j \leq n+1}) \\
&= (\Phi'_\delta(\nu \Phi'_\delta))_i \quad \text{by definition of } \Phi' \\
&= (\nu \Phi'_\delta)_i \quad \text{by definition of } \nu.
\end{aligned}$$

□

The following lemma states two useful properties of the series U_k .

Lemma 3 Consider two BESs B, B' , a context δ , and the associated series U_k as in Definition 5. The following properties hold for all $k \geq 0$:

1. $U_k \supseteq \Phi'_\delta(U_k) \supseteq U_{k+1}$;
2. $\Phi_\delta^k(\text{true}^{n+1}) \supseteq U_k \supseteq \nu \Phi'_\delta$.

Proof.

Property 1. We proceed by induction on k .

Base step. From the definition of U_k , we derive the first inequality: $U_0 = \text{true}^{n+1} \supseteq \Phi'_\delta(U_0)$.

We derive now the second inequality:

$$\begin{aligned}
\Phi'_\delta(U_0) &= \Phi'_\delta(\text{true}^{n+1}) && \text{by definition of } U_k \\
&= \langle \llbracket \varphi_i \rrbracket (\delta \circ [\text{true}/X_j]_{1 \leq j \leq n+1}) \rangle_{1 \leq i \leq n+1} && \text{by definition of } \Phi'_\delta \\
&= \langle (\Phi_{\delta \circ [\text{true}/X_{n+1}]}(\text{true}^n))_i, (\Phi'_\delta(\text{true}^{n+1}))_{n+1} \rangle_{1 \leq i \leq n} && \text{by definition of } \Phi_\delta, \Phi'_\delta \\
&\supseteq \langle (\nu \Phi_{\delta \circ [\text{true}/X_{n+1}]})_i, (\Phi'_\delta(\text{true}^{n+1}))_{n+1} \rangle_{1 \leq i \leq n} && \text{by Kleene's theorem} \\
&\supseteq \langle (\nu \Phi_{\delta \circ [(\Phi'_\delta(\text{true}^{n+1}))_{n+1}/X_{n+1}]}))_i, (\Phi'_\delta(\text{true}^{n+1}))_{n+1} \rangle_{1 \leq i \leq n} && \text{by monotonicity} \\
&= U_1 && \text{by definition of } U_k.
\end{aligned}$$

Inductive step. We derive the first inequality:

$$\begin{aligned}
U_{k+1} &= \langle (\nu \Phi_{\delta \circ [(\Phi'_\delta(U_k))_{n+1}/X_{n+1}]}))_i, (\Phi'_\delta(U_k))_{n+1} \rangle_{1 \leq i \leq n} && \text{by definition of } U_k \\
&= \langle (\Phi_{\delta \circ [(\Phi'_\delta(U_k))_{n+1}/X_{n+1}]}(\nu \Phi_{\delta \circ [(\Phi'_\delta(U_k))_{n+1}/X_{n+1}]}))_i, (\Phi'_\delta(U_k))_{n+1} \rangle_{1 \leq i \leq n} \\
&\quad \text{by definition of } \nu \\
&= \langle (\Phi_{\delta \circ [(\Phi'_\delta(U_k))_{n+1}/X_{n+1}]}(\langle (U_{k+1})_j \rangle_{1 \leq j \leq n}))_i, (\Phi'_\delta(U_k))_{n+1} \rangle_{1 \leq i \leq n} \\
&\quad \text{by definition of } U_k \\
&\supseteq \langle (\Phi_{\delta \circ [(U_{k+1})_{n+1}/X_{n+1}]}(\langle (U_{k+1})_j \rangle_{1 \leq j \leq n}))_i, (\Phi'_\delta(U_{k+1}))_{n+1} \rangle_{1 \leq i \leq n} \\
&\quad \text{by induction hypothesis and monotonicity} \\
&= \langle \llbracket \varphi_i \rrbracket (\delta \circ [(U_{k+1})_j/X_j]_{1 \leq j \leq n+1}), \llbracket \varphi_{n+1} \rrbracket (\delta \circ [(U_{k+1})_j/X_j]_{1 \leq j \leq n+1}) \rangle_{1 \leq i \leq n} \\
&\quad \text{by definition of } \Phi_\delta \text{ and } \Phi'_\delta \\
&= \Phi'_\delta(U_{k+1}) \text{ by definition of } \Phi'_\delta.
\end{aligned}$$

We derive now the second inequality:

$$\begin{aligned}
\Phi'_\delta(U_{k+1}) &= \langle \llbracket \varphi_i \rrbracket (\delta \circ [(U_{k+1})_j / X_j]_{1 \leq j \leq n+1}), \llbracket \varphi_{n+1} \rrbracket (\delta \circ [(U_{k+1})_j / X_j]_{1 \leq j \leq n+1}) \rangle_{1 \leq i \leq n} \\
&\quad \text{by definition of } \Phi'_\delta \\
&= \langle (\Phi_{\delta \circ [(U_{k+1})_{n+1} / X_{n+1}]}(\langle (U_{k+1})_j \rangle_{1 \leq j \leq n}))_i, (\Phi'_\delta(U_{k+1}))_{n+1} \rangle_{1 \leq i \leq n} \\
&\quad \text{by definition of } \Phi_\delta \text{ and } \Phi'_\delta \\
&\sqsupseteq \langle (\Phi_{\delta \circ [(\Phi'_\delta(U_{k+1}))_{n+1} / X_{n+1}]}(\langle (U_{k+1})_j \rangle_{1 \leq j \leq n}))_i, (\Phi'_\delta(U_{k+1}))_{n+1} \rangle_{1 \leq i \leq n} \\
&\quad \text{by the property above and monotonicity} \\
&= \langle (\Phi_{\delta \circ [(\Phi'_\delta(U_{k+1}))_{n+1} / X_{n+1}]}(\nu \Phi_{\delta \circ [(\Phi'_\delta(U_k))_{n+1} / X_{n+1}]})_i, (\Phi'_\delta(U_{k+1}))_{n+1} \rangle_{1 \leq i \leq n} \\
&\quad \text{by definition of } U_k \\
&\sqsupseteq \langle (\Phi_{\delta \circ [(\Phi'_\delta(U_{k+1}))_{n+1} / X_{n+1}]}(\nu \Phi_{\delta \circ [(\Phi'_\delta(U_{k+1}))_{n+1} / X_{n+1}]})_i, (\Phi'_\delta(U_{k+1}))_{n+1} \rangle_{1 \leq i \leq n} \\
&\quad \text{by induction hypothesis and monotonicity} \\
&= \langle (\nu \Phi_{\delta \circ [(\Phi'_\delta(U_{k+1}))_{n+1} / X_{n+1}]})_i, (\Phi'_\delta(U_{k+1}))_{n+1} \rangle_{1 \leq i \leq n} \quad \text{by definition of } \nu \\
&= U_{k+2} \quad \text{by definition of } U_k.
\end{aligned}$$

Property 2. We proceed by induction on k .

Base step. $\Phi'_\delta{}^0(\text{true}^{n+1}) = \text{true}^{n+1} = U_0 \sqsupseteq \nu \Phi'_\delta$.

Inductive step. We derive the first inequality:

$$\begin{aligned}
\Phi'_\delta{}^{k+1}(\text{true}^{n+1}) &= \Phi'_\delta(\Phi'_\delta{}^k(\text{true}^{n+1})) && \text{by definition of } \Phi'_\delta \\
&\sqsupseteq \Phi'_\delta(U_k) && \text{by induction hypothesis} \\
&= \langle \llbracket \varphi_i \rrbracket (\delta \circ [(U_k)_j / X_j]_{1 \leq j \leq n+1}), \llbracket \varphi_{n+1} \rrbracket (\delta \circ [(U_k)_j / X_j]_{1 \leq j \leq n+1}) \rangle_{1 \leq i \leq n} \\
&\quad \text{by definition of } \Phi'_\delta \\
&= \langle (\Phi_{\delta \circ [(U_k)_{n+1} / X_{n+1}]}(\langle (U_k)_j \rangle_{1 \leq j \leq n}))_i, (\Phi'_\delta(U_k))_{n+1} \rangle_{1 \leq i \leq n} \\
&\quad \text{by definition of } \Phi_\delta \text{ and } \Phi'_\delta \\
&= \langle (\Phi_{\delta \circ [(U_k)_{n+1} / X_{n+1}]}(\nu \Phi_{\delta \circ [(\Phi'_\delta(U_{k-1}))_{n+1} / X_{n+1}]})_i, (\Phi'_\delta(U_k))_{n+1} \rangle_{1 \leq i \leq n} \\
&\quad \text{by definition of } U_k \\
&\sqsupseteq \langle (\Phi_{\delta \circ [(U_k)_{n+1} / X_{n+1}]}(\nu \Phi_{\delta \circ [(U_k)_{n+1} / X_{n+1}]})_i, (\Phi'_\delta(U_k))_{n+1} \rangle_{1 \leq i \leq n} \\
&\quad \text{by Property 1} \\
&= \langle (\nu \Phi_{\delta \circ [(U_k)_{n+1} / X_{n+1}]})_i, (\Phi'_\delta(U_k))_{n+1} \rangle_{1 \leq i \leq n} \quad \text{by definition of } \nu \\
&\sqsupseteq \langle (\nu \Phi_{\delta \circ [(\Phi'_\delta(U_k))_{n+1} / X_{n+1}]})_i, (\Phi'_\delta(U_k))_{n+1} \rangle_{1 \leq i \leq n} \quad \text{by Property 1} \\
&= U_{k+1} \quad \text{by definition of } U_k.
\end{aligned}$$

We derive now the second inequality:

$$\begin{aligned}
U_{k+1} &= \langle (\nu \Phi_{\delta \circ [(\Phi'_\delta(U_k))_{n+1} / X_{n+1}]})_i, (\Phi'_\delta(U_k))_{n+1} \rangle_{1 \leq i \leq n} && \text{by definition of } U_k \\
&\sqsupseteq \langle (\nu \Phi_{\delta \circ [(\Phi'_\delta(\nu \Phi'_\delta))_{n+1} / X_{n+1}]})_i, (\Phi'_\delta(\nu \Phi'_\delta))_{n+1} \rangle_{1 \leq i \leq n} && \text{by induction hypothesis} \\
&= \langle (\nu \Phi_{\delta \circ [(\nu \Phi'_\delta)_{n+1} / X_{n+1}]})_i, (\nu \Phi'_\delta)_{n+1} \rangle_{1 \leq i \leq n} && \text{by definition of } \nu \\
&\sqsupseteq \langle (\nu \Phi'_\delta)_i, (\nu \Phi'_\delta)_{n+1} \rangle_{1 \leq i \leq n} && \text{by Lemma 2} \\
&= \nu \Phi'_\delta \text{ by definition.}
\end{aligned}$$

□

The following lemma states that appending an equation to two BESs that have the same interpretation yields new BESs still having the same interpretation.

Lemma 4 (Equation appending) *Consider two couples of BESs $B = \{X_i \stackrel{\nu}{=} \varphi_i\}_{1 \leq i \leq n}$, $B' = \{X_i \stackrel{\nu}{=} \varphi_i\}_{1 \leq i \leq n+1}$ and $C = \{X_i \stackrel{\nu}{=} \psi_i\}_{1 \leq i \leq n}$, $C' = \{X_i \stackrel{\nu}{=} \psi_i\}_{1 \leq i \leq n+1}$ like in Definition 5, such that φ_{n+1} and ψ_{n+1} are identical. Let Φ_δ , Φ'_δ and Ψ_δ , Ψ'_δ be the functionals*

associated to B, B' and C, C' in a context δ . Suppose that B and C have the same interpretation in any context δ , i.e., $\nu\Phi_\delta = \nu\Psi_\delta$. Then:

$$\nu\Phi'_\delta = \nu\Psi'_\delta$$

for any context δ .

Proof. Let δ be a context. We show the desired equality by double inclusion.

Inclusion $\nu\Phi'_\delta \sqsupseteq \nu\Psi'_\delta$. Consider the two series U_k and U'_k associated to the couples of BESS B, B' and C, C' in the context δ as in Definition 5. We show first that for all $k \geq 0$, $U_k = U'_k$. We proceed by induction on k .

Base step. $U_0 = \text{true}^{n+1} = U'_0$.

Inductive step. We first show the statement below:

$$\begin{aligned} (\Phi'_\delta(U_k))_{n+1} &= \llbracket \varphi_{n+1} \rrbracket (\delta \otimes [(U_k)_j / X_j]_{1 \leq j \leq n+1}) && \text{by definition of } \Phi'_\delta \\ &= \llbracket \psi_{n+1} \rrbracket (\delta \otimes [(U_k)_j / X_j]_{1 \leq j \leq n+1}) && \text{by hypothesis} \\ &= (\Psi'_\delta(U_k))_{n+1} && \text{by definition of } \Psi'_\delta \\ &= (\Psi'_\delta(U'_k))_{n+1} && \text{by induction hypothesis.} \end{aligned}$$

We show the equality:

$$\begin{aligned} U_{k+1} &= \langle (\nu\Phi_{\delta \otimes [(U'_k)_j / X_j]_{1 \leq j \leq n+1}})_{n+1}, (\Phi'_\delta(U_k))_{n+1} \rangle_{1 \leq i \leq n} && \text{by definition of } U_k \\ &= \langle (\nu\Phi_{\delta \otimes [(\Psi'_\delta(U'_k))_{n+1} / X_{n+1}]})_i, (\Psi'_\delta(U'_k))_{n+1} \rangle_{1 \leq i \leq n} && \text{by the property above} \\ &= \langle (\nu\Psi_{\delta \otimes [(\Psi'_\delta(U'_k))_{n+1} / X_{n+1}]})_i, (\Psi'_\delta(U'_k))_{n+1} \rangle_{1 \leq i \leq n} && \text{by hypothesis} \\ &= U'_{k+1} && \text{by definition of } U'_k. \end{aligned}$$

We now prove the desired inclusion by using the Kleene's characterization of Φ'_δ and proving that for all $k \geq 0$, $\Phi'^k_\delta(\text{true}^{n+1}) \sqsupseteq \nu\Psi'_\delta$:

$$\begin{aligned} \Phi'^k_\delta(\text{true}^{n+1}) &\sqsupseteq U_k && \text{by Lemma 3, property 2} \\ &= U'_k && \text{by the property above} \\ &\sqsupseteq \nu\Psi'_\delta && \text{by Lemma 3, property 2.} \end{aligned}$$

Inclusion $\nu\Phi'_\delta \sqsubseteq \nu\Psi'_\delta$. The proof is symmetric to the converse inclusion.

This concludes the proof of the desired equality. \square

We are now ready to show the correctness of the BES encoding for branching bisimulation. We show first a lemma concerning the computation of τ -closures using boolean equations, and then we show the main proposition.

Lemma 5 *Let $M_1 = \langle Q_1, A_1, T_1, q_{01} \rangle$, $M_2 = \langle Q_2, A_2, T_2, q_{02} \rangle$ be two τ -convergent LTSSs, and consider the BESS:*

$$B = \left\{ \begin{array}{l} U_{pp'qa} \stackrel{\nu}{=} \bigvee_{q \xrightarrow{\tau^*} q'} \bigvee_{q' \xrightarrow{a} q''} W_{pp'q'q''} \\ V_{pqq'a} \stackrel{\nu}{=} \bigvee_{p \xrightarrow{\tau^*} p'} \bigvee_{p' \xrightarrow{a} p''} W_{p'p''qq'} \end{array} \right\}_{p, p' \in Q_1, q, q' \in Q_2, a \in A_1 \cup A_2}$$

and

$$B' = \left\{ \begin{array}{l} U_{pp'qa} \stackrel{\nu}{=} \bigvee_{q \rightarrow q'} W_{pp'qq'} \vee \bigvee_{q \xrightarrow{\tau} q'} U_{pp'q'a} \\ V_{ppq'a} \stackrel{\nu}{=} \bigvee_{p \rightarrow p'} W_{pp'qq'} \vee \bigvee_{p \xrightarrow{\tau} p'} V_{p'qq'a} \end{array} \right\}_{p,p' \in Q_1, q, q' \in Q_2, a \in A_1 \cup A_2.}$$

Then:

$$\llbracket B \rrbracket \delta = \llbracket B' \rrbracket \delta$$

for any context $\delta : \mathcal{X} \rightarrow \mathbf{Bool}$, where the set \mathcal{X} contains the variables $U_{pp'qa}$ and $V_{ppq'a}$.

Proof. Let $\delta : \mathcal{X} \rightarrow \mathbf{Bool}$ be a context and $\Phi_\delta, \Phi'_\delta : \mathbf{Bool}^{|Q_1|^2 \cdot |Q_2| \cdot (|A_1| + |A_2|)} \times \mathbf{Bool}^{|Q_1| \cdot |Q_2|^2 \cdot (|A_1| + |A_2|)} \rightarrow \mathbf{Bool}^{|Q_1|^2 \cdot |Q_2| \cdot (|A_1| + |A_2|)} \times \mathbf{Bool}^{|Q_1| \cdot |Q_2|^2 \cdot (|A_1| + |A_2|)}$ be the functionals associated to B and B' in the context δ (for simplicity, we omit the subscript domains when their meaning is clear):

$$\begin{aligned} \Phi_\delta(\langle u_{pp'qa}, v_{ppq'a} \rangle) &= \langle \llbracket \bigvee_{q \xrightarrow{\tau^*} q'} \bigvee_{q' \xrightarrow{\alpha} q''} W_{pp'q'q''} \rrbracket (\delta \odot [u_{pp'qa}/U_{pp'qa}, v_{ppq'a}/V_{ppq'a}]), \\ &\quad \llbracket \bigvee_{p \xrightarrow{\tau^*} p'} \bigvee_{p' \xrightarrow{\alpha} p''} W_{p'p''qq'} \rrbracket (\delta \odot [u_{pp'qa}/U_{pp'qa}, v_{ppq'a}/V_{ppq'a}]) \rangle \\ &= \langle \bigvee_{q \xrightarrow{\tau^*} q'} \bigvee_{q' \xrightarrow{\alpha} q''} \delta(W_{pp'q'q''}), \bigvee_{p \xrightarrow{\tau^*} p'} \bigvee_{p' \xrightarrow{\alpha} p''} \delta(W_{p'p''qq'}) \rangle \\ \Phi'_\delta(\langle u_{pp'qa}, v_{ppq'a} \rangle) &= \langle \llbracket \bigvee_{q \xrightarrow{\alpha} q'} W_{pp'qq'} \vee \bigvee_{q \xrightarrow{\tau} q'} U_{pp'q'a} \rrbracket (\delta \odot [u_{pp'qa}/U_{pp'qa}, v_{ppq'a}/V_{ppq'a}]), \\ &\quad \llbracket \bigvee_{p \xrightarrow{\alpha} p'} W_{pp'qq'} \vee \bigvee_{p \xrightarrow{\tau} p'} V_{p'qq'a} \rrbracket (\delta \odot [u_{pp'qa}/U_{pp'qa}, v_{ppq'a}/V_{ppq'a}]) \rangle \\ &= \langle \bigvee_{q \xrightarrow{\alpha} q'} \delta(W_{pp'qq'}) \vee \bigvee_{q \xrightarrow{\tau} q'} u_{pp'q'a}, \bigvee_{p \xrightarrow{\alpha} p'} \delta(W_{pp'qq'}) \vee \bigvee_{p \xrightarrow{\tau} p'} v_{p'qq'a} \rangle \end{aligned}$$

To prove that $\nu\Phi_\delta = \nu\Phi'_\delta$, we show first that $\nu\Phi_\delta \sqsubseteq \nu\Phi'_\delta$ and then we show that the strict inclusion $\nu\Phi_\delta \sqsubset \nu\Phi'_\delta$ does not hold. Since the right-hand sides of the equations of B contain neither occurrences of $U_{pp'qa}$, nor of $V_{ppq'a}$, the associated functional Φ_δ is constant and its maximal fixed point is obtained simply by evaluating the functional on some arbitrary arguments:

$$\nu\Phi_\delta = \langle (\bigvee_{q \xrightarrow{\tau^*} q'} \bigvee_{q' \xrightarrow{\alpha} q''} \delta(W_{pp'q'q''}))_{pp'qa}, (\bigvee_{p \xrightarrow{\tau^*} p'} \bigvee_{p' \xrightarrow{\alpha} p''} \delta(W_{p'p''qq'}))_{ppq'a} \rangle$$

By applying Φ'_δ on this fixed point, we obtain:

$$\begin{aligned} \Phi'_\delta(\nu\Phi_\delta) &= \Phi'_\delta(\langle \bigvee_{q \xrightarrow{\tau^*} q'} \bigvee_{q' \xrightarrow{\alpha} q''} \delta(W_{pp'q'q''}), \bigvee_{p \xrightarrow{\tau^*} p'} \bigvee_{p' \xrightarrow{\alpha} p''} \delta(W_{p'p''qq'}) \rangle) \\ &\quad \text{by definition of } \nu\Phi_\delta \\ &= \langle \bigvee_{q \xrightarrow{\alpha} q'} \delta(W_{pp'qq'}) \vee \bigvee_{q \xrightarrow{\tau} q'} (\bigvee_{q \xrightarrow{\tau^*} q'} \bigvee_{q' \xrightarrow{\alpha} q''} \delta(W_{pp'q'q''}))_{pp'q'a}, \\ &\quad \bigvee_{p \xrightarrow{\alpha} p'} \delta(W_{pp'qq'}) \vee \bigvee_{p \xrightarrow{\tau} p'} (\bigvee_{p \xrightarrow{\tau^*} p'} \bigvee_{p' \xrightarrow{\alpha} p''} \delta(W_{p'p''qq'}))_{p'qq'a} \rangle \\ &\quad \text{by definition of } \Phi'_\delta \\ &= \langle \bigvee_{q \xrightarrow{\alpha} q'} \delta(W_{pp'qq'}) \vee \bigvee_{q \xrightarrow{\tau} q'} \bigvee_{q \xrightarrow{\tau^*} q'} \bigvee_{q' \xrightarrow{\alpha} q''} \delta(W_{pp'q'q''}) \\ &\quad \bigvee_{p \xrightarrow{\alpha} p'} \delta(W_{pp'qq'}) \vee \bigvee_{p \xrightarrow{\tau} p'} \bigvee_{p \xrightarrow{\tau^*} p'} \bigvee_{p' \xrightarrow{\alpha} p''} \delta(W_{p'p''qq'}) \rangle \\ &\quad \text{by subscript substitution} \\ &= \langle (\bigvee_{q \xrightarrow{\tau^*} q'} \bigvee_{q' \xrightarrow{\alpha} q''} \delta(W_{pp'q'q''}))_{pp'qa}, (\bigvee_{p \xrightarrow{\tau^*} p'} \bigvee_{p' \xrightarrow{\alpha} p''} \delta(W_{p'p''qq'}))_{ppq'a} \rangle \\ &\quad \text{by definition of } \tau\text{-closure} \\ &= \nu\Phi_\delta \quad \text{by definition of } \nu\Phi_\delta. \end{aligned}$$

From Tarski's theorem [42], this implies $\nu\Phi_\delta \sqsubseteq \nu\Phi'_\delta$. It remains to show that the strict inclusion $\nu\Phi_\delta \sqsubset \nu\Phi'_\delta$ does not hold. Suppose that $\nu\Phi_\delta \sqsubset \nu\Phi'_\delta$, meaning that:

$$\langle (\nu\Phi_\delta)_{pp'qa}, (\nu\Phi_\delta)_{pp'qa} \rangle \sqsubset \langle (\nu\Phi'_\delta)_{pp'qa}, (\nu\Phi'_\delta)_{pp'qa} \rangle$$

Two cases are possible, depending on whether the first or the second component of $\nu\Phi_\delta$ is smaller than the corresponding component of $\nu\Phi'_\delta$. We consider only the first case here, the other one being completely symmetric. Let $p, p' \in Q_1$, $q \in Q_2$, and $a \in A_1 \cup A_2$ such that $(\nu\Phi_\delta)_{pp'qa} = \text{false}$ and $(\nu\Phi'_\delta)_{pp'qa} = \text{true}$.

From the definition of Φ_δ , we infer that $\bigvee_{q \xrightarrow{\tau^*} q'} \bigvee_{q' \xrightarrow{a} q''} \delta(W_{pp'q'q''}) = \text{false}$, meaning that there is no τ -sequence going out of q and leading to a state q' that has an outgoing transition $q' \xrightarrow{a} q''$ such that $\delta(W_{pp'q'q''}) = \text{true}$.

From the definition of Φ'_δ and the fact that $\nu\Phi'_\delta$ is a fixed point, we infer that $\bigvee_{q \xrightarrow{a} q'} \delta(W_{pp'qq'}) \vee \bigvee_{q \xrightarrow{\tau} q'} (\nu\Phi'_\delta)_{pp'q'a} = \text{true}$. But the disjunct $\bigvee_{q \xrightarrow{a} q'} \delta(W_{pp'qq'})$ cannot be true because this would imply the existence of a zero-step τ -sequence going out of q and leading, after an a -transition, to a state q' such that $W_{pp'qq'} = \text{true}$, which is forbidden by the condition above. So the other disjunct must be true, meaning that there exists a transition $q \xrightarrow{\tau} q'$ such that $(\nu\Phi'_\delta)_{pp'q'a} = \text{true}$.

By repeating the above reasoning, we can construct an infinite sequence $(q =) q_0 \xrightarrow{\tau} q_1 \xrightarrow{\tau} q_2 \xrightarrow{\tau} \dots$ such that $(\nu\Phi'_\delta)_{pp'q_i a} = \text{true}$ for all $i \geq 0$. This contradicts the hypothesis of M_2 being τ -convergent, and therefore concludes the proof. \square

Proposition 2 (Correctness of branching bisimulation BES) *Let $M_1 = \langle Q_1, A_1, T_1, q_{01} \rangle$, $M_2 = \langle Q_2, A_2, T_2, q_{02} \rangle$ be two τ -convergent LTSSs, and let B_b be the BES encoding the branching bisimulation between M_1 and M_2 . Then:*

$$\Gamma(\llbracket B_b \rrbracket) = \approx_b .$$

Proof. We start with the direct BES encoding of branching bisimulation (previously shown in Table 1, middle part, first row) and we progressively refine it until obtaining the full BES encoding given by Definition 4 (previously shown in Table 1, middle part, second row).

The direct encoding of branching bisimulation is given by the BES B_{b1} defined below:

$$B_{b1} = \left\{ \begin{array}{l} X_{pq} \stackrel{\nu}{=} \bigwedge_{p \xrightarrow{a} p'} ((a = \tau \wedge X_{p'q}) \vee \bigvee_{q \xrightarrow{\tau^*} q' \xrightarrow{a} q''} (X_{pq'} \wedge X_{p'q''})) \wedge \\ \bigwedge_{q \xrightarrow{a} q'} ((a = \tau \wedge X_{pq'}) \vee \bigvee_{p \xrightarrow{\tau^*} p' \xrightarrow{a} p''} (X_{p'q} \wedge X_{p''q'})) \end{array} \right\}_{p \in Q_1, q \in Q_2}$$

This encoding is correct, i.e., $\llbracket B_{b1} \rrbracket = \approx_b$. The proof of this fact is similar to that of Proposition 1 and is omitted here for the sake of conciseness. Note that this proof is valid for arbitrary LTSSs, and therefore also for τ -convergent LTSSs.

We now refine the BES B_{b1} into a BES B_{b2} by replacing certain subformulas with new variables defined by additional equations, such that the right-hand side of each equation of

B_{b_2} contains a single type of boolean operator:

$$B_{b_2} = \left\{ \begin{array}{l} X_{pq} \stackrel{\nu}{=} \bigwedge_{p \rightarrow p'}^a Y_{pp'qa} \wedge \bigwedge_{q \rightarrow q'}^a Z_{pqq'a} \\ Y_{pp'qa} \stackrel{\nu}{=} (a = \tau \wedge X_{p'q}) \vee U_{pp'qa} \\ U_{pp'qa} \stackrel{\nu}{=} \bigvee_{q \rightarrow q'}^{\tau^*} \bigvee_{q' \rightarrow q''}^a W_{pp'q'q''} \\ Z_{pqq'a} \stackrel{\nu}{=} (a = \tau \wedge X_{pq'}) \vee V_{pqq'a} \\ V_{pqq'a} \stackrel{\nu}{=} \bigvee_{p \rightarrow p'}^{\tau^*} \bigvee_{p' \rightarrow p''}^a W_{p'p''qq'} \\ W_{pp'qq'} \stackrel{\nu}{=} X_{pq} \wedge X_{p'q'} \end{array} \right\}_{p,p' \in Q_1, q, q' \in Q_2, a \in A_1 \cup A_2}$$

This transformation into a simple BES B_{b_2} as we consider in our framework (see Section 2) does not change the interpretation of the variables $X_{p,q}$ of the original BES, i.e., $(\llbracket B_{b_1} \rrbracket)_{X_{p,q}} = (\llbracket B_{b_2} \rrbracket)_{X_{p,q}}$ for all $p \in Q_1$ and $q \in Q_2$.

The final step towards the BES B_b given in Definition 4 is to get rid of the τ -closures present in the right-hand sides of the equations defining $U_{pp'qa}$ and $V_{pqq'a}$. To achieve this, we consider the following BES:

$$B = \left\{ \begin{array}{l} U_{pp'qa} \stackrel{\nu}{=} \bigvee_{q \rightarrow q'}^{\tau^*} \bigvee_{q' \rightarrow q''}^a W_{pp'q'q''} \\ V_{pqq'a} \stackrel{\nu}{=} \bigvee_{p \rightarrow p'}^{\tau^*} \bigvee_{p' \rightarrow p''}^a W_{p'p''qq'} \end{array} \right\}_{p,p' \in Q_1, q, q' \in Q_2, a \in A_1 \cup A_2}$$

Since both LTSS M_1 and M_2 are τ -convergent, Lemma 5 ensures that $\llbracket B \rrbracket \delta = \llbracket B' \rrbracket \delta$, where B' is defined as follows:

$$B' = \left\{ \begin{array}{l} U_{pp'qa} \stackrel{\nu}{=} \bigvee_{q \rightarrow q'}^a W_{pp'qq'} \vee \bigvee_{q \rightarrow q'}^{\tau} U_{pp'q'a} \\ V_{pqq'a} \stackrel{\nu}{=} \bigvee_{p \rightarrow p'}^a W_{pp'qq'} \vee \bigvee_{p \rightarrow p'}^{\tau} V_{p'qq'a} \end{array} \right\}_{p,p' \in Q_1, q, q' \in Q_2, a \in A_1 \cup A_2}$$

Starting with the BESS B and B' , which have the same interpretation in any context δ , we can apply Lemma 4 repeatedly in order to add all the equations of B_{b_2} defining variables X_{pq} , $Y_{pp'qa}$, $Z_{pqq'a}$, and $W_{pp'qq'}$ for all $p, p' \in Q_1$, $q, q' \in Q_2$, and $a \in A_1 \cup A_2$, still ensuring that the resulting BESS have the same interpretation. Upon completion of this process, the BES derived from B is B_{b_2} and the BES derived from B' is B_b (note that we can permute freely the equations of a BES without changing the interpretation of its variables):

$$B_b = \left\{ \begin{array}{l} X_{pq} \stackrel{\nu}{=} \bigwedge_{p \rightarrow p'}^a Y_{pp'qa} \wedge \bigwedge_{q \rightarrow q'}^a Z_{pqq'a} \\ Y_{pp'qa} \stackrel{\nu}{=} (a = \tau \wedge X_{p'q}) \vee U_{pp'qa} \\ U_{pp'qa} \stackrel{\nu}{=} \bigvee_{q \rightarrow q'}^a W_{pp'qq'} \vee \bigvee_{q \rightarrow q'}^{\tau} U_{pp'q'a} \\ Z_{pqq'a} \stackrel{\nu}{=} (a = \tau \wedge X_{pq'}) \vee V_{pqq'a} \\ V_{pqq'a} \stackrel{\nu}{=} \bigvee_{p \rightarrow p'}^a W_{pp'qq'} \vee \bigvee_{p \rightarrow p'}^{\tau} V_{p'qq'a} \\ W_{pp'qq'} \stackrel{\nu}{=} X_{pq} \wedge X_{p'q'} \end{array} \right\}_{p,p' \in Q_1, q, q' \in Q_2, a \in A_1 \cup A_2}$$

By virtue of Lemma 4 these BESS have the same interpretation (the context δ becomes useless since both BESS are closed), meaning that B_b has in turn the same interpretation as B_{b_1} , and thus it reflects the branching bisimulation of the τ -convergent LTSS M_1 and M_2 correctly. \square



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