

Six Blind Men and the Elephant: Benchmarking and Simulation in the Exascale Era

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Paolo Faraboschi (HP Labs)

As we leave the Petascale milestone behind us, the computing industry is changing rapidly to address the next challenges. Energy, dependability, cost pressure and economy of scale are pushing IT consolidation into large "cloud" datacenters where new workloads and legacy applications coexist. Data is growing at a higher exponential rate than computing, and novel data-centric architectures are starting to emerge. Heterogeneity and specialization, within and across instruction sets, are reemerging to address energy efficiency.

In light of these secular shifts in the IT industry, the benchmarking and simulation techniques are lagging behind and need to deeply transform to address the upcoming challenges. Like the ancient Hindu parable, current practices in the architecture community suffer from an excessive focus on narrow metrics, none of which is either completely correct or totally wrong, but often risk missing the big picture.

This talk will discuss how computer architecture simulation and benchmarking must evolve to provide better quality decision support data for datacenter-level computing. Speed, full-system, validation and modularity are some of the fundamental characteristics of a scalable simulator. Dynamically trading off speed and accuracy, running unmodified software, and the flexibility to interface with multiple tools are other key aspects that should drive the development of the next generation simulators. As a case study, the talk will cover some of the design considerations behind COTSon, an open-source scalable full-system simulation infrastructure targeting fast and accurate evaluation of current and future computing systems.

Paolo Faraboschi is a Distinguished Technologist in the Exascale Computing Lab of HP Labs, working on next-generation data center research. He has been at HP since 1994 and recently led the COTSon full-system simulation infrastructure (<http://cotson.sourceforge.net/>). In the past, he was the principal architect of the Lx/ST200 family of embedded VLIW processor cores (http://en.wikipedia.org/wiki/ST200_family). Paolo is a co-author in over 30 papers, 16 patents, and the book "Embedded Computing, a VLIW approach to architecture compilers and tools". He is an active member of the architecture community, was recently program co-chair of HiPEAC'10 and MICRO'41, and is an associate editor for TACO. Paolo received his PhD in EECS from the University of Genoa (Italy) in 1993. For more information see http://www.hpl.hp.com/people/paolo_faraboschi