

Off-line Test Selection with Test Purposes for Non-Deterministic Timed Automata

Nathalie Bertrand, Thierry Jéron, Amélie Stainer, Moez Krichen

► **To cite this version:**

Nathalie Bertrand, Thierry Jéron, Amélie Stainer, Moez Krichen. Off-line Test Selection with Test Purposes for Non-Deterministic Timed Automata. [Research Report] RR-7501, INRIA. 2011, pp.20. <inria-00550923>

HAL Id: inria-00550923

<https://hal.inria.fr/inria-00550923>

Submitted on 31 Dec 2010

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.



INSTITUT NATIONAL DE RECHERCHE EN INFORMATIQUE ET EN AUTOMATIQUE

*Off-line Test Selection with Test Purposes for
Non-Deterministic Timed Automata*

Nathalie Bertrand — Thierry Jéron — Amélie Stainer — Moez Krichen

N° 7501

January 2011

— Embedded and Real Time Systems —

*R*apport
de recherche

Off-line Test Selection with Test Purposes for Non-Deterministic Timed Automata

Nathalie Bertrand , Thierry Jéron , Amélie Stainer , Moez Krichen

Theme : Embedded and Real Time Systems
Algorithmics, Programming, Software and Architecture
Équipe-Projet Vertecs

Rapport de recherche n° 7501 — January 2011 — 17 pages

Abstract: This report proposes novel off-line test generation techniques for non-deterministic timed automata with inputs and outputs (TAIOs) in the formal framework of the **tioco** conformance theory. In this context, a first problem is the determinization of TAIOs, which is necessary to foresee next enabled actions, but is in general impossible. This problem is solved here thanks to an approximate determinization using a game approach, which preserves **tioco** and guarantees the soundness of generated test cases. A second problem is test selection for which a precise description of timed behaviors to be tested is carried out by expressive test purposes modeled by a generalization of TAIOs. Finally, using a symbolic co-reachability analysis guided by the test purpose, test cases are generated in the form of TAIOs equipped with verdicts.

Key-words: timed automata, determinization, test purpose, selection

A short version of this report appears in TACAS'2011 [BJSK11]. This work was partially funded by the french ANR Testec.

Sélection de tests hors-ligne avec objectifs de tests pour des automates temporisés non-déterministes

Résumé : Ce rapport propose de nouvelles techniques de génération de tests pour des automates temporisés avec entrées et sorties (TAIOs for *timed automata with inputs and outputs*) dans le cadre formel de la théorie du test de conformité **tioco**. Dans ce contexte, un premier problème consiste en la détermination des TAIOs, nécessaire pour prévoir les prochaines actions tirables, mais en général impossible. Ce problème est résolu grâce à une détermination approchée fondée sur la théorie des jeux, préservant **tioco** et garantissant la correction des cas de tests générés. Le second problème est celui de la sélection de tests pour lequel une description précise des comportements temporisés à tester est fournie par des objectifs de tests expressifs modélisés par une généralisation des TAIOs. Enfin, grâce à une analyse symbolique de co-accessibilité guidée par les objectifs de tests, les cas de tests sont générés sous la forme de TAIOs équipés de verdicts.

Mots-clés : automates temporisés, détermination, objectif de test, sélection

1 Introduction

Conformance testing is the process of testing whether an implementation behaves correctly with respect to a specification. Implementations are considered as *black boxes*, *i.e.* the source code is unknown, only their interface with the environment is known and used to interact with the tester. In *formal model-based conformance testing* models are used to describe testing artifacts (specifications, implementations, test cases, ...), conformance is formally defined and test cases with verdicts are generated automatically. Then, the quality of testing may be characterized by properties of test cases which relate the verdicts of their executions with conformance (*e.g.* soundness). For timed models, model-based conformance testing has already been explored in the last decade, with different models and conformance relations (see *e.g.* [ST08] for a survey), and test generation algorithms (*e.g.* [BB05, KT09, NS03]). In this context, a very popular model is *timed automata with inputs and outputs* (TAIOs), a variant of *timed automata* (TAs) [AD94], in which observable actions are partitioned into inputs and outputs. We consider here partially observable and non-deterministic TAIOs with invariants for the modeling of urgency.

One of the main difficulties encountered in test generation for those partially observable, non-deterministic TAIOs is determinization, which is impossible in general as for TAs [AD94], but is required in order to foresee the next enabled actions during execution and to emit a correct verdict. Two different approaches have been taken for test generation from timed models in the literature, which induce different treatments of non-determinism. In *off-line test generation* test cases are first generated as TAs (or timed sequences, trees, or timed transition systems) and subsequently executed on the implementation. One advantage is that test cases can be stored and further used *e.g.* for regression testing and documentation. However, due to the non-determinizability of TAIOs, the approach has often been limited to deterministic or determinizable TAIOs (see *e.g.* [KJM04, NS03]), except in [KT09] where the problem is solved by the use of an over-approximate determinization with fixed resources, or [DLLN09] where winning strategies of timed games are used as test cases. In *on-line test generation*, test cases are generated during their execution. This can be applied to any TAIO as only possible observable actions are computed along the current finite execution, thus avoiding a complete determinization. It is of particular interest to rapidly discover errors, but may sometimes be impracticable due to a lack of reactivity (the time needed to compute successor states on-line may sometimes be incompatible with delays).

In this paper, we propose to generate test cases off-line for general non-deterministic TAIOs, in the formal context of the **tioco** conformance theory. The determinization problem is tackled thanks to an approximate determinization with fixed resources in the spirit of [KT09], using a game approach [BSJK11]. Determinization is exact for all known classes of determinizable TAIOs (*e.g.* event-clock TAs, TAs with integer resets, strongly non-Zeno TAs) if resources are sufficient. In the general case, approximate determinization guarantees soundness of generated test cases by producing a deterministic *io-abstraction* of the TAIO for a particular *io-refinement* relation, generalizing the *io-refinement* for deterministic TAIOs of [DLL⁺10]. Our method is more precise than [KT09] (see [BSJK11] for details) and preserves the richness of our model by dealing with partial observability and urgency. Behaviors of specifications to be tested are identified by means of test purposes. These are defined as *open timed automata with inputs and outputs* (OTAIOs), a model generalizing TAIOs, allowing to precisely describe behaviors according to actions and clocks of the specification as well as proper clocks. Then, in the same spirit as for the TGV tool in the untimed case [JJ04], test selection is performed by a co-reachability analysis, and produces a test case in the form of a TAIO. To our knowledge, this work constitutes the most general and advanced off-line test selection approach for TAIOs.

The paper is structured as follows. In the next section we introduce the model of OTAIOs, its semantics, some notations and operations. Section 3 recalls the **tioco** conformance theory including expected properties relating conformance and verdicts, and an *io-refinement* relation preserving **tioco**. Section 4 presents our game approach for the approximate determinization compatible with the *io-refinement*. In Section 5 we detail the test selection mechanism using test purposes and prove some properties on generated test cases.

2 A model of open timed automata with inputs/outputs

Timed automata (TAs) [AD94] is a usual model for time constrained systems. In the context of model-based testing, TAs have been extended to timed automata with inputs and outputs (TAIOs) whose sets of

actions are partitioned into inputs, outputs and unobservable actions. In this section, we further extend TAIOS into the model of *open timed automata with inputs/outputs* (OTAIOS for short), by partitioning the set of clocks into proper and observed clocks. While the submodel of TAIOS (with only proper clocks) is sufficient for most testing artifacts, observed clocks of OTAIOS will be particularly useful to express test purposes whose aim is to focus on the timed behavior of the specification. Like in [AD94] for TAs, we consider OTAIOS and TAIOS with location invariants for the modeling of urgency.

2.1 Open timed automata with inputs/outputs

We start by introducing notations and useful definitions concerning TAs, TAIOS and OTAIOS.

Given X a finite set of *clocks*, and $\mathbb{R}_{\geq 0}$ the set of non-negative real numbers, a *clock valuation* is a mapping $v : X \rightarrow \mathbb{R}_{\geq 0}$. If v is a valuation over X and $t \in \mathbb{R}$, then $v + t$ denotes the valuation which assigns to every clock $x \in X$ the value $v(x) + t$. For $X' \subseteq X$ we write $v_{[X', \leftarrow 0]}$ for the valuation equal to v on $X \setminus X'$ and assigning 0 to all clocks of X' .

Given M a non-negative integer, an M -*bounded guard* (or simply *guard*) over X is a finite conjunction of constraints of the form $x \sim c$ where $x \in X$, $c \in [0, M] \cap \mathbb{N}$ and $\sim \in \{<, \leq, =, \geq, >\}$. Given g a guard and v a valuation, we write $v \models g$ if v satisfies g . We abuse notations and write g for the set of valuations satisfying g . *Invariants* are restricted cases of guards: given $M \in \mathbb{N}$, an M -bounded invariant over X is a finite conjunction of constraints of the form $x \triangleleft c$ where $x \in X$, $c \in [0, M] \cap \mathbb{N}$ and $\triangleleft \in \{<, \leq\}$. We denote by $G_M(X)$ (resp. $I_M(X)$) the set of M -bounded guards (resp. invariants) over X .

Definition 1 (OTAIO) An open timed automaton with inputs and outputs (OTAIO) is a tuple $\mathcal{A} = (L^{\mathcal{A}}, \ell_0^{\mathcal{A}}, \Sigma_{\tau}^{\mathcal{A}}, \Sigma_{\uparrow}^{\mathcal{A}}, \Sigma_{\downarrow}^{\mathcal{A}}, X_p^{\mathcal{A}}, X_o^{\mathcal{A}}, M^{\mathcal{A}}, I^{\mathcal{A}}, E^{\mathcal{A}})$ such that:

- $L^{\mathcal{A}}$ is a finite set of locations, with $\ell_0^{\mathcal{A}} \in L^{\mathcal{A}}$ the initial location,
- $\Sigma_{\tau}^{\mathcal{A}}$, $\Sigma_{\uparrow}^{\mathcal{A}}$ and $\Sigma_{\downarrow}^{\mathcal{A}}$ are disjoint finite alphabets of input actions (noted $a?, b?, \dots$), output actions (noted $a!, b!, \dots$), and internal actions (noted τ_1, τ_2, \dots). We note $\Sigma_{obs}^{\mathcal{A}} = \Sigma_{\tau}^{\mathcal{A}} \sqcup \Sigma_{\uparrow}^{\mathcal{A}}$ (where \sqcup denotes the disjoint union) for the alphabet of observable actions, and $\Sigma^{\mathcal{A}} = \Sigma_{\tau}^{\mathcal{A}} \sqcup \Sigma_{\uparrow}^{\mathcal{A}} \sqcup \Sigma_{\downarrow}^{\mathcal{A}}$ for the whole set of actions.
- $X_p^{\mathcal{A}}$ and $X_o^{\mathcal{A}}$ are disjoint finite sets of proper clocks and observed clocks, respectively. We note $X^{\mathcal{A}} = X_p^{\mathcal{A}} \sqcup X_o^{\mathcal{A}}$ for the whole set of clocks.
- $M^{\mathcal{A}} \in \mathbb{N}$ is the maximal constant of \mathcal{A} , and we will refer to $(|X^{\mathcal{A}}|, M^{\mathcal{A}})$ as the resources of \mathcal{A} ,
- $I^{\mathcal{A}} : L^{\mathcal{A}} \rightarrow I_{M^{\mathcal{A}}}(X^{\mathcal{A}})$ is a mapping labeling each location with an invariant,
- $E^{\mathcal{A}} \subseteq L^{\mathcal{A}} \times G_{M^{\mathcal{A}}}(X^{\mathcal{A}}) \times \Sigma^{\mathcal{A}} \times 2^{X_p^{\mathcal{A}}} \times L^{\mathcal{A}}$ is a finite set of edges where guards are defined on $X^{\mathcal{A}}$, but resets are restricted to proper clocks in $X_p^{\mathcal{A}}$.

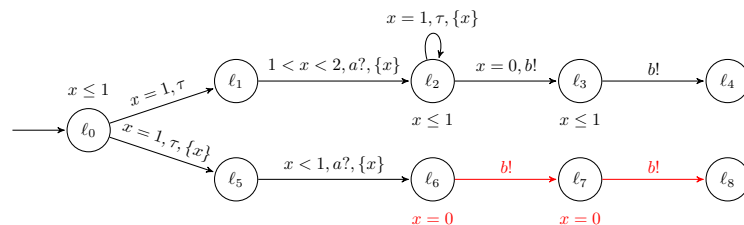


Figure 1: Specification \mathcal{A}

The reason for introducing the OTAIO model is to have a unique model (syntax and semantics) that will be next specialized for particular testing artifacts. In particular, an OTAIO with an empty set of observed clocks $X_o^{\mathcal{A}}$ is a classical TAIIO, and will be the model for specifications, implementations and test cases. For example, Fig. 1 represents such a TAIIO for a specification \mathcal{A} with clock x , input a , output b and internal action τ . The partition of actions reflects their roles in the testing context: the environment

cannot observe internal actions, but controls inputs and observes outputs (and delays). The set of clocks is also partitioned into *proper clocks*, i.e. usual clocks controlled by \mathcal{A} , and *observed clocks* referring to proper clocks of another OTAIO. These cannot be reset to avoid intrusiveness, but synchronization with them in guards and invariants is allowed. In particular, test purposes have observed clocks which observe proper clocks of specifications in order to describe time constrained behaviors to be tested.

2.2 The semantics of OTAIOs

The semantics of an OTAIO $\mathcal{A} = (L^A, \ell_0^A, \Sigma_\tau^A, \Sigma_i^A, \Sigma_r^A, X_p^A, X_o^A, M^A, I^A, E^A)$ is a timed transition system $\mathcal{T}^A = (S^A, s_0^A, \Gamma^A, \rightarrow_{\mathcal{A}})$ where $S^A = L^A \times \mathbb{R}_{\geq 0}^{X^A}$ is the set of *states* i.e. pairs (ℓ, v) consisting in a location and a valuation of clocks; $s_0^A = (\ell_0^A, \bar{0}) \in S^A$ is the *initial state*; $\Gamma^A = \mathbb{R}_{\geq 0} \sqcup E^A \times 2^{X_o^A}$ is the set of transition *labels* consisting in either a delay δ or a pair (e, X_o') formed by an edge and a set of observed clocks; the transition relation $\rightarrow_{\mathcal{A}} \subseteq S^A \times \Gamma^A \times S^A$ is the smallest set of the following moves:

- *Discrete moves*: $(\ell, v) \xrightarrow{(e, X_o')}_{\mathcal{A}} (\ell', v')$ whenever there exists $e = (\ell, g, a, X_p', \ell') \in E^A$ such that $v \models g \wedge I^A(\ell)$, $X_o' \subseteq X_o^A$ is an arbitrary subset of observed clocks, $v' = v|_{X_p' \sqcup X_o' \leftarrow 0}$ and $v' \models I^A(\ell')$. Note that X_o' is unconstrained as observed clocks are controlled by another OTAIO.
- *Time elapse*: $(\ell, v) \xrightarrow{\delta}_{\mathcal{A}} (\ell, v + \delta)$ for $\delta \in \mathbb{R}_{\geq 0}$ if $v + \delta \models I^A(\ell)$.

A *partial run* of \mathcal{A} is a finite sequence of subsequent moves in $(S^A \times \Gamma^A)^* \cdot S^A$. For example $\rho = s_0 \xrightarrow{\delta_1}_{\mathcal{A}} s'_0 \xrightarrow{(e_1, X_o^1)}_{\mathcal{A}} s_1 \cdots s_{k-1} \xrightarrow{\delta_k}_{\mathcal{A}} s'_{k-1} \xrightarrow{(e_k, X_o^k)}_{\mathcal{A}} s_k$. The sum of delays in ρ is noted $time(\rho)$. A *run* is a partial run starting in s_0^A . $Run(\mathcal{A})$ and $pRun(\mathcal{A})$ will denote respectively runs and partial runs of \mathcal{A} . A state s is *reachable* if there exists a run leading to s . A state s is *co-reachable* from a set $S' \subseteq S^A$ if there is a partial run from s to a state in S' . We note $reach(\mathcal{A})$ the set of reachable states and $coreach(\mathcal{A}, S')$ the set of states co-reachable from S' .

A (partial) *sequence* is a projection of a (partial) run where states are forgotten, and discrete transitions are abstracted to actions and proper resets which are grouped with observed resets. The sequence corresponding to a run $\rho = s_0 \xrightarrow{\delta_1}_{\mathcal{A}} s'_0 \xrightarrow{(e_1, X_o^1)}_{\mathcal{A}} s_1 \cdots s_{k-1} \xrightarrow{\delta_k}_{\mathcal{A}} s'_{k-1} \xrightarrow{(e_k, X_o^k)}_{\mathcal{A}} s_k$ is $\mu = \delta_1.(a_1, X_p^1 \sqcup X_o^1) \cdots \delta_k.(a_k, X_p^k \sqcup X_o^k)$ where $\forall i \in [1, k], e_i = (\ell_i, g_i, a_i, X_p^i, \ell'_i)$. We then note $s_0^A \xrightarrow{\mu}_{\mathcal{A}} s_k$. We write $s_0^A \xrightarrow{\mu}_{\mathcal{A}}$ for $\exists s_k, s_0^A \xrightarrow{\mu}_{\mathcal{A}} s_k$. We note $Seq(\mathcal{A}) \subseteq (\mathbb{R}_{\geq 0} \sqcup (\Sigma^A \times 2^{X^A}))^*$ (respectively $pSeq(\mathcal{A})$) the set of sequences (resp. partial sequences) of \mathcal{A} . For a sequence μ , we note $time(\mu)$ the sum of delays in μ .

For a (partial) sequence $\mu \in pSeq(\mathcal{A})$, $Trace(\mu)$ denotes the observable timed word in $(\mathbb{R}_{\geq 0} \sqcup \Sigma_{obs}^A)^* \cdot \mathbb{R}_{\geq 0}$ obtained by erasing from μ all internal actions, and summing delays between observable actions (if any). It is defined inductively as follows: $Trace(\varepsilon) = 0$, $Trace((\tau, X) \cdot \mu) = Trace(\mu)$, $Trace(\delta_1 \dots \delta_k) = \sum_{i=1}^k \delta_i$, and $Trace(\delta_1 \dots \delta_k.(a, X') \cdot \mu) = (\sum_{i=1}^k \delta_i).a.Trace(\mu)$ if $a \in \Sigma_{obs}^A$. For example $Trace(1.(\tau, X^1).2.(a, X^2).2.(\tau, X^3)) = (3, a).2$ and $Trace(1.(\tau, X^1).2.(a, X^2)) = (3, a).0$. For a run ρ projecting onto a sequence μ , we write $Trace(\rho)$ for $Trace(\mu)$. The set of traces of runs of \mathcal{A} is denoted by $Traces(\mathcal{A}) \subseteq (\mathbb{R}_{\geq 0} \sqcup \Sigma_{obs}^A)^* \cdot \mathbb{R}_{\geq 0}$. Two OTAIOs are said *equivalent* if they have the same sets of traces.

Let $\sigma \in (\mathbb{R}_{\geq 0} \sqcup \Sigma_{obs}^A)^* \cdot \mathbb{R}_{\geq 0}$ be an observable timed word, and $s \in S^A$ a state, \mathcal{A} **after** $\sigma = \{s \in S^A \mid \exists \mu \in Seq(\mathcal{A}), s_0^A \xrightarrow{\mu}_{\mathcal{A}} s \wedge trace(\mu) = \sigma\}$ denotes the set of states where \mathcal{A} can stay after observing the trace σ . We note $elapse(s) = \{t \in \mathbb{R}_{\geq 0} \mid s \xrightarrow{t}_{\mathcal{A}}\}$ the set of possible delays in s , and $out(s) = \{a \in \Sigma_i^A \mid \exists X \subseteq X^A, s \xrightarrow{(a, X)}_{\mathcal{A}}\} \sqcup \{a \in \Sigma_r^A \mid s \xrightarrow{(a, X)}_{\mathcal{A}}\}$ (and $in(s) = \{a \in \Sigma_\tau^A \mid s \xrightarrow{(a, X)}_{\mathcal{A}}\}$) for the set of outputs and delays (respectively inputs) that can be observed from s . For $S' \subseteq S^A$, $out(S') = \bigcup_{s \in S'} out(s)$ and $in(S') = \bigcup_{s \in S'} in(s)$.

2.3 Properties and operations

An TAIIO \mathcal{A} is *deterministic* (and called a DTAIO) whenever for any $\sigma \in Traces(\mathcal{A})$, s_0^A **after** σ is a singleton¹. A TAIIO \mathcal{A} is *determinizable* if there exists an equivalent DTAIO. It is well-known that some

¹The notion of determinism is needed here and defined only for TAIIOs. For OTAIOs the right definition would consider the projection of s_0^A **after** σ which forgets values of observed clocks, as these introduce “environmental” non-determinism.

timed automata are not determinizable [AD94]; moreover, the determinizability of timed automata is an undecidable problem, even with fixed resources [Tri06, Fin06].

An OTAIO \mathcal{A} is *complete* if in every location ℓ , $I^{\mathcal{A}}(\ell) = \mathbf{true}$ and for every action $a \in \Sigma^{\mathcal{A}}$, the disjunction of all guards of transitions leaving ℓ and labeled by a is \mathbf{true} . This implies that $\text{Traces}(\mathcal{A})$ is the universal language on $\Sigma^{\mathcal{A}}$. An OTAIO \mathcal{A} is *input-complete* in a state $s \in \text{reach}(\mathcal{A})$, if for all $a \in \Sigma^{\mathcal{A}}$, $s \xrightarrow{a}$. An OTAIO \mathcal{A} is *non-blocking* if $\forall s \in \text{reach}(\mathcal{A}), \forall t \in \mathbb{R}_{\geq 0}, \exists \mu \in \text{pSeq}(\mathcal{A}) \cap (\mathbb{R}_{\geq 0} \sqcup (\Sigma_1^{\mathcal{A}} \sqcup \Sigma_{\tau}^{\mathcal{A}}) \times 2^{X^{\mathcal{A}}})^*, \text{time}(\mu) = t \wedge s \xrightarrow{\mu}$.

We now define a product operation on OTAIOs which extends the classical product of TAs, with a particular attention to observed clocks:

Definition 2 (Product) *The product of two OTAIOs $\mathcal{A}^i = (L^i, \ell_0^i, \Sigma_?, \Sigma_1, \Sigma_{\tau}, X_p^i, X_o^i, M^i, I^i, E^i)$, $i = 1, 2$, with same alphabets and disjoint sets of proper clocks ($X_p^1 \cap X_p^2 = \emptyset$) is the OTAIO $\mathcal{A}^1 \times \mathcal{A}^2 = (L, \ell_0, \Sigma_?, \Sigma_1, \Sigma_{\tau}, X_p, X_o, M, I, E)$ where: $L = L^1 \times L^2$; $\ell_0 = (\ell_0^1, \ell_0^2)$; $X_p = X_p^1 \sqcup X_p^2$, $X_o = (X_o^1 \cup X_o^2) \setminus X_p$; $M = \max(M^1, M^2)$; $\forall (\ell^1, \ell^2) \in L, I((\ell^1, \ell^2)) = I^1(\ell^1) \wedge I^2(\ell^2)$; and $((\ell^1, \ell^2), g^1 \wedge g^2, a, X_p^1 \sqcup X_p^2, (\ell^1, \ell^2)) \in E$ if $(\ell^i, g^i, a, X_p^i, \ell^i) \in E^i$, $i=1,2$.*

Intuitively, \mathcal{A}^1 and \mathcal{A}^2 synchronize on both time and common actions (including internal ones). \mathcal{A}^2 may observe proper clocks of \mathcal{A}^1 with its observed clocks $X_o^1 \cap X_p^2$, and vice versa. The set of proper clocks of $\mathcal{A}^1 \times \mathcal{A}^2$ is the union of proper clocks of \mathcal{A}^1 and \mathcal{A}^2 , and observed clocks are those observed clocks of one OTAIO that are not proper. For example, the OTAIO in Fig. 3 represents the product of the TAIIO \mathcal{A} in Fig. 1 and the OTAIO \mathcal{TP} of Fig. 2.

The product is the right operation for intersecting sets of sequences. In fact, let $\mathcal{A}^1 \uparrow^{(X_p^2, X_o^2)}$ (respectively $\mathcal{A}^2 \uparrow^{(X_p^1, X_o^1)}$) denote the same TAIIO \mathcal{A}^1 (resp. \mathcal{A}^2) defined on $(X_p^1, X_p^2 \cup X_o^2 \cup X_o^1 \setminus X_p^1)$ (resp. on $(X_p^2, X_p^1 \cup X_o^1 \cup X_o^2 \setminus X_p^2)$). Then we get:

$$\text{Seq}(\mathcal{A}^1 \times \mathcal{A}^2) = \text{Seq}(\mathcal{A}^1 \uparrow^{(X_p^2, X_o^2)}) \cap \text{Seq}(\mathcal{A}^2 \uparrow^{(X_p^1, X_o^1)}).$$

An OTAIO equipped with a set of states $F \subseteq S^{\mathcal{A}}$ can play the role of an acceptor. $\text{Run}_F(\mathcal{A})$ denotes the set of runs *accepted* in F , those runs ending in F , $\text{Seq}_F(\mathcal{A})$ denotes the set of sequences of accepted runs and $\text{Traces}_F(\mathcal{A})$ the set of their traces. By abuse of notation, if L is a subset of locations $L^{\mathcal{A}}$, we write $\text{Run}_L(\mathcal{A})$ for $\text{Run}_{L \times \mathbb{R}_{\geq 0}^{X^{\mathcal{A}}}}(\mathcal{A})$ and similarly for $\text{Seq}_L(\mathcal{A})$ and $\text{Traces}_L(\mathcal{A})$. Note that for the product $\mathcal{A}^1 \times \mathcal{A}^2$, if F^1 and F^2 are subsets of states of \mathcal{A}^1 and \mathcal{A}^2 respectively, we get the equality:

$$\text{Seq}_{F^1 \times F^2}(\mathcal{A}^1 \times \mathcal{A}^2) = \text{Seq}_{F^1}(\mathcal{A}^1 \uparrow^{X_p^2, X_o^2}) \cap \text{Seq}_{F^2}(\mathcal{A}^2 \uparrow^{X_p^1, X_o^1}).$$

3 Conformance testing theory

In this section, we recall the conformance relation **tioco** [KT09], that formally defines the set of correct implementations of a given TAIIO specification. **tioco** extends naturally the **ioco** relation of Tretmans [Tre96] to timed systems. We then define test cases, formalize their executions, verdicts and expected properties. Finally, we introduce a refinement relation between TAIIOs that preserves **tioco**.

3.1 The tioco conformance theory

We consider that the specification is given as a (possibly non-deterministic) TAIIO $\mathcal{A} = (L^{\mathcal{A}}, \ell_0^{\mathcal{A}}, \Sigma_?, \Sigma_1, \Sigma_{\tau}, X_p^{\mathcal{A}}, \emptyset, M^{\mathcal{A}}, I^{\mathcal{A}}, E^{\mathcal{A}})$. The implementation is a black box, unknown except for its alphabet of observable actions, which is the same as the one of \mathcal{A} . As usual, in order to formally reason about conformance, we assume that the implementation can be modeled by an (unknown) TAIIO $\mathcal{I} = (L^{\mathcal{I}}, \ell_0^{\mathcal{I}}, \Sigma_?, \Sigma_1, \Sigma_{\tau}^{\mathcal{I}}, X_p^{\mathcal{I}}, \emptyset, M^{\mathcal{I}}, I^{\mathcal{I}}, E^{\mathcal{I}})$ with same observable alphabet as \mathcal{A} , and require that it is input-complete and non-blocking. The set of such possible implementations of \mathcal{A} is denoted by $\mathcal{I}(\mathcal{A})$. Among these, the conformance relation **tioco** [KT09] formally defines which ones conform to \mathcal{A} :

Definition 3 (Conformance relation) *Let \mathcal{A} be a TAIIO and $\mathcal{I} \in \mathcal{I}(\mathcal{A})$, \mathcal{I} **tioco** \mathcal{A} if $\forall \sigma \in \text{Traces}(\mathcal{A}), \text{out}(\mathcal{I} \text{ after } \sigma) \subseteq \text{out}(\mathcal{A} \text{ after } \sigma)$.*

Intuitively, \mathcal{I} conforms to \mathcal{A} ($\mathcal{I} \text{ tioco } \mathcal{A}$) if after any timed trace enabled in \mathcal{A} , every output or delay of \mathcal{I} is specified in \mathcal{A} . In practice, conformance is checked by test cases run on implementations. In our setting, we define test cases as deterministic TAIOS equipped with verdicts defined by a partition of states.

Definition 4 (Test case, test suite) *Given a specification TAIOS \mathcal{A} , a test case for \mathcal{A} is a pair $(\mathcal{TC}, \text{Verdicts})$ consisting of a deterministic TAIOS (DTAIOS) $\mathcal{TC} = (L^{\mathcal{TC}}, \ell_0^{\mathcal{TC}}, \Sigma_7^{\mathcal{TC}}, \Sigma_1^{\mathcal{TC}}, \Sigma_r^{\mathcal{TC}}, X_p^{\mathcal{TC}}, \emptyset, M^{\mathcal{TC}}, I^{\mathcal{TC}}, E^{\mathcal{TC}})$ together with a partition **Verdicts** of the set of states $S^{\mathcal{TC}} = \mathbf{None} \sqcup \mathbf{Inconc} \sqcup \mathbf{Pass} \sqcup \mathbf{Fail}$ where states outside **None** are called verdict states. We require that $\Sigma_7^{\mathcal{TC}} = \Sigma_7^{\mathcal{A}}$ and $\Sigma_1^{\mathcal{TC}} = \Sigma_1^{\mathcal{A}}$, $I^{\mathcal{TC}}(\ell) = \mathbf{true}$ for all $\ell \in L^{\mathcal{TC}}$, and \mathcal{TC} is input-complete in all **None** states, meaning that it is ready to receive any input from the implementation before reaching a verdict. A test suite is a set of test cases.*

We say that the verdict of an execution $\sigma \in \text{Traces}(\mathcal{TC})$, noted $\text{Verdict}(\sigma, \mathcal{TC})$, is **Pass**, **Fail**, **Inconc** or **None** if \mathcal{TC} after σ is included in the corresponding states set. We note \mathcal{I} fails \mathcal{TC} if some execution σ of $\mathcal{TC} \parallel \mathcal{I}$ leads \mathcal{TC} to a **Fail** state, i.e. when $\text{Traces}_{\mathbf{Fail}}(\mathcal{TC}) \cap \text{Traces}(\mathcal{I}) \neq \emptyset$ ². Notice that this is only a possibility to reach the **Fail** verdict among the infinite set of executions.

Definition 5 (Test case properties) *A test suite \mathcal{TS} for \mathcal{A} is:*

- sound if $\forall \mathcal{I} \in \mathcal{I}(\mathcal{A}), \forall \mathcal{TC} \in \mathcal{TS}, \mathcal{I} \text{ fails } \mathcal{TC} \Rightarrow \neg(\mathcal{I} \text{ tioco } \mathcal{A})$,
- strict if $\forall \mathcal{I} \in \mathcal{I}(\mathcal{A}), \forall \mathcal{TC} \in \mathcal{TS}, \neg(\mathcal{I} \parallel \mathcal{TC} \text{ tioco } \mathcal{A}) \Rightarrow \mathcal{I} \text{ fails } \mathcal{TC}$.

Soundness means that no conformant implementation is rejected by the test suite. This is a crucial property, ensured by our test generation method. In the other direction, strictness means that non-conformance is detected as soon as it occurs, and is ensured by our method when determinization is exact.

3.2 Refinement preserving tioco

We introduce an io-refinement relation between TAIOS, a generalization to non-deterministic TAIOS of the io-refinement between DTAIOS introduced in [DLL⁺10], itself a generalization of alternating simulation [AHKV98]. We prove that io-abstraction (the inverse relation) preserves **tioco**: if \mathcal{I} conforms to \mathcal{A} , it also conforms to any io-abstraction \mathcal{B} of \mathcal{A} . This will ensure that soundness of test cases is preserved by the approximate determinization defined in Section 4.

Definition 6 *Let \mathcal{A} and \mathcal{B} be two TAIOS with same input and output alphabets, we say that \mathcal{A} io-refines \mathcal{B} (or \mathcal{B} io-abstracts \mathcal{A}) and note $\mathcal{A} \preceq \mathcal{B}$ if*

$$\begin{aligned} \forall \sigma \in \text{Traces}(\mathcal{B}), \text{out}(\mathcal{A} \text{ after } \sigma) &\subseteq \text{out}(\mathcal{B} \text{ after } \sigma) \text{ and,} \\ \forall \sigma \in \text{Traces}(\mathcal{A}), \text{in}(\mathcal{B} \text{ after } \sigma) &\subseteq \text{in}(\mathcal{A} \text{ after } \sigma). \end{aligned}$$

It can be proved that \preceq is a preorder relation. Moreover, as the second condition is always satisfied if \mathcal{A} is input-complete, for $\mathcal{I} \in \mathcal{I}(\mathcal{A})$, $\mathcal{I} \text{ tioco } \mathcal{A}$ is equivalent to $\mathcal{I} \preceq \mathcal{A}$. By transitivity of \preceq , it follows that io-refinement preserves conformance:

Proposition 1 *If $\mathcal{A} \preceq \mathcal{B}$ then $\forall \mathcal{I} \in \mathcal{I}(\mathcal{A}) (= \mathcal{I}(\mathcal{B}))$, $\mathcal{I} \text{ tioco } \mathcal{A} \Rightarrow \mathcal{I} \text{ tioco } \mathcal{B}$.*

As a corollary, we get that io-abstraction preserves soundness of test suites:

Corollary 1 *If $\mathcal{A} \preceq \mathcal{B}$ then any sound test suite for \mathcal{B} is also sound for \mathcal{A} .*

In the sequel, this corollary will justify that if an OTAIO \mathcal{B} io-abstracting \mathcal{A} can be obtained by approximate determinization, one can generate sound test cases for \mathcal{B} that are still sound for \mathcal{A} .

²The execution of a test case \mathcal{TC} on an implementation \mathcal{I} is usually modeled by the standard parallel composition $\mathcal{TC} \parallel \mathcal{I}$. Due to space limitations, \parallel is not defined here, but we use its trace properties: $\text{Traces}(\mathcal{I} \parallel \mathcal{TC}) = \text{Traces}(\mathcal{I}) \cap \text{Traces}(\mathcal{TC})$.

4 Approximate determinization preserving tioco

We recently proposed a game approach to determinize or provide a deterministic over-approximation for TAs [BSJK11]. Determinization is exact on all known classes of determinizable TAIOS (*e.g.* event-clock TAs, TAs with integer resets, strongly non-Zeno TAs) if resources are sufficient. Provided a couple of extensions, this method can be adapted to the context of testing for building a deterministic io-abstraction of a given TAIOS. Thanks to Proposition 1, the construction preserves **tioco**, and thus guarantees the soundness of generated test cases.

The approximate determinization uses the classical region construction [AD94]. As for classical timed automata, the regions form a partition of valuations over a given set of clocks which allows to make abstractions in order to decide properties like the reachability of a location. We note $\text{Reg}_{(X,M)}$ the set of regions over clocks X with maximal constant M . A region r' is a *time-successor* of a region r if $\exists v \in r, \exists t \in \mathbb{R}_{\geq 0}, v + t \in r'$. Given X and Y two finite sets of clocks, a *relation* between clocks of X and those of Y is a finite conjunction C of atomic constraints of the form $x - y \sim c$ where $x \in X, y \in Y, \sim \in \{<, =, >\}$ and $c \in \mathbb{N}$. When $c \in [-M', M]$, for $M, M' \in \mathbb{N}$, we denote by $\text{Rel}_{M,M'}(X, Y)$ the set of relations between X and Y .

4.1 A game approach to determinize timed automata

The technique presented in [BSJK11] applies first to TAs, *i.e.* the alphabet only consists of one kind of actions (output actions), and the invariants are all trivial. Given such a TA \mathcal{A} over the set of clocks $X^{\mathcal{A}}$, the goal is to build a deterministic TA \mathcal{B} with $\text{Traces}(\mathcal{A}) = \text{Traces}(\mathcal{B})$ as often as possible, or $\text{Traces}(\mathcal{A}) \subseteq \text{Traces}(\mathcal{B})$. In order to do so, resources of \mathcal{B} (number of clocks k and maximal constant $M^{\mathcal{B}}$) are fixed, and a finite 2-player turn-based safety game $\mathcal{G}_{\mathcal{A},(k,M^{\mathcal{B}})}$ is built. The two players, Spoiler and Determinizator, alternate moves, the objective of player Determinizator being to remain in a set of safe states where intuitively, for sure no over-approximation has been performed. Every strategy for Determinizator yields a deterministic automaton \mathcal{B} with $\text{Traces}(\mathcal{A}) \subseteq \text{Traces}(\mathcal{B})$, and every winning strategy induces a deterministic TA \mathcal{B} equivalent to \mathcal{A} . It is well known that for this kind of games, winning strategies can be chosen positional and computed in linear time in the size of the arena.

Let us now give more details on the definition of the game. Let $X^{\mathcal{B}}$ be a set of clocks of cardinality k . The initial state of the game is a state of Spoiler consisting of the initial location of \mathcal{A} , the simplest relation between $X^{\mathcal{A}}$ and $X^{\mathcal{B}}$: $\forall x \in X^{\mathcal{A}}, \forall y \in X^{\mathcal{B}}, x - y = 0$, a marking \top indicating that no over-approximation was done so far, together with the null region over $X^{\mathcal{B}}$. In each of his states, Spoiler challenges Determinizator by proposing a region $r \in \text{Reg}_{(X^{\mathcal{B}}, M^{\mathcal{B}})}$, and an action $a \in \Sigma$. Determinizator answers by deciding the subset of clocks $Y' \subseteq X^{\mathcal{B}}$ he wishes to reset. The next state of Spoiler contains a region over $X^{\mathcal{B}}$ ($r' = r_{[Y', \leftarrow 0]}$), and a finite set of configurations: triples formed of a location of \mathcal{A} , a relation between clocks in $X^{\mathcal{A}}$ and clocks in $X^{\mathcal{B}}$, and a boolean marking (\top or \perp). A state of Spoiler thus constitutes a states' estimate of \mathcal{A} , and the role of the markings is to indicate whether over-approximations possibly happened. Bad states Determinizator wants to avoid are states where all configurations are marked \perp , *i.e.* configurations where an approximation possibly happened.

A strategy for Determinizator thus assigns to each state of Determinizator a set $Y' \subseteq X^{\mathcal{B}}$ of clocks to be reset. With every strategy for Determinizator Π we associate the TA $\mathcal{B} = \text{Aut}(\Pi)$ obtained by merging a transition of Spoiler with the transition chosen by Determinizator just after. The following theorem links strategies of Determinizator with deterministic over-approximations of the original traces language and enlightens the interest of the game:

Theorem 1 ([BSJK11]) *Let \mathcal{A} be a TA, and $k, M^{\mathcal{B}} \in \mathbb{N}$. For every strategy Π of Determinizator in $\mathcal{G}_{\mathcal{A},(k,M^{\mathcal{B}})}$, $\mathcal{B} = \text{Aut}(\Pi)$ is a deterministic timed automaton over resources $(k, M^{\mathcal{B}})$ and satisfies $\text{Traces}(\mathcal{A}) \subseteq \text{Traces}(\mathcal{B})$. Moreover, if Π is winning, then $\text{Traces}(\mathcal{A}) = \text{Traces}(\mathcal{B})$.*

4.2 Extensions to TAIOS and adaptation to tioco

In the context of model-based testing, the above-mentioned determinization technique must be adapted to TAIOS, as detailed in [BSJK11], and summarized below. First the model of TAIOS is more expressive than TAs and incorporates internal actions and invariants. Second, in order to preserve **tioco**, the goal is to build from a TAIOS \mathcal{A} a DTAIO \mathcal{B} such that $\mathcal{A} \preceq \mathcal{B}$, thus inputs and outputs must be treated differently.

Internal actions: Specifications naturally include internal actions that cannot be observed during test executions, and should thus be removed during determinization. In order to do so, a closure by internal actions is performed for each state during the construction of the game. To this attempt, states of the game have to be extended since internal actions might be enabled only from some time-successor of the region associated with the state. Therefore, each configuration is associated with a proper region which is a time-successor of the initial region of the state. The closure by silent transitions is effectively computed the same way as successors in the original construction when Determinizator does not reset any clock, computations thus terminate for the same reasons. It is well known that timed automata with silent transitions are strictly more expressive than standard timed automata [BGP96]. Therefore, our approximation can be coarse, but it performs as well as possible with its available clock information.

Invariants: Modeling urgency is quite important and using invariants to this aim is classical. Without the ability to express urgency, for instance, any inactive system would conform to all specifications. Ignoring all invariants in the approximation surely yields an io-abstraction: delays (considered as outputs) are over-approximated. In order to be more precise, while preserving \preceq , with each state of the game is associated the most restrictive invariant containing invariants of all the configurations in the state. In the computation of the successors, invariants are treated as guards and their validity is verified at both extremities of the transition. A state whose invariant is strictly over-approximated is not safe.

io-abstraction vs. over-approximation: Rather than over-approximating a given TAIIO \mathcal{A} , we aim here at building a DTAIO \mathcal{B} such that \mathcal{B} io-abstracts \mathcal{A} ($\mathcal{A} \preceq \mathcal{B}$). Successors by output are over-approximated as in the original game, while successors by inputs must be under-approximated. The over-approximated closure by silent transitions is not suitable to under-approximation. Therefore, states of the game are extended to contain both over- and under-approximated closures. Thus, the litigious successors by an input (where possibly an over-approximation would be done), are not built.

All in all, these modifications allow to deal with the full TAIIO model with invariants, silent transitions and inputs/outputs. In particular, the treatment of invariants is consistent with the io-abstraction: delays are considered as outputs, thus over-approximated. Fig.4 represents a part of this game for the TAIIO of Fig.3. The new game then enjoys the following nice property:

Proposition 2 ([BSJK11]³) *Let \mathcal{A} be a TAIIO, and $k, M^B \in \mathbb{N}$. For every strategy Π of Determinizator in $\mathcal{G}_{\mathcal{A},(k,M^B)}$, $\mathcal{B} = \text{Aut}(\Pi)$ is a DTAIO over resources (k, M^B) with $\mathcal{A} \preceq \mathcal{B}$. Moreover, if Π is winning, then $\text{Traces}(\mathcal{A}) = \text{Traces}(\mathcal{B})$.*

In other words, the approximations produced by our method are deterministic io-abstractions of the initial specification, hence our approach preserves **tioco** (Proposition 1) and soundness of test cases (Corollary 1). In comparison, the algorithm proposed in [KT09] is an over-approximation, thus preserves **tioco** only if the specification is input-complete. Moreover it does not preserve urgency.

5 Off-line test case generation

In this section we first define test purposes and then give the principles for off-line test selection with test purposes and properties of generated test cases.

5.1 Test purposes

Test purposes are practical means to select behaviors to be tested, either focusing on usual behaviors, or on suspected errors in implementations. In this work we choose the following definition of test purposes, and discuss some alternatives in the conclusion.

Definition 7 (Test purpose) *For a specification TAIIO \mathcal{A} , a test purpose is a pair $(\mathcal{TP}, \text{Accept})$ where $\mathcal{TP} = (L^{\mathcal{TP}}, \ell_0^{\mathcal{TP}}, \Sigma_?, \Sigma!, \Sigma_\tau, X_p^{\mathcal{TP}}, X_o^{\mathcal{TP}}, M^{\mathcal{TP}}, I^{\mathcal{TP}}, E^{\mathcal{TP}})$ is a complete OTAIO (in particular $I^{\mathcal{TP}}(\ell) = \text{true}$ for any $\ell \in L^{\mathcal{TP}}$) with $X_o^{\mathcal{TP}} = X_p^{\mathcal{A}}$ (\mathcal{TP} observes proper clocks of \mathcal{A}), and $\text{Accept} \subseteq L^{\mathcal{TP}}$ is a subset of trap locations.*

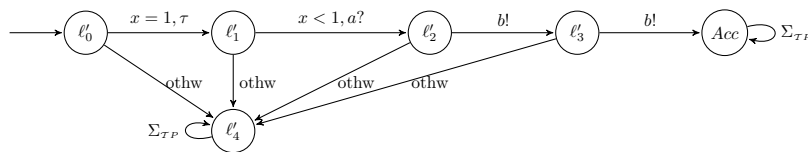
Figure 2: Test purpose \mathcal{TP} .

Fig. 2 represents a test purpose for the specification \mathcal{A} of Fig. 1. It has no proper clock and observes the unique clock x of \mathcal{A} . It accepts sequences where τ occurs at $x = 1$, followed by an input $a?$ at $x < 1$ (thus focusing on the lower branch of \mathcal{A} where x is reset), and two subsequent $b!$'s. The label *othw* (for otherwise) is an abbreviation for the complement of specified transitions.

5.2 Principle of test generation

Given a specification TAIIO \mathcal{A} and a test purpose $(\mathcal{TP}, \text{Accept}^{\mathcal{TP}})$, our aim is to build a test case $(\mathcal{TC}, \text{Verdicts})$ which is sound and, if possible, strict. It should also focus on traces of sequences accepted by \mathcal{TP} . This is formalized by the following property:

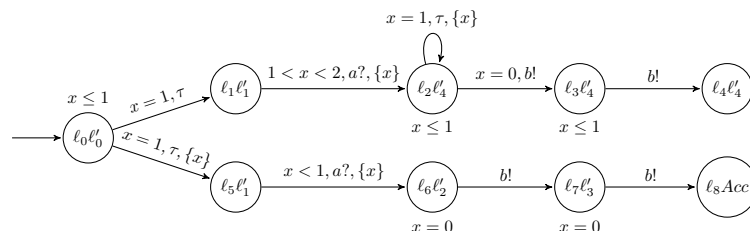
Definition 8 A test suite \mathcal{TS} for \mathcal{A} and \mathcal{TP} is precise if $\forall \mathcal{TC} \in \mathcal{TS}, \forall \sigma \in (\Sigma_{obs}^{\mathcal{A}})^*, \text{Verdict}(\sigma, \mathcal{TC}) = \text{Pass} \iff \sigma \in \text{Traces}(\text{Seq}_{\text{Accept}}^{\mathcal{TP}}(\mathcal{TP}) \cap \text{Seq}(\mathcal{A}))$, meaning that **Pass** verdicts are correctly delivered on traces of sequences of \mathcal{A} accepted by \mathcal{TP} in $\text{Accept}^{\mathcal{TP}}$.

The different steps of test generation are described in the following paragraphs.

Product: we first build the TAIIO $\mathcal{P} = \mathcal{A} \times \mathcal{TP}$ associated with the set of marked locations $\text{Accept}^{\mathcal{P}} = L^{\mathcal{A}} \times \text{Accept}^{\mathcal{TP}}$. Fig. 3 represents this product \mathcal{P} for the specification \mathcal{A} in Fig. 1 and the test purpose \mathcal{TP} in Fig. 2. The effect of the product is to unfold \mathcal{A} and to mark those sequences of \mathcal{A} accepted by \mathcal{TP} in locations $\text{Accept}^{\mathcal{TP}}$. \mathcal{TP} is complete, thus $\text{Seq}(\mathcal{P}) = \text{Seq}(\mathcal{A} \uparrow^{X_p^{\mathcal{TP}}, X_o^{\mathcal{TP}}})$ (sequences of the product are sequences of \mathcal{A} lifted to $X^{\mathcal{TP}}$), and then $\text{Traces}(\mathcal{P}) = \text{Traces}(\mathcal{A})$, which implies that \mathcal{P} and \mathcal{A} define the same sets of conformant implementations.

We also have $\text{Seq}_{\text{Accept}^{\mathcal{P}}}(\mathcal{P}) = \text{Seq}(\mathcal{A} \uparrow^{X_p^{\mathcal{TP}}, X_o^{\mathcal{TP}}}) \cap \text{Seq}_{\text{Accept}^{\mathcal{TP}}}(\mathcal{TP})$ which induces $\text{Traces}_{\text{Accept}^{\mathcal{P}}}(\mathcal{P}) = \text{Traces}(\text{Seq}(\mathcal{A}) \cap \text{Seq}_{\text{Accept}^{\mathcal{TP}}}(\mathcal{TP}))$.

Let $\text{ATraces}(\mathcal{A}, \mathcal{TP}) = \text{Traces}_{\text{Accept}^{\mathcal{P}}}(\mathcal{P})$ and $\text{RTraces}(\mathcal{A}, \mathcal{TP}) = \text{Traces}(\mathcal{A}) \setminus \text{pref}(\text{ATraces}(\mathcal{A}, \mathcal{TP}))$ where, for a set of traces T , $\text{pref}(T)$ denotes the set of prefixes of traces in T . The principle is to select traces in $\text{ATraces}(\mathcal{A}, \mathcal{TP})$ and try to avoid or at least detect those in $\text{RTraces}(\mathcal{A}, \mathcal{TP})$ as these traces cannot be prefixes of traces of sequences satisfying the test purpose.

Figure 3: Product $\mathcal{P} = \mathcal{A} \times \mathcal{TP}$.

Approximate determinization of \mathcal{P} into \mathcal{DP} : If \mathcal{P} is already deterministic, we simply take $\mathcal{DP} = \mathcal{P}$. Otherwise, with the approximate determinization of Section 4, we can build a deterministic io-abstraction \mathcal{DP} of \mathcal{P} with resources $(k, M^{\mathcal{DP}})$ fixed by the user, thus $\mathcal{P} \leq \mathcal{DP}$. \mathcal{DP} is equipped with the set of marked locations $\text{Accept}^{\mathcal{DP}}$ consisting of locations in $L^{\mathcal{DP}}$ containing some configuration whose location is in $\text{Accept}^{\mathcal{P}}$. If the determinization is exact, we get $\text{Traces}(\mathcal{DP}) = \text{Traces}(\mathcal{P})$ and $\text{Traces}_{\text{Accept}^{\mathcal{DP}}}(\mathcal{DP}) =$

$\text{ATraces}(\mathcal{A}, \mathcal{TP})$. Fig. 4 partially represents the game $\mathcal{G}_{\mathcal{P},(1,2)}$ for the TAIIO \mathcal{P} of Fig. 3 where, for readability reasons, some behaviors not co-reachable from $\text{Accept}^{\mathcal{DP}}$ are omitted. \mathcal{DP} is simply obtained from $\mathcal{G}_{\mathcal{P},(1,2)}$ by merging transitions of Spoiler and Determinizator.

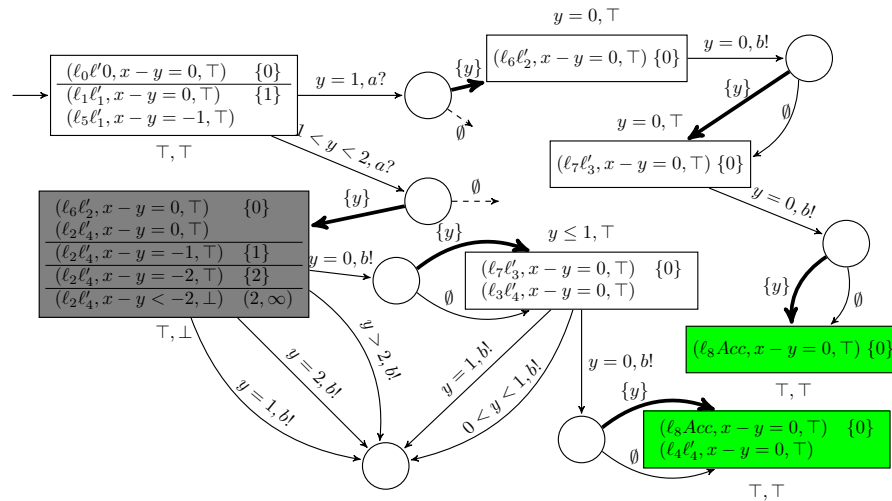


Figure 4: Game $\mathcal{G}_{\mathcal{P},(1,2)}$.

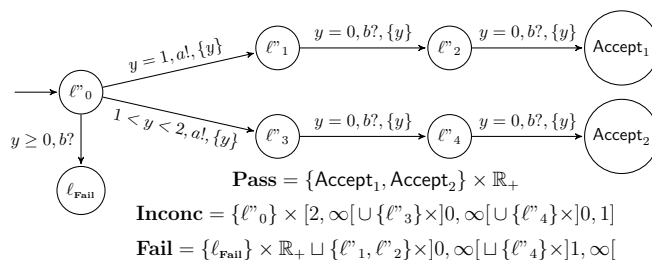
Generating \mathcal{TC} from \mathcal{DP} : The next step consists in building $(\mathcal{TC}, \text{Verdicts})$ from \mathcal{DP} , using an analysis of the co-reachability to locations $\text{Accept}^{\mathcal{DP}}$ in \mathcal{DP} .

The test case built from $\mathcal{DP} = (L^{\mathcal{DP}}, \ell_0^{\mathcal{DP}}, \Sigma_?^{\mathcal{DP}}, \Sigma_I^{\mathcal{DP}}, X_p^{\mathcal{DP}}, \emptyset, M^{\mathcal{DP}}, I^{\mathcal{DP}}, E^{\mathcal{DP}})$ and $\text{Accept}^{\mathcal{DP}}$ is the TAIIO $\mathcal{TC} = (L^{\mathcal{TC}}, \ell_0^{\mathcal{TC}}, \Sigma_?^{\mathcal{TC}}, \Sigma_I^{\mathcal{TC}}, X_p^{\mathcal{TC}}, \emptyset, M^{\mathcal{TC}}, I^{\mathcal{TC}}, E^{\mathcal{TC}})$ such that $L^{\mathcal{TC}} = L^{\mathcal{DP}} \sqcup \{\ell_{\text{Fail}}\}$ where ℓ_{Fail} is a new location; $\ell_0^{\mathcal{TC}} = \ell_0^{\mathcal{DP}}$; $\Sigma_?^{\mathcal{TC}} = \Sigma_I^{\mathcal{DP}} = \Sigma_I^{\mathcal{A}}$ and $\Sigma_I^{\mathcal{TC}} = \Sigma_?^{\mathcal{DP}} = \Sigma_?^{\mathcal{A}}$, *i.e.* input/output alphabets are mirrored in order to reflect the opposite role of actions in the synchronization of \mathcal{TC} and \mathcal{I} ; $X_p^{\mathcal{TC}} = X_p^{\mathcal{DP}}$ and $X_o^{\mathcal{TC}} = \emptyset$; $M^{\mathcal{TC}} = M^{\mathcal{DP}}$; **Verdicts** is the partition of $S^{\mathcal{TC}}$ with **Pass** = $\bigcup_{\ell \in \text{Accept}^{\mathcal{DP}}} \{\ell\} \times I^{\mathcal{DP}}(\ell)$, **None** = $\text{coreach}(\mathcal{DP}, \text{Pass}) \setminus \text{Pass}$, **Inconc** = $S^{\mathcal{DP}} \setminus \text{coreach}(\mathcal{DP}, \text{Pass})$, and **Fail** = $\{\ell_{\text{Fail}}\} \times \mathbb{R}_+^{X^{\mathcal{TC}}} \sqcup \{(\ell, \neg I^{\mathcal{DP}}(\ell)) \mid \ell \in L^{\mathcal{DP}}\}$; $I^{\mathcal{TC}}(\ell) = \text{true}$ for any $\ell \in L^{\mathcal{TC}}$; $E^{\mathcal{TC}} = E_I^{\mathcal{DP}} \sqcup E_{\ell_{\text{Fail}}}$ where $E_I^{\mathcal{DP}} = \{(\ell, g \wedge I^{\mathcal{DP}}(\ell), a, X, \ell') \mid (\ell, g, a, X, \ell') \in E^{\mathcal{DP}}\}$ and $E_{\ell_{\text{Fail}}} = \{(\ell, \bar{g}, a, X_p^{\mathcal{TC}}, \ell_{\text{Fail}}) \mid \ell \in L^{\mathcal{DP}}, a \in \Sigma_I^{\mathcal{DP}}, \bar{g} = \neg \bigvee_{(\ell, g, a, X, \ell') \in E^{\mathcal{DP}}} g\}$.

The important points to understand in the construction of \mathcal{TC} are the completion to **Fail** and the computation of **Inconc**. For the completion, the idea is to detect unspecified outputs and delays of \mathcal{DP} . Outputs of \mathcal{DP} being inputs of \mathcal{TC} , in any location ℓ , for each input $a \in \Sigma_?^{\mathcal{TC}} = \Sigma_I^{\mathcal{DP}}$, a transition leading to ℓ_{Fail} is added, labeled with a , and whose guard is the negation of the disjunction of all guards of transitions labeled by a and leaving ℓ (thus **true** if no a -action leaves ℓ). Authorized delays in \mathcal{DP} being defined by invariants, all states in $(\ell, \neg I^{\mathcal{DP}}(\ell))$, $\ell \in L^{\mathcal{DP}}$, *i.e.* states where the invariant runs out, are put into **Fail**. Moreover, in each location ℓ , the invariant $I^{\mathcal{DP}}(\ell)$ in \mathcal{DP} is removed and shifted to guards of all transitions leaving ℓ in \mathcal{TC} .

The computation of **Inconc** is based on an analysis of the co-reachability to **Pass**. **Inconc** contains all states not co-reachable from locations in **Pass**. Notice that $\text{coreach}(\mathcal{DP}, \text{Pass})$, and thus **Inconc**, can be computed symbolically in the region graph of \mathcal{DP} . Fig.5 represents the test case obtained from \mathcal{A} and \mathcal{TP} .

Test selection: So far, the construction of \mathcal{TC} determines **Verdicts**, but does not perform any selection of behaviors. A last step consists in trying to control the behavior of \mathcal{TC} in order to avoid **Inconc** states (thus stay in $\text{pref}(\text{ATraces}(\mathcal{A}, \mathcal{TP}))$), or produce an **Inconc** verdict when this is impossible. To this aim, guards of transitions are refined in two complementary ways. First, transitions leaving a verdict state are useless, thus for each transition, the guard is intersected with the set of valuations associated with **None** in the source location. Second, transitions arriving in **Inconc** states and carrying inputs are also useless, thus for any transition labeled by an input, the guard is intersected with the set of valuations

Figure 5: Test case \mathcal{TC}

associated with $\text{coreach}(\mathcal{DP}, \mathbf{Pass})$ in the target location. For example in \mathcal{TC} (Fig. 5), the bottom-left state of the game in Fig. 4 has been removed.

After these steps, generated test cases exhibit the following properties:

Theorem 2 *Any test case \mathcal{TC} built by the procedure is sound for \mathcal{A} . If \mathcal{DP} is an exact approximation of \mathcal{P} , \mathcal{TC} is also strict and precise for \mathcal{A} and \mathcal{TP} .*

The proof is given in the appendix. Soundness comes from the construction of $E_{\mathbf{Fail}}$ in \mathcal{TC} and preservation of soundness by the approximate determinization \mathcal{DP} of \mathcal{P} given by Corollary 1. When \mathcal{DP} is an exact determinization of \mathcal{P} , $\text{Traces}(\mathcal{DP}) = \text{Traces}(\mathcal{P}) = \text{Traces}(\mathcal{A})$. Strictness then comes from the fact that \mathcal{DP} and \mathcal{A} have the same non-conformant traces and from the definition of $E_{\mathbf{Fail}}$ in \mathcal{TC} . Precision comes from $\text{Traces}_{\text{Accept}^{\mathcal{DP}}}(\mathcal{DP}) = \text{ATraces}(\mathcal{A}, \mathcal{TP})$ and from the definition of \mathbf{Pass} .

When \mathcal{DP} is not exact however, there is a risk that some behaviors allowed in \mathcal{DP} are not in \mathcal{P} , thus some non-conformant behaviors are not detected, even if they are executed by \mathcal{TC} . Similarly, some \mathbf{Pass} verdicts may be produced for non-accepted or non-conformant behaviors.

Test execution After test selection, it remains to execute test cases on a real implementation. As the test case is a TAIIO, a number of decisions still need to be made at each node of the test case: (1) whether to wait for a certain delay, to receive an input or emit an output (2) which output to send, in case there is a choice. Some of these choices can be made either randomly, or according to user-defined strategies, for example by applying a technique similar to the control approach of [DLLN09] whose goal is to avoid $\text{RTraces}(\mathcal{A}, \mathcal{TP})$.

6 Conclusion

In this paper, we presented a complete formalization and operations for the automatic off-line generation of test cases from non-deterministic timed automata with inputs and outputs (TAIOs). The model of TAIIOs is general enough to take into account non-determinism, partial observation and urgency. One main contribution is the ability to tackle any TAIIO, thanks to an original approximate determinization procedure. Another main contribution is the selection of test cases with expressive test purposes described as OTAIIOs having the ability to precisely describe behaviors to be tested based on clocks and actions of the specification as well as proper clocks. Test cases are generated as TAIIOs using a symbolic co-reachability analysis of the observable behaviors of the specification guided by the test purpose.

Related work and discussion: As mentioned in the introduction, off-line test selection is in general limited to deterministic or determinizable timed automata, except in [KT09] which relies on an approximate determinization. Compared to this work, our approximate determinization is more precise (it is exact in more cases) and preserves urgency in test cases as much as possible.

In several other works [KCL98, END03], test purposes are used for test case selection from TAIIOs. In all these works, test purposes only have proper clocks, thus cannot observe clocks of the specification. The advantage of our definition is its generality and a fine tuning of selection. One could argue that the cost of producing a test suite can be heavy, as for each test purpose, the whole sequence of operations,

including determinization, must be done. In order to avoid this, an alternative would be to define test purposes recognizing timed traces and perform selection on the approximate determinization \mathcal{B} of \mathcal{A} . But then, the test purpose should not use \mathcal{A} 's clocks as these are lost by determinization. Then, test purposes are either defined after determinization and observe \mathcal{B} 's clocks, or their expressive power is further restricted by using only proper clocks in order not to depend on \mathcal{B} .

Concerning test selection, in [DLLN09], the authors propose a game approach which effect can be understood as a way to completely avoid $\text{RTraces}(\mathcal{A}, \mathcal{TP})$, with the possible risk to miss some or even all traces in $\text{pref}(\text{ATraces}(\mathcal{A}, \mathcal{TP}))$. Our selection, which allows to lose the game and produce an **Inconc** verdict when this happens, is both more liberal and closer to usual practice.

It should be noticed that selection by test purposes can be used for test selection with respect to coverage criteria. Those coverage criteria define a set of elements (generally syntactic ones) to be covered (e.g. locations, transitions, branches, etc). Each element can then be translated into a test purpose, the produced test suite covering the given criteria.

References

- [AD94] R. Alur and D. L. Dill. A theory of timed automata. *Theoretical Computer Science*, 126(2):183–235, 1994.
- [AHKV98] R. Alur, T. A. Henzinger, O. Kupferman, and M. Y. Vardi. Alternating refinement relations. In *9th International Conference on Concurrency Theory (CONCUR '98)*, volume 1466 of *LNCS*, pages 163–178, 1998.
- [BB05] L. B. Briones and E. Brinksma. A test generation framework for quiescent real-time systems. In *FATES'2004*, volume 3395 of *LNCS*, pages 64–78, 2005.
- [BGP96] B. Bérard, P. Gastin, and A. Petit. On the power of non-observable actions in timed automata. In *13th Annual Symposium on Theoretical Aspects of Computer Science (STACS'96)*, volume 1046 of *LNCS*, pages 255–268, 1996.
- [BJSK11] N. Bertrand, T. Jéron, A. Stainer, and M. Krichen. Off-line test selection with test purposes for non-deterministic timed automata. In *Tools and Algorithms for the Construction and Analysis of Systems (TACAS'2011)*, march 2011.
- [BSJK11] N. Bertrand, A. Stainer, T. Jéron, and M. Krichen. A game approach to determinize timed automata. In *FOSSACS 2011*, march 2011. Extended version as INRIA report, <http://hal.inria.fr/inria-00524830>.
- [DLL⁺10] A. David, K. G. Larsen, A. Legay, U. Nyman, and A. Wasowski. Timed I/O automata: a complete specification theory for real-time systems. In *13th ACM international conference on Hybrid systems: computation and control (HSCC '10)*, pages 91–100, 2010.
- [DLLN09] A. David, K. G. Larsen, S. Li, and B. Nielsen. Timed testing under partial observability. In *International Conference on Software Testing Verification and Validation (ICST'09)*, pages 61–70, 2009.
- [END03] A. En-Nouaary and R. Dssouli. A guided method for testing timed input output automata. In *Testing of Communicating Systems (TestCom'03)*, volume 2644 of *LNCS*, pages 211–225, 2003.
- [Fin06] O. Finkel. Undecidable problems about timed automata. In *4th International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS'06)*, volume 4202 of *LNCS*, pages 187–199, 2006.
- [JJ04] C. Jard and T. Jéron. TGV: theory, principles and algorithms, a tool for the automatic synthesis of conformance test cases for non-deterministic reactive systems. *Software Tools for Technology Transfer (STTT)*, 6, October 2004.
- [KCL98] O. Koné, R. Castanet, and P. Laurencot. On the fly test generation for real time protocols. In *International Conference on Computer Communications & Networks (IC3N'98)*, 1998.
- [KJM04] A. Khoumsi, T. Jéron, and H. Marchand. Test cases generation for nondeterministic real-time systems. In *Formal Approaches to Software Testing (FATES'03)*, volume 2931 of *LNCS*, pages 131–145, 2004.
- [KT09] M. Krichen and S. Tripakis. Conformance testing for real-time systems. *Formal Methods in System Design*, 34(3):238–304, 2009.
- [NS03] B. Nielsen and A. Skou. Automated test generation from timed automata. *STTT*, 5:59–77, 2003.
- [ST08] J. Schmaltz and J. Tretmans. On conformance testing for timed systems. In *6th International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS'08)*, volume 5215 of *LNCS*, pages 250–264, 2008.

- [Tre96] J. Tretmans. Test generation with inputs, outputs and repetitive quiescence. *Software - Concepts and Tools*, 3:103–120, 1996.
- [Tri06] S. Tripakis. Folk theorems on the determinization and minimization of timed automata. *Information Processing Letters*, 99(6):222–226, 2006.

Appendix

Proof of Proposition 1 and Corollary 1

We start by proving that \preceq is a preorder. It is trivially reflexive and we prove that it is transitive.

Suppose that $\mathcal{A} \preceq \mathcal{B}$ and $\mathcal{B} \preceq \mathcal{C}$. By definition of \preceq we have:

$$\forall \sigma \in \text{Traces}(\mathcal{B}), \text{out}(\mathcal{A} \text{ after } \sigma) \subseteq \text{out}(\mathcal{B} \text{ after } \sigma) \quad (1)$$

$$\forall \sigma \in \text{Traces}(\mathcal{A}), \text{in}(\mathcal{B} \text{ after } \sigma) \subseteq \text{in}(\mathcal{A} \text{ after } \sigma) \quad (2) \quad \text{and}$$

$$\forall \sigma \in \text{Traces}(\mathcal{C}), \text{out}(\mathcal{B} \text{ after } \sigma) \subseteq \text{out}(\mathcal{C} \text{ after } \sigma) \quad (3)$$

$$\forall \sigma \in \text{Traces}(\mathcal{B}), \text{in}(\mathcal{C} \text{ after } \sigma) \subseteq \text{in}(\mathcal{B} \text{ after } \sigma) \quad (4)$$

We want to prove that $\mathcal{A} \preceq \mathcal{C}$ thus

$$\forall \sigma \in \text{Traces}(\mathcal{C}), \text{out}(\mathcal{A} \text{ after } \sigma) \subseteq \text{out}(\mathcal{C} \text{ after } \sigma) \quad (5)$$

$$\forall \sigma \in \text{Traces}(\mathcal{A}), \text{in}(\mathcal{C} \text{ after } \sigma) \subseteq \text{in}(\mathcal{A} \text{ after } \sigma) \quad (6)$$

In order to prove (5), let $\sigma \in \text{Traces}(\mathcal{C})$, and examine the two cases:

- If $\sigma \in \text{Traces}(\mathcal{B}) \cap \text{Traces}(\mathcal{C})$ then by (1) and (3) we get $\text{out}(\mathcal{A} \text{ after } \sigma) \subseteq \text{out}(\mathcal{B} \text{ after } \sigma)$ and $\text{out}(\mathcal{B} \text{ after } \sigma) \subseteq \text{out}(\mathcal{C} \text{ after } \sigma)$ thus $\text{out}(\mathcal{A} \text{ after } \sigma) \subseteq \text{out}(\mathcal{C} \text{ after } \sigma)$ and we are done.
- If $\sigma \in \text{Traces}(\mathcal{C}) \setminus \text{Traces}(\mathcal{B})$, there exists σ', σ'' and $a \in \Sigma_{obs}$ such that $\sigma = \sigma'.a.\sigma''$ with $\sigma' \in \text{Traces}(\mathcal{B})$ and $\sigma'.a \in \text{Traces}(\mathcal{C}) \setminus \text{Traces}(\mathcal{B})$. As $\mathcal{B} \preceq \mathcal{C}$, by (4) we get that $a \in \Sigma_I \sqcup \mathbb{R}_{\geq 0}$. But as $\mathcal{A} \preceq \mathcal{B}$, and $\sigma' \in \text{Traces}(\mathcal{B})$, condition (1) induces $\text{out}(\mathcal{A} \text{ after } \sigma') \subseteq \text{out}(\mathcal{B} \text{ after } \sigma')$, and then $\sigma'.a \in \text{Traces}(\mathcal{C}) \setminus \text{Traces}(\mathcal{A})$. Thus $\text{out}(\mathcal{A} \text{ after } \sigma'.a) = \emptyset$ and we conclude by $\text{out}(\mathcal{A} \text{ after } \sigma) = \emptyset \subseteq \text{out}(\mathcal{C} \text{ after } \sigma)$.

The proof of (6) is almost symmetric.

We now prove Proposition 1:

Proposition 1. If $\mathcal{A} \preceq \mathcal{B}$ then $\forall \mathcal{I} \in \mathcal{I}(\mathcal{A}) = \mathcal{I}(\mathcal{B}), \mathcal{I} \text{ tioco } \mathcal{A} \Rightarrow \mathcal{I} \text{ tioco } \mathcal{B}$.

The proof is then a direct consequence of the transitivity of \preceq . In fact when \mathcal{I} is input complete, $\forall \sigma \in \text{Traces}(\mathcal{I}), \text{in}(\mathcal{A} \text{ after } \sigma) \subseteq \text{in}(\mathcal{I} \text{ after } \sigma) = \Sigma_I$ trivially holds. Thus $\mathcal{I} \text{ tioco } \mathcal{A}$ (which is defined by $\forall \sigma \in \text{Traces}(\mathcal{A}), \text{out}(\mathcal{I} \text{ after } \sigma) \subseteq \text{out}(\mathcal{A} \text{ after } \sigma)$) is equivalent to $\mathcal{I} \preceq \mathcal{A}$. Now suppose $\mathcal{A} \preceq \mathcal{B}$ and $\mathcal{I} \text{ tioco } \mathcal{A}$ then the transitivity of \preceq gives $\mathcal{I} \text{ tioco } \mathcal{B}$.

Remark: unfortunately, the converse of Proposition 1 is in general false. This comes from the fact that when a specification does not specify an input after a trace, for conformance this is equivalent to specifying this input and then accept the universal language on Σ_{obs} . A counter-example of the converse of Proposition 1 then consists in taking \mathcal{A} which receives no input, and \mathcal{B} receiving an input a and then accepting Σ_{obs} . They have same sets of conformant implementations, but $\neg(\mathcal{A} \preceq \mathcal{B})$ as $\text{in}(\mathcal{B} \text{ after } \epsilon) = a \notin \text{in}(\mathcal{A} \text{ after } \epsilon) = \emptyset$.

We now prove Corollary 1:

Corollary 1. If $\mathcal{A} \preceq \mathcal{B}$ then any sound test suite for \mathcal{B} is also sound for \mathcal{A} .

Let \mathcal{TS} be a sound test suite for \mathcal{B} . By definition $\forall \mathcal{I} \in \mathcal{I}(\mathcal{B}), \forall \mathcal{TC} \in \mathcal{TS}, \mathcal{I} \text{ fails } \mathcal{TC} \Rightarrow \neg(\mathcal{I} \text{ tioco } \mathcal{B})$. As we have $\mathcal{A} \preceq \mathcal{B}$, by Proposition 1, we have $\neg(\mathcal{I} \text{ tioco } \mathcal{B}) \Rightarrow \neg(\mathcal{I} \text{ tioco } \mathcal{A})$ which implies $\forall \mathcal{I} \in \mathcal{I}(\mathcal{B}), \forall \mathcal{TC} \in \mathcal{TS}, \mathcal{I} \text{ fails } \mathcal{TC} \Rightarrow \neg(\mathcal{I} \text{ tioco } \mathcal{A})$. Thus \mathcal{TS} is sound for \mathcal{A} .

Proof of Theorem 2

Theorem 2. Any test case \mathcal{TC} built by the procedure is sound for \mathcal{A} . If \mathcal{DP} is an exact approximation of \mathcal{P} , \mathcal{TC} is also strict and precise for \mathcal{A} and \mathcal{TP} .

Soundness: To prove soundness, we need to show that for any $\mathcal{I} \in \mathcal{I}(\mathcal{A})$, $\mathcal{I} \text{ fails } \mathcal{TC}$ implies $\neg(\mathcal{I} \text{ tioco } \mathcal{A})$. Assume that $\mathcal{I} \text{ fails } \mathcal{TC}$, then there exists a trace $\sigma \in \text{Traces}(\mathcal{I}) \cap \text{Traces}(\mathcal{TC})$ leading to **Fail**. By the construction of the set E_{Fail} in \mathcal{TC} , either $\sigma = \sigma'.a.0$ where $\sigma' \in \text{Traces}(\mathcal{DP})$, $a \in \Sigma_!^{\mathcal{P}\mathcal{P}}$ is unspecified in \mathcal{DP} after σ' , or $\sigma = \sigma'.\delta$ where $\delta > 0$ is unspecified in \mathcal{DP} after σ . In both cases, this means that $\neg(\mathcal{I} \text{ tioco } \mathcal{DP})$, thus \mathcal{TC} is sound for \mathcal{DP} . Now, as \mathcal{DP} is an io-abstraction of $AxTP$ ($\mathcal{P} \preceq \mathcal{DP}$), by Corollary 1 this implies \mathcal{TC} is sound for \mathcal{P} . Finally, we have $\text{Traces}(\mathcal{P}) = \text{Traces}(\mathcal{A})$, which trivially implies $\mathcal{A} \preceq \mathcal{P}$, and then \mathcal{TC} is also sound for \mathcal{A} .

Strictness: For strictness, we have to prove that for any $\mathcal{I} \in \mathcal{I}(\mathcal{A})$, $\neg(\mathcal{I} \parallel \mathcal{TC} \text{ tioco } \mathcal{A})$ implies that $\mathcal{I} \text{ fails } \mathcal{TC}$. Suppose that $\neg(\mathcal{I} \parallel \mathcal{TC} \text{ tioco } \mathcal{A})$, then there exists $\sigma \in \text{Traces}(\mathcal{A})$ and $a \in \text{out}(\mathcal{I} \parallel \mathcal{TC} \text{ after } \sigma)$ such that $a \notin \text{out}(\mathcal{A} \text{ after } \sigma)$. If \mathcal{DP} is an exact approximation of \mathcal{P} , then $\text{Traces}(\mathcal{DP}) = \text{Traces}(\mathcal{P}) = \text{Traces}(\mathcal{A})$, thus $\sigma \in \text{Traces}(\mathcal{DP})$ and $a \notin \text{out}(\mathcal{DP} \text{ after } \sigma)$. By construction of \mathcal{TC} , it follows that $\sigma.a \in \text{Traces}_{\text{Fail}}(\mathcal{TC})$ which, together with $\sigma.a \in \text{Traces}(\mathcal{I})$, implies that $\mathcal{I} \text{ fails } \mathcal{TC}$. Thus \mathcal{TC} is strict.

Precision: To prove precision, we have to show that for any $\sigma \in (\Sigma_{obs}^{\mathcal{A}})^*$, $\text{Verdict}(\sigma, \mathcal{TC}) = \text{Pass} \iff \sigma \in \text{Traces}(\text{Seq}_{\text{Accept}}^{\mathcal{TP}}(\mathcal{TP}) \cap \text{Seq}(\mathcal{A}))$. The definition of $\text{Pass} = \bigcup_{\ell \in \text{Accept}^{\mathcal{DP}}} \ell \times I^{\mathcal{DP}}(\ell)$ in \mathcal{TC} implies that a **Pass** verdict is produced for σ exactly when $\sigma \in \text{Traces}_{\text{Accept}^{\mathcal{DP}}}(\mathcal{DP})$ which equals $\text{ATraces}(\mathcal{A}, \mathcal{TP})$ and thus $\text{Traces}(\text{Seq}_{\text{Accept}}^{\mathcal{TP}}(\mathcal{TP}) \cap \text{Seq}(\mathcal{A}))$ when \mathcal{DP} is exact.



Centre de recherche INRIA Rennes – Bretagne Atlantique
IRISA, Campus universitaire de Beaulieu - 35042 Rennes Cedex (France)

Centre de recherche INRIA Bordeaux – Sud Ouest : Domaine Universitaire - 351, cours de la Libération - 33405 Talence Cedex
Centre de recherche INRIA Grenoble – Rhône-Alpes : 655, avenue de l'Europe - 38334 Montbonnot Saint-Ismier
Centre de recherche INRIA Lille – Nord Europe : Parc Scientifique de la Haute Borne - 40, avenue Halley - 59650 Villeneuve d'Ascq
Centre de recherche INRIA Nancy – Grand Est : LORIA, Technopôle de Nancy-Brabois - Campus scientifique
615, rue du Jardin Botanique - BP 101 - 54602 Villers-lès-Nancy Cedex
Centre de recherche INRIA Paris – Rocquencourt : Domaine de Voluceau - Rocquencourt - BP 105 - 78153 Le Chesnay Cedex
Centre de recherche INRIA Saclay – Île-de-France : Parc Orsay Université - ZAC des Vignes : 4, rue Jacques Monod - 91893 Orsay Cedex
Centre de recherche INRIA Sophia Antipolis – Méditerranée : 2004, route des Lucioles - BP 93 - 06902 Sophia Antipolis Cedex

Éditeur
INRIA - Domaine de Voluceau - Rocquencourt, BP 105 - 78153 Le Chesnay Cedex (France)
<http://www.inria.fr>
ISSN 0249-6399